Nch 60V 47A Power MOSFET

V _{DSS}	60V
R _{DS(on)} (Max.)	9.7mΩ
I _D	±47A
P _D	31W

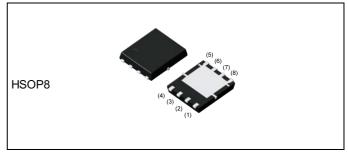
Features

- 1) Low on resistance
- 2) High power package (HSOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

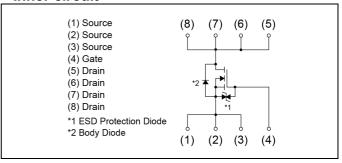
Application

Switching

Outline



●Inner circuit



Packaging specifications

	jiiig opcomounciio	
	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	RS1L145GN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	60	V	
Continuous drain surrent	T _c = 25°C	I _D *1	±47	Α
Continuous drain current	T _a = 25°C	I _D	±14.5	Α
Pulsed drain current	I _{DP} *2	±58	А	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single pulse	I _{AS} *3	14.5	Α	
Avalanche energy, single pulse	E _{AS} *3	31	mJ	
		P _D *1	31	W
Power dissipation		P _D *4	3.0	W
Junction temperature	Tj	150	°C	
Operating junction and storage tem	T _{stg}	-55 to +150	°C	

●Thermal resistance

Doromotor	Curah al	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	4.0	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	41.7	°C/W

● Electrical characteristics (T_a = 25°C)

Daramatar	Cymah al	Conditions	Values			Linit	
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		60	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	-	-	10	μА	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 200 \mu A$	1.3	-	2.7	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = 1mA referenced to 25°C	-	-5.6	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 14.5A	-	6.7	9.7	O	
on - state resistance	R _{DS(on)} *5	V _{GS} = 4.5V, I _D = 14.5A	-	9.6	14.1	mΩ	
Gate resistance	R_G	f=1MHz, open drain	-	2.2		Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 14.5A	16.7	-	-	S	

^{*1}Tc=25°C, Limited only by maximum temperature allowed.

^{*2} Pw≦10µs , Duty cycle≦1%

^{*3} L \simeq 0.2mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Dorameter	Cumb of	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	1880	-	
Output capacitance	C _{oss}	V _{DS} = 30V	-	365	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	95	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	1	20	1	
Rise time	t _r *5	I _D = 7.25A	-	12	-	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 4.14\Omega$	-	50	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	17	-	

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate charge	O *5		V _{GS} = 10V	-	37.0	-	
Total gate charge	Q_g^{*5}	V _{DD} ≃ 30V		-	19.5	-	~ C
Gate - Source charge	Q _{gs} *5	I _D = 14.5A	V _{GS} = 4.5V	-	7.9	-	nC
Gate - Drain charge	Q _{gd} *5			-	7.1	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Daramatar	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T _a = 25°C	-	-	2.5	Α
Pulse forward current	I _{SP} *2	1 _a - 25 C	-	-	58	Α
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_S = 2.5A$	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 14.5A, V _{GS} =0V	-	35	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	40	-	nC

Fig.1 Power Dissipation Derating Curve

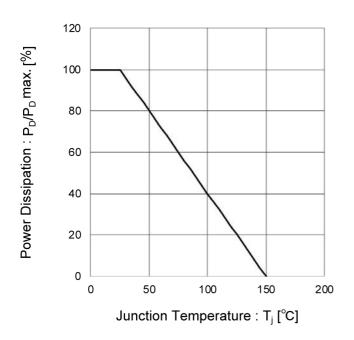
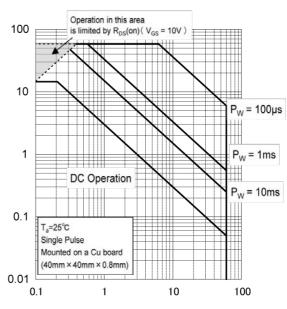


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

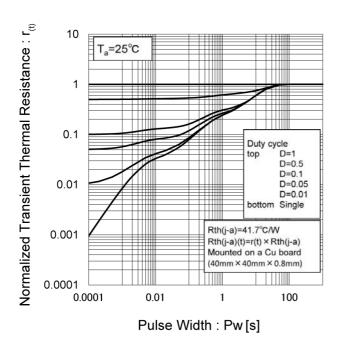
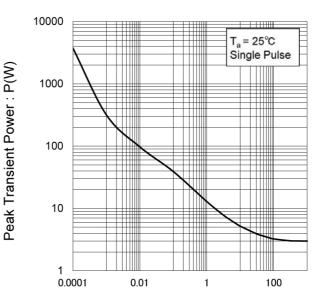


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw[s]

Drain Current : I_D [A]

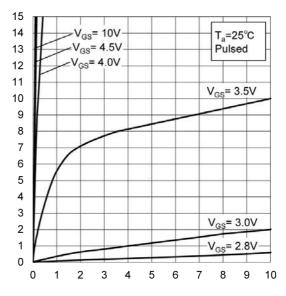
Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

15 14 T_a=25°C Pulsed 13 V_{GS}= 10V 12 V_{GS}= 4.5V 11 V_{GS}= 4.0V 10 9 8 7 6 V_{GS}= 3.5V 5 4 3 $_{c}V_{GS} = 3.0V$ 2 V_{GS}= 2.8V 0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 0

Drain - Source Voltage : V_{DS} [V]

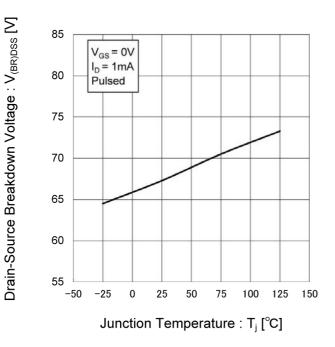
Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.8 Typical Transfer Characteristics

Fig.7 Breakdown Voltage vs. **Junction Temperature**



Drain Current : I_D [A]

Drain Current : I_D [A]

100 V_{DS}=V_{GS} Pulsed 10 T.= 125°C _T_a= 75°C 0.1 T_a= 25°C ST_a= - 25°C 0.01 0.001 0 0.5 2 1.5 3.5 4.5

Gate - Source Voltage : V_{GS} [V]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

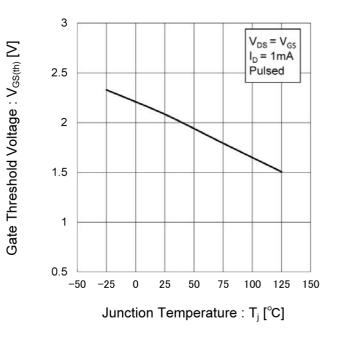


Fig.10 Forward Transfer Admittance vs.
Drain Current

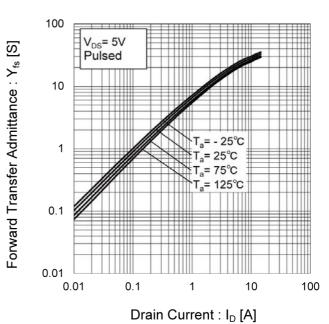


Fig.11 Drain Current Derating Curve

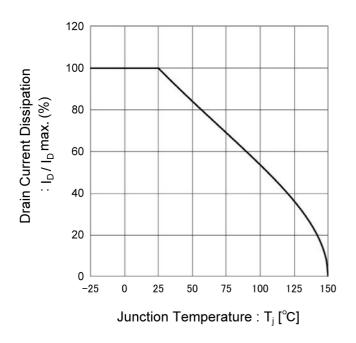


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

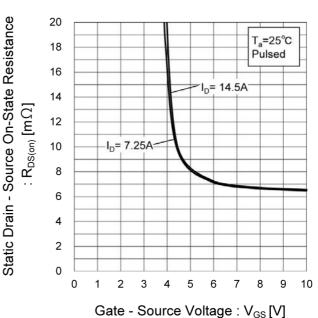


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

Static Drain - Source On-State Resistance 15 14 V_{GS} = 10V 13 Pulsed 12 11 10 $R_{DS(on)}$ [m Ω] 9 8 7 $I_D = 14.5A$ 6 5 4 3 2 1 0 -50 -25 25 50 75 100 125 Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

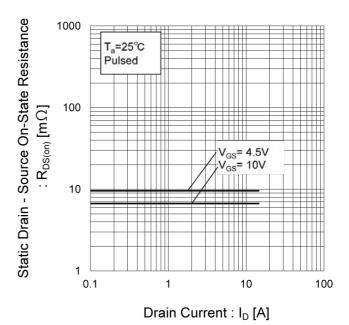


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

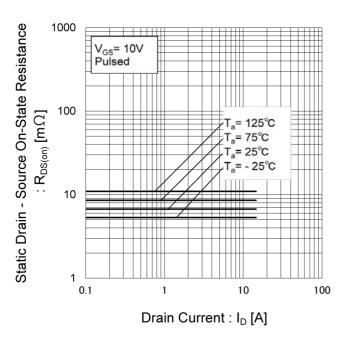


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

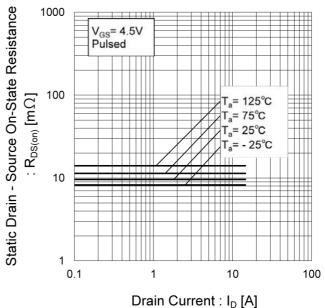


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

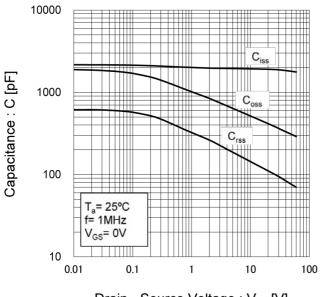
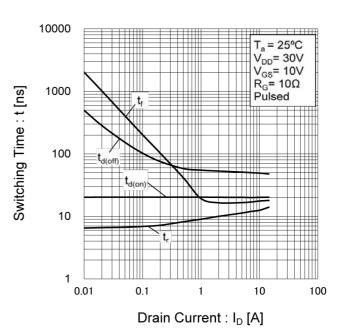


Fig.18 Switching Characteristics



Drain - Source Voltage : $V_{DS}[V]$

Fig.19 Dynamic Input Characteristics

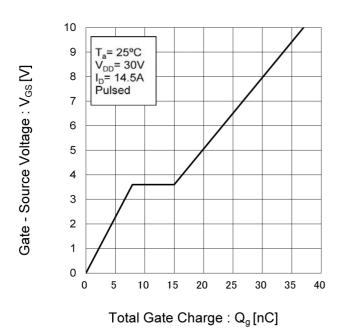
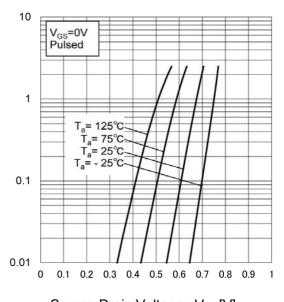


Fig.20 Source Current vs.

Source Drain Voltage



Source-Drain Voltage : $V_{\text{SD}}[V]$

Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

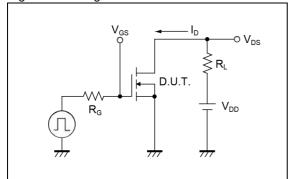


Fig.1-2 Switching Waveforms

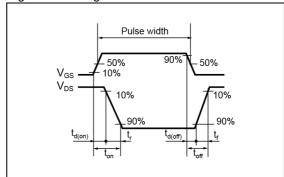


Fig.2-1 Gate Charge Measurement Circuit

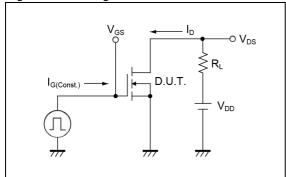


Fig.2-2 Gate Charge Waveform

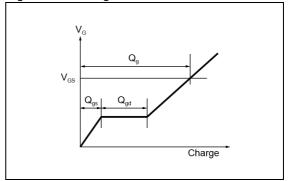


Fig.3-1 Avalanche Measurement Circuit

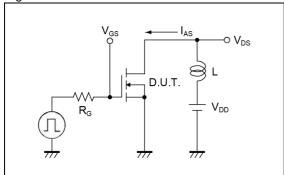
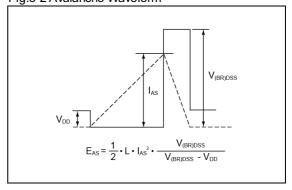


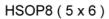
Fig.3-2 Avalanche Waveform

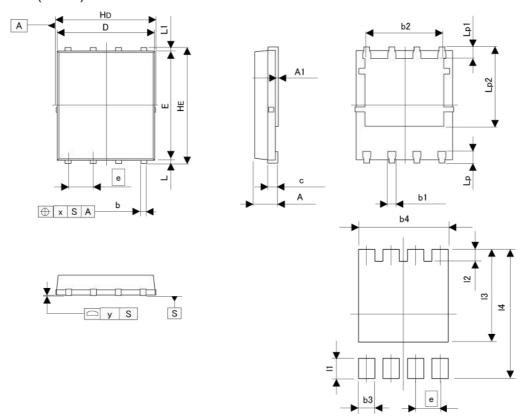


Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions





Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
е	1.	27	0.0	50
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
x	- 2	0.10	12.00	0.004
У	-	0.10	-	0.004

DIM	MILIME	ETERS	INC	NCHES	
DIIVI	MIN	MAX	MIN	MAX	
b3	15	0.59	25	0.023	
b4	¥	4.21	((4)	0.166	
11	2	0.80	(2)	0.031	
12	-	0.82	-	0.032	
13	*	4.32	OH:	0.170	
14	¥	6.10	-	0.240	

Dimension in mm/inches



Notice

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JÁPAN	USA	EU	CHINA
CLASSⅢ	ОГУООШ	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.004

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- Оперативные сроки поставки под заказ (от 5 рабочих дней);
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- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
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- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.