

## Off-Line Digital Green-Mode Quasi-Resonant PWM Controller Integrated with Power BJT

### 1 Description

The iW1818 is a high performance AC/DC power supply control device that uses digital control technology to build peak current mode PWM flyback power supplies. The device includes an internal power BJT, operates in quasi-resonant mode, and includes key built-in protection features. This combination of features and integration simplifies design, improves efficiency, reduces EMI and lowers the bill of material cost.



The iW1818 features a distinctive soft-start scheme, which allows for fast and yet smooth start-up with both small and large capacitive loads. It removes the need for a secondary feedback circuit while achieving excellent line and load regulation. It also eliminates the need for loop compensation components while maintaining stability over all operating conditions. The pulse-by-pulse waveform analysis allows for fast dynamic load response. The built-in power limit function enables optimized transformer design for a wide input voltage range.

Dialog's innovative proprietary technology ensures that power supplies built with the iW1818 can achieve both the highest average efficiency and less than 50mW no-load power consumption; they also have fast yet smooth start-up

### 2 Features

- Targeted at 10W to 12W AC/DC applications with <50mW no-load power consumption at 230V<sub>AC</sub> and fast dynamic response
- **AccuSwitch™** technology with integrated 800V bipolar junction transistor (BJT)
- Adaptively controlled soft-start enables fast and smooth start-up for a wide range of capacitive loads (from 330µF to 6,000µF) with output voltage of 5V to 12V and above
- Very tight constant voltage and constant current regulation over entire operating range
- **PrimAccurate™** primary-side feedback eliminates opto-isolator and simplifies design
- **EZ-EMI®** design enhances manufacturability
- Intrinsically low common mode noise
- Optimized 72kHz maximum PWM switching frequency achieves best size and efficiency
- Adaptive multi-mode PWM/PFM control improves efficiency
- Quasi-resonant operation for highest overall efficiency
- Dynamic base current control
- No external loop compensation components required
- Built-in single-point protections against output short-circuit, output low impedance, and output overvoltage
- Built-in over-temperature protection (OTP)
- No audible noise over entire operating range

### 3 Applications

- Network power adapters for ADSL, wireless access points and routers
- AC/DC adaptor for set top box
- AC/DC power supplies for home appliances





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4 Pinout Description

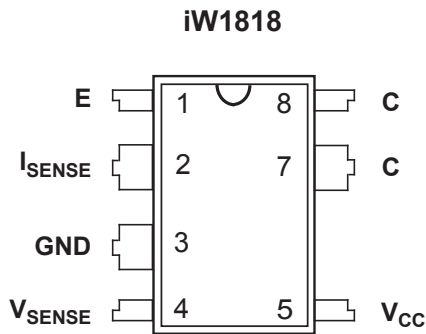


Figure 4.1 : 7-Lead PDIP Package

Pin Number	Pin Name	Type	Pin Description
1	E	BJT Emitter	Emitter of internal BJT (pin 1 and pin 2 must be shorted on PCB).
2	I <sub>SENSE</sub>	Analog Input	Primary current sense. Used for cycle-by-cycle peak current control and current limit.
3	GND	Ground	Ground.
4	V <sub>SENSE</sub>	Analog Input	Auxiliary voltage sense (used for primary-side regulation).
5	V <sub>CC</sub>	Power Input	Power supply for control logic
7	C	BJT Collector	Collector of internal BJT
8	C	BJT Collector	Collector of internal BJT

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### 5 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges that can cause permanent damage if exceeded. For maximum safe operating conditions, refer to Electrical Characteristics in Section 7. ( $T_A = 25^\circ\text{C}$ , unless otherwise noted). Proper design precautions must be made to ensure that the internal die junction temperature of the iW1818 does not exceed  $150^\circ\text{C}$ . Otherwise permanent damage to the device may occur.

Parameter	Symbol	Value	Units
DC supply voltage range (pin 5, $I_{CC} = 20\text{mA}$ max)	$V_{CC}$	-0.3 to 25	V
Continuous DC supply current at $V_{CC}$ pin ( $V_{CC} = 15\text{V}$ )	$I_{CC}$	20	mA
$V_{SENSE}$ input (pin 4, $I_{VSENSE} \leq 10\text{mA}$ )		-0.7 to 4.0	V
$I_{SENSE}$ input (pin 2)		-0.3 to 4.0	V
ESD rating per JEDEC	HBM	2000	V
	MM	200	
	CDM	1000	
Latch-up test per JESD78A		$\pm 100$	mA
Collector-Emitter breakdown voltage (Emitter and base shorted together; $I_C = 1\text{mA}$ , $R_{EB} = 0\Omega$ )	$V_{CES}$	800	V
Collector current (Note 1)	$I_C$	4	A
Collector peak current (Note 1) ( $t_p < 1\text{ms}$ )	$I_{CM}$	8	A
Maximum junction temperature	$T_{JMAX}$	150	$^\circ\text{C}$
Storage temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$

#### Notes:

Note 1. Limited by maximum junction temperature.

### 6 Thermal Characteristics

Parameter	Symbol	Value	Units
Thermal Resistance Junction-to-Ambient (Note 1) (Dissipated power 0.9W)	$\theta_{JA}$	80	$^\circ\text{C/W}$
Thermal Resistance Junction-to-GND pin (pin 3) (Note 2) (Dissipated power 0.9W)	$\psi_{JB}$	25	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Collector pin (Note 2) (Dissipated power 0.9W)	$\psi_{J-BJT}$	35	$^\circ\text{C/W}$
Thermal Shutdown Threshold (Note 3)	$T_{SD}$	150	$^\circ\text{C}$
Thermal Shutdown Recovery (Note 3)	$T_{SD-R}$	120	$^\circ\text{C}$

#### Notes:

Note 1.  $\theta_{JA}$  is measured in a one-cubic-foot natural convection chamber.

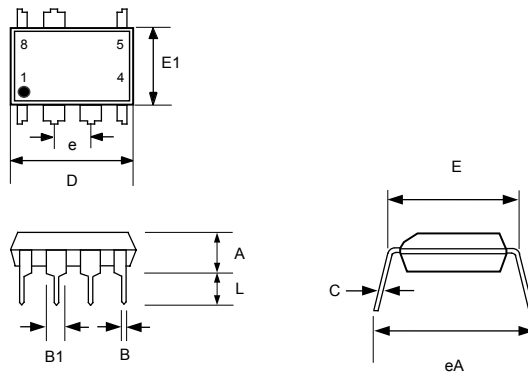
Note 2.  $\psi_{JB}$  [Psi Junction to Board] provides an estimation of the die junction temperature relative to the PCB [Board] surface temperature.  $\psi_{J-BJT}$  [Psi Junction to Collector pin] provides an estimation of the die junction temperature relative to the collector pin [internal BJT Collector] surface temperature.  $\psi_{JB}$  is measured at the ground pin (pin 3) without using any thermal adhesives. See Section 10.14 for more information.

Note 3. These parameters are typical and they are guaranteed by design.

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### 7 Physical Dimensions

#### 7-Lead Plastic Dual In-Line (PDIP) Package



Symbol	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.135	0.145	3.429	3.683
B	0.015	0.021	0.381	0.533
B1	0.050	0.065	1.270	1.650
C	0.007	0.014	0.200	0.356
D	0.367	0.387	9.322	9.830
E	0.300	0.325	7.620	8.255
E1	0.240	0.260	6.096	6.604
e	0.1 BSC		2.54 BSC	
eA	0.332	0.392	8.433	9.957
L	0.120	0.140	3.048	3.556

Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:

[a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1

[b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; package can withstand 10 s immersion < 260°C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E1 does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25 mm per side.

The package top may be smaller than the package bottom. Dimensions D and E1 are determined at the outermost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.

### 8 Ordering Information

Part no.	Options	Package	Description
iW1818-00		PDIP-7	Tube <sup>1</sup>

**Note 1:** Packing quantity is 50 units/tube, 1,000 units (20 tubes)/box. Minimum ordering quantity is 1,000 units.

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