

3-Terminal Fixed Positive Voltage Regulator

DESCRIPTION

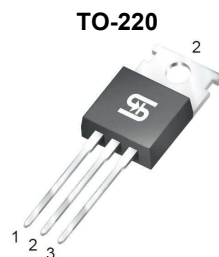
The TS7800 series voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators for a wide variety of applications including local, on-card regulation. These regulators employ internal current limiting, thermal shutdown, and safe-area compensation. With adequate heatsink they can deliver output currents up to 1 ampere. Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

FEATURES

- Output Voltage Range 5V, 8V, 9V, 12V, 24V
- Output current up to 1A
- No external components required
- Internal thermal overload protection
- Internal short-circuit current limiting
- Output transistor safe-area compensation
- Output voltage offered in 4% tolerance
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC.
- Halogen-free according to IEC 61249-2-21

APPLICATION

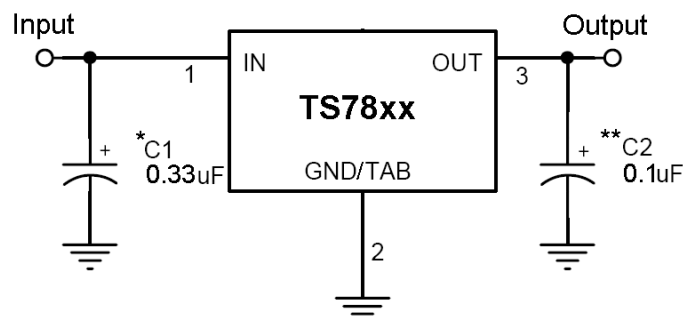
- Power supply
- Home appliance



Pin Definition:

1. Input
2. Ground (tab)
3. Output

TYPICAL APPLICATION CIRCUIT



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the Input ripple voltage.

XX = these two digits of the type number indicate voltage.

* = C_{IN} is required if regulator is located an appreciable distance from power supply filter.

** = C_{OUT} is not needed for stability; however, it does improve transient response.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted) ^(Note 1)				
PARAMETER		SYMBOL	LIMIT	UNIT
Input Voltage	$V_{\text{OUT}}=5\text{V}, 8\text{V}, 9\text{V}, 12\text{V}$	V_{IN}	35	V
	$V_{\text{OUT}}=24\text{V}$		40	
Output Current		I_{OUT}	Internal Limited	
Power Dissipation		P_D	Internal Limited	
Operating Junction Temperature		T_J	$0 \sim +125$	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	$-65 \sim +150$	$^\circ\text{C}$
Thermal Resistance - Junction to Case		$R_{\theta\text{JC}}$	5	$^\circ\text{C/W}$
Thermal Resistance - Junction to Ambient		$R_{\theta\text{JA}}$	50	$^\circ\text{C/W}$

Note:

Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.

TS7805 ELECTRICAL SPECIFICATIONS							
(V _{IN} =10V, I _{OUT} =500mA, 0°C≤T _J ≤125°C, C _{IN} =0.33μF, C _{OUT} =0.1μF; unless otherwise specified.)							
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	V _{OUT}	T _J =25°C		4.80	5	5.20	V
		7.5V≤V _{IN} ≤20V, 10mA≤I _{OUT} ≤1A, P _D ≤15W		4.75	5	5.25	
Line Regulation	REG _{LINE}	T _J =25°C	7.5V≤V _{IN} ≤25V	--	3	100	mV
			8V≤V _{IN} ≤12V	--	1	50	
Load Regulation	REG _{LOAD}	T _J =25°C	10mA≤I _{OUT} ≤1A	--	15	100	
			250mA≤I _{OUT} ≤750mA	--	5	50	
Quiescent Current	I _Q	I _{OUT} =0, T _J =25°C		--	4.2	8	mA
Quiescent Current Change	ΔI _Q	7.5V≤V _{IN} ≤25V		--	--	1.3	
		10mA≤I _{OUT} ≤1A		--	--	0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, T _J =25°C		--	40	--	μV
Ripple Rejection Ratio	RR	f=120Hz, 8V≤V _{IN} ≤18V		62	78	--	dB
Voltage Drop	V _{DROP}	I _{OUT} =1.0A, T _J =25°C		--	2	--	V
Output Resistance	R _{OUT}	f=1KHz		--	17	--	mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C		--	750	--	mA
Peak Output Current	I _{O peak}	T _J =25°C		--	2.2	--	A
Temperature Coefficient of Output Voltage	ΔV _{OUT} / ΔT _J	I _{OUT} =10mA, 0°C≤T _J ≤125°C		--	-0.6	--	mV/ °C

Note:

1. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible and thermal effects must be taken into account separately.
2. This specification applies only for DC power dissipation permitted by absolute maximum ratings.

TS7808 ELECTRICAL SPECIFICATIONS

 ($V_{IN}=14V$, $I_{OUT}=500mA$, $0^{\circ}C \leq T_J \leq 125^{\circ}C$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$; unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	V _{OUT}	T _J =25°C		7.69	8	8.32	V
		10.5V≤V _{IN} ≤23V, 10mA≤I _{OUT} ≤1A, P _D ≤15W		7.61	8	8.40	
Line Regulation	REG _{LINE}	T _J =25°C	10.5V≤V _{IN} ≤25V	6	100	160	mV
			11V≤V _{IN} ≤17V	2	50	80	
Load Regulation	REG _{LOAD}	T _J =25°C	10mA≤I _{OUT} ≤1A	12	100	160	
			250mA≤I _{OUT} ≤750mA	4	50	80	
Quiescent Current	I _Q	I _{OUT} =0, T _J =25°C		--	4.3	8	mA
Quiescent Current Change	ΔI _Q	10.5V≤V _{IN} ≤25V		--	--	1	
		10mA≤I _{OUT} ≤1A		--	--	0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, T _J =25°C		--	52	--	μV
Ripple Rejection Ratio	RR	f=120Hz, 11V≤V _{IN} ≤21V		56	72	--	dB
Voltage Drop	V _{DROP}	I _{OUT} =1.0A, T _J =25°C		--	2	--	V
Output Resistance	R _{OUT}	f=1KHz		--	16	--	mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C		--	450	--	mA
Peak Output Current	I _{O peak}	T _J =25°C		--	2.2	--	A
Temperature Coefficient of Output Voltage	ΔV _{OUT} / ΔT _J	I _{OUT} =10mA, 0°C≤T _J ≤125°C		--	-0.8	--	mV/ °C

TS7809 ELECTRICAL SPECIFICATIONS

 ($V_{IN}=15V$, $I_{OUT}=500mA$, $0^{\circ}C \leq T_J \leq 125^{\circ}C$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$; unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	V _{OUT}	T _J =25°C		8.65	9	9.36	V
		11.5V≤V _{IN} ≤23V, 10mA≤I _{OUT} ≤1A, P _D ≤15W		8.57	9	9.45	
Line Regulation	REG _{LINE}	T _J =25°C	11.5V≤V _{IN} ≤26V	6	100	180	mV
			12V≤V _{IN} ≤17V	2	50	90	
Load Regulation	REG _{LOAD}	T _J =25°C	10mA≤I _{OUT} ≤1A	12	100	180	
			250mA≤I _{OUT} ≤750mA	4	50	90	
Quiescent Current	I _Q	I _{OUT} =0, T _J =25°C		--	4.3	8	mA
Quiescent Current Change	ΔI _Q	11.5V≤V _{IN} ≤26V		--	--	1	
		10mA≤I _{OUT} ≤1A		--	--	0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, T _J =25°C		--	52	--	μV
Ripple Rejection Ratio	RR	f=120Hz, 12V≤V _{IN} ≤22V		55	72	--	dB
Voltage Drop	V _{DROP}	I _{OUT} =1.0A, T _J =25°C		--	2	--	V
Output Resistance	R _{OUT}	f=1KHz		--	16	--	mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C		--	450	--	mA
Peak Output Current	I _{O peak}	T _J =25°C		--	2.2	--	A
Temperature Coefficient of Output Voltage	ΔV _{OUT} / ΔT _J	I _{OUT} =10mA, 0°C≤T _J ≤125°C		--	-1	--	mV/ °C

Note:

1. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible and thermal effects must be taken into account separately.
2. This specification applies only for DC power dissipation permitted by absolute maximum ratings.

TS7812 ELECTRICAL SPECIFICATIONS

 ($V_{IN}=19V$, $I_{OUT}=500mA$, $0^{\circ}C \leq T_J \leq 125^{\circ}C$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$; unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	V _{OUT}	T _J =25°C		11.53	12	12.48	V
		14.5V≤V _{IN} ≤27V, 10mA≤I _{OUT} ≤1A, P _D ≤15W		11.42	12	12.60	
Line Regulation	REG _{LINE}	T _J =25°C	14.5V≤V _{IN} ≤30V	10	100	240	mV
			15V≤V _{IN} ≤19V	3	50	120	
Load Regulation	REG _{LOAD}	T _J =25°C	10mA≤I _{OUT} ≤1A	12	100	240	
			250mA≤I _{OUT} ≤750mA	4	50	120	
Quiescent Current	I _Q	I _{OUT} =0, T _J =25°C		--	4.3	8	mA
Quiescent Current Change	ΔI _Q	14.5V≤V _{IN} ≤30V		--	--	1	
		10mA≤I _{OUT} ≤1A		--	--	0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, T _J =25°C		--	75	--	μV
Ripple Rejection Ratio	RR	f=120Hz, 15V≤V _{IN} ≤25V		55	71	--	dB
Voltage Drop	V _{DROP}	I _{OUT} =1.0A, T _J =25°C		--	2	--	V
Output Resistance	R _{OUT}	f=1KHz		--	18	--	mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C		--	350	--	mA
Peak Output Current	I _{O peak}	T _J =25°C		--	2.2	--	A
Temperature Coefficient of Output Voltage	ΔV _{OUT} / ΔT _J	I _{OUT} =10mA, 0°C≤T _J ≤125°C		--	-1	--	mV/ °C

TS7824 ELECTRICAL SPECIFICATIONS

 ($V_{IN}=33V$, $I_{OUT}=500mA$, $0^{\circ}C \leq T_J \leq 125^{\circ}C$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$; unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	V _{OUT}	T _J =25°C		23.07	24	24.96	V
		27V≤V _{IN} ≤38V, 10mA≤I _{OUT} ≤1A, P _D ≤15W		22.85	24	25.20	
Line Regulation	REG _{LINE}	T _J =25°C	27V≤V _{IN} ≤38V	18	100	480	mV
			28V≤V _{IN} ≤32V	6	50	240	
Load Regulation	REG _{LOAD}	T _J =25°C	10mA≤I _{OUT} ≤1A	12	100	480	
			250mA≤I _{OUT} ≤750mA	4	50	240	
Quiescent Current	I _Q	I _{OUT} =0, T _J =25°C		--	4.6	8	mA
Quiescent Current Change	ΔI _Q	27V≤V _{IN} ≤38V		--	--	1	
		10mA≤I _{OUT} ≤1A		--	--	0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, T _J =25°C		--	170	--	μV
Ripple Rejection Ratio	RR	f=120Hz, 27V≤V _{IN} ≤37V		54	70	--	dB
Voltage Drop	V _{DROP}	I _{OUT} =1.0A, T _J =25°C		--	2	--	V
Output Resistance	R _{OUT}	f=1KHz		--	28	--	mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C		--	150	--	mA
Peak Output Current	I _{O peak}	T _J =25°C		--	2.2	--	A
Temperature Coefficient of Output Voltage	ΔV _{OUT} / ΔT _J	I _{OUT} =10mA, 0°C≤T _J ≤125°C		--	-1.5	--	mV/ °C

Note:

1. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible and thermal effects must be taken into account separately.
2. This specification applies only for DC power dissipation permitted by absolute maximum ratings.

ORDERING INFORMATION

OUTPUT VOLTAGE	PART NO.	PACKAGE	PACKING
5V	TS7805CZ C0	TO-220	50pcs / Tube
	TS7805CZ C0G	TO-220	50pcs / Tube
8V	TS7808CZ C0	TO-220	50pcs / Tube
	TS7808CZ C0G	TO-220	50pcs / Tube
9V	TS7809CZ C0	TO-220	50pcs / Tube
	TS7809CZ C0G	TO-220	50pcs / Tube
12V	TS7812CZ C0	TO-220	50pcs / Tube
	TS7812CZ C0G	TO-220	50pcs / Tube
24V	TS7824CZ C0	TO-220	50pcs / Tube
	TS7824CZ C0G	TO-220	50pcs / Tube

ELECTRICAL CHARACTERISTICS CURVES ($T_A=25^\circ\text{C}$, unless otherwise noted)

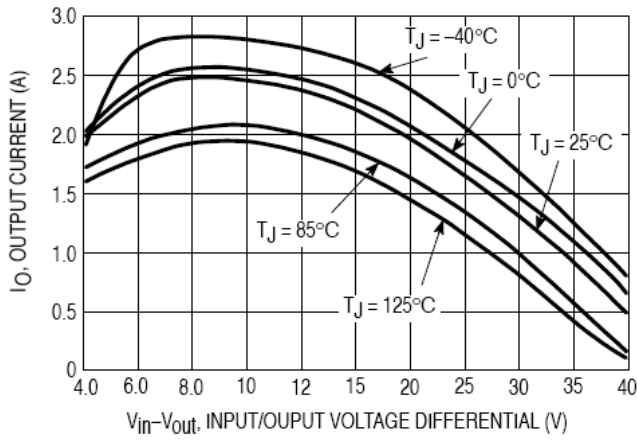


Figure 1. Peak Output Current as a Function of Input-Output Differential Voltage

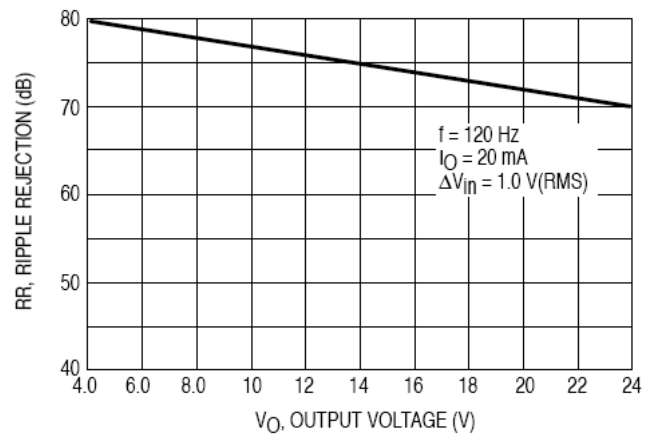


Figure 2. Ripple Rejection as a Function of Output Voltage

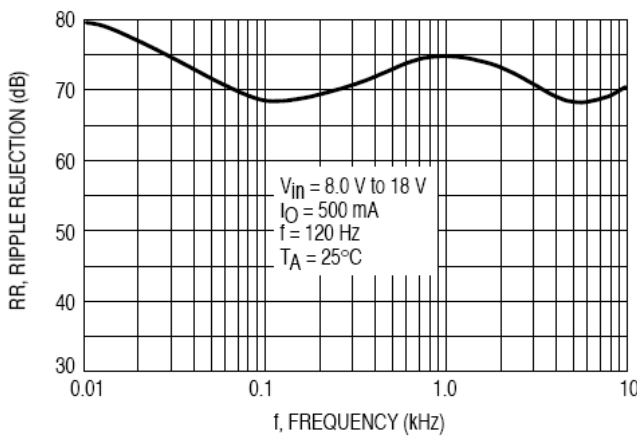


Figure 3. Ripple Rejection as a Function of Frequency

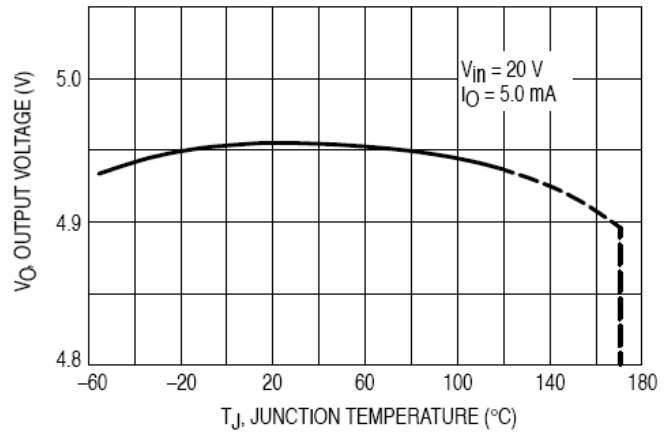


Figure 4. Output Voltage as a Function of Junction Temperature

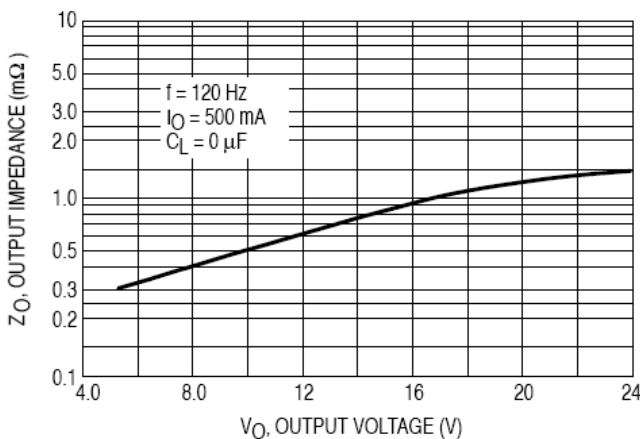


Figure 5. Output Impedance as a Function of Output Voltage

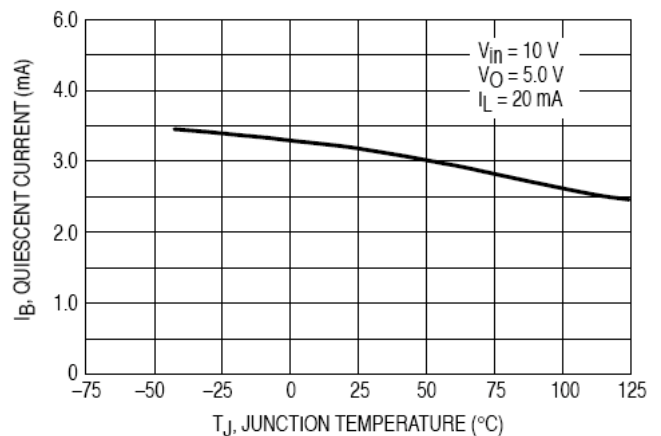
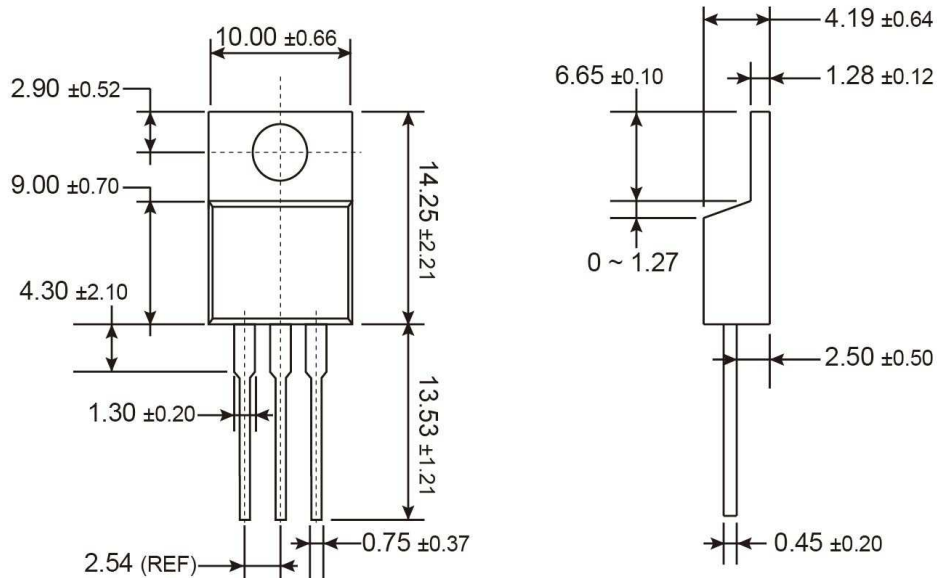


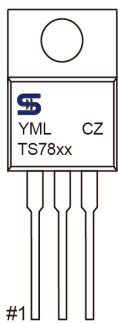
Figure 6. Quiescent Current as a Function of Temperature

PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

TO-220



MARKING DIAGRAM



- xx** = Output Voltage Code
(**05**=5V, **08**=8V, **09**=9V, **12**=12V, **24**=24V)
- Y** = Year Code
- M** = Month Code for Halogen Free Product

O =Jan	P =Feb	Q =Mar	R =Apr
S =May	T =Jun	U =Jul	V =Aug
W =Sep	X =Oct	Y =Nov	Z =Dec
- L** = Lot Code
- CZ** = Package Code for TO-220

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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