

1.5A High-Speed 30V MOSFET Drivers

Features

- High Peak Output Current 1.5 A
- Wide Input Supply Operating Range:
 - 4.5V to 30V
- · High Capacitive Load Drive Capability:
 - 1000 pF in 25 nsec
- Short Delay Times <78 nsec Typ.
- Low Supply Current:
 - With Logic '1' Input 2.5 mA
 - With Logic '0' Input 300 μA
- Low Output Impedance 7 Ω
- Latch-Up Protected: Will Withstand >300 mA Reverse Current
- ESD Protected 4 kV

Applications

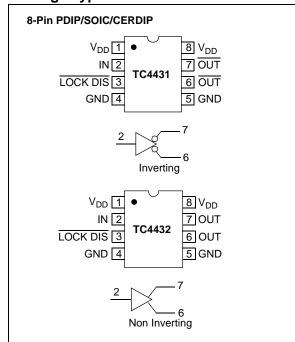
- · Small Motor Drive
- Power MOSFET Driver
- · Driving Bipolar Transistors

General Description

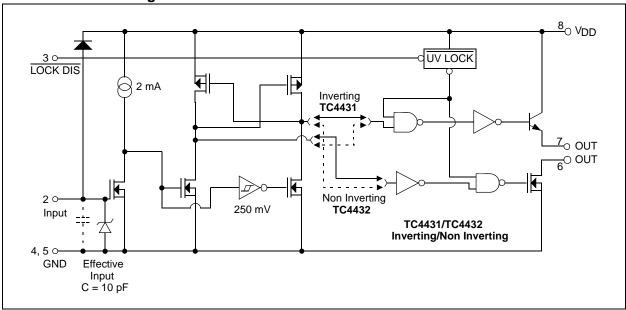
The TC4431/TC4432 are 30V CMOS buffer/drivers suitable for use in high-side driver applications. They will not latch up under any conditions within their power and voltage ratings. They can accept, without damage or logic upset, up to 300 mA of reverse current (of either polarity) being forced back into their outputs. All terminals are fully protected against up to 4 kV of electrostatic discharge.

Under-voltage lockout circuitry forces the output to a 'low' state when the input supply voltage drops below 7V. For operation at lower voltages, disable the lockout and start-up circuit by grounding pin 3 (LOCK DIS); for all other situations, pin 3 (LOCK DIS) should be left floating. The under-voltage lockout and start-up circuit gives brown out protection when driving MOSFETS.

Package Type



Functional Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings†

 † Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise noted, $T_A = +25^{\circ}C$ with $4.5V \le V_{DD} \le 30V$.						
Parameters	Sym	Min	Тур	Max	Units	Conditions
Input						
Logic '1', High Input Voltage	V _{IH}	2.4	_	_	V	
Logic '0', Low Input Voltage	V_{IL}	_	_	0.8	V	
Input Current (Note 1)	I _{IN}	-1	_	1	μA	$0V \le V_{IN} \le 12V$
Output						
High Output Voltage	V _{OH}	V _{DD} – 1.0	V _{DD} – 0.8	_	V	I _{OUT} = 100 mA
Low Output Voltage	V _{OL}	_	_	0.025	V	
Output Resistance	R _O	_	7	10	Ω	I _{OUT} = 10 mA, V _{DD} = 30V
Peak Output Current	I _{PK}	_	3.0 1.5		Α	Source: $V_{DD} = 30V$ Sink: $V_{DD} = 30V$
Latch-Up Protection Withstand Reverse Current	I _{REV}	_	0.3	_	А	Duty cycle ≤ 2%, t ≤ 300 µsec
Switching Time (Note 2)	ı		l .	ı		
Rise Time	t _R	_	25	40	ns	Figure 4-1
Fall Time	t _F	_	33	50	ns	Figure 4-1
Delay Time	t _{D1}	_	62	80	ns	Figure 4-1
Delay Time	t _{D2}	_	78	90	ns	Figure 4-1
Power Supply					•	
Power Supply Current	I _S		2.5 0.3	4 0.4	mA	$V_{IN} = 3V$ $V_{IN} = 0V$
Start-up Threshold	Vs	_	8.4	10	V	
Drop-out Threshold	V _{DO}	7	7.7	_	V	Note 3

- Note 1: For inputs >12V, add a 1 $k\Omega$ resistor in series with the input. See **Section 2.0** "Typical Performance Curves" for input current graph.
 - 2: Switching times are ensured by design.
 - **3:** For operation below 7V, pin 3 (LOCK DIS) should be tied to ground to disable the lockout and start-up circuit, otherwise, pin 3 **must** be left floating.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise noted, Over operating temperature range with $4.5V \le V_{DD} \le 30V$.						
Parameters	Sym	Min	Тур	Max	Units	Conditions
Input						
Logic '1', High Input Voltage	V _{IH}	2.4	_	_	V	
Logic '0', Low Input Voltage	V_{IL}	_	_	0.8	V	
Input Current (Note 1)	I _{IN}	-10	_	10	μΑ	$0V \le V_{IN} \le 12V$
Output						
High Output Voltage	V _{OH}	V _{DD} – 1.2	_	_	V	I _{OUT} = 100 mA
Low Output Voltage	V _{OL}	_	_	0.025	V	
Output Resistance	R _O	_	_	12	Ω	I _{OUT} = 10 mA, V _{DD} = 30V
Switching Time (Note 2)						
Rise Time	t _R	_	_	60	ns	Figure 4-1
Fall Time	t _F	_	_	70	ns	Figure 4-1
Delay Time	t _{D1}	_	_	100	ns	Figure 4-1
Delay Time	t _{D2}	_	_	110	ns	Figure 4-1
Power Supply						
Power Supply Current	I _S	_	_	6	mA	V _{IN} = 3V
		_	_	0.7		$V_{IN} = 0V$
Start-up Threshold	V _S	_	8.4	10	V	
Drop-out Threshold	V_{DO}	7	7.7	_	V	Note 3

- Note 1: For inputs >12V, add a 1 k Ω resistor in series with the input. See **Section 2.0** "Typical Performance Curves" for input current graph.
 - **2:** Switching times are ensured by design.
 - **3:** For operation below 7V, pin 3 (LOCK DIS) should be tied to ground to disable the lockout and start-up circuit, otherwise, pin 3 **must** be left floating.

TEMPERATURE CHARACTERISTICS

Electrical Specifications: Unless otherwise noted, all parameters apply with 4.5V \leq V _{DD} \leq 30V.						
Parameters	Sym	Min	Тур	Max	Units	Conditions
Temperature Ranges						
Specified Temperature Range (C)	T _A	0	_	+70	۰C	
Specified Temperature Range (E)	T _A	-40	_	+85	۰C	
Specified Temperature Range (V)	T _A	-40	_	+125	۰C	
Maximum Junction Temperature	T_J	_	_	+150	۰C	
Storage Temperature Range	T_A	-65	_	+150	۰C	
Package Thermal Resistances:						
Thermal Resistance, 8L-SOIC	θ_{JA}	_	155	_	°C/W	
Thermal Resistance, 8L-PDIP	θ_{JA}		125	_	°C/W	
Thermal Resistance, 8L-CERDIP	θ_{JA}	_	150	_	°C/W	

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, T_A = +25°C with 4.5V \leq V_{DD} \leq 30V.

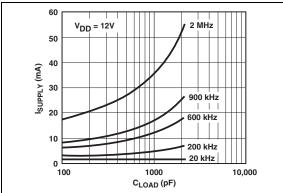


FIGURE 2-1: Supply Current vs. Capacitive Load.

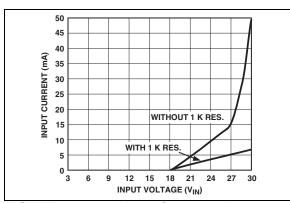


FIGURE 2-2: Input Current vs. Input Voltage.

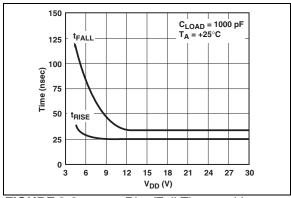


FIGURE 2-3: Rise/Fall Time vs. V_{DD}.

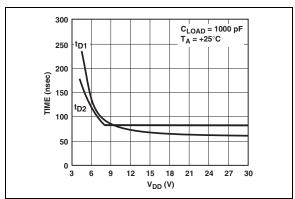


FIGURE 2-4: t_{D1} and t_{D2} Delay vs. V_{DD} .

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin No.	Symbol	Description
1	V_{DD}	Supply Input, 4.5V to 30V
2	IN	TTL/CMOS Compatible Input
3	LOCK DIS	Input Pin, Enable/Disable for UV Lockout
4	GND	Ground
5	GND	Ground
6	OUT	Drive Output, Pull Down
7	OUT	Drive Output, Pull Up
8	V_{DD}	Supply Input, 4.5V to 30V

3.1 Supply Input (V_{DD})

The V_{DD} input is the bias supply input for the MOSFET driver and is rated for 4.5V to 30V with respect to the ground pins. The V_{DD} input should be bypassed to ground with a local ceramic capacitor. The value of this capacitor should be chosen based on the capacitive load that is being driven.

3.2 Control Input (IN)

The MOSFET driver input is a TTL/CMOS compatible input with 250 mV of hysteresis between the high and low threshold voltages. If an input signal level of greater than 12V is applied to the device, a series current limiting resistor is recommended.

3.3 Lockout Disable (LOCK DIS)

The lockout pin enables/disables the undervoltage lockout feature of the device. If undervoltage lockout is desired (output is not enabled until the bias voltage reaches 8.4V (typical) on the rising edge and is disabled when the bias voltage reaches 7.7V (typical) on the falling edge), the lockout pin should be left floating. If operation below 7V is desired, the lockout pin should be tied to ground.

3.4 Ground (GND)

The ground pins are the return path for the bias current and for the high peak currents which discharge the load capacitor. Both ground pins should be used to ensure proper operation. The ground pins should be tied into a ground plane or have short traces to the bias supply source return.

3.5 Drive Output (OUT)

The TC4431/TC4432 devices have individual source and sink output pins. This feature can be used to adjust the rise and fall time independently by adding separate charge and discharge resistors external to the device. Pin 7 (source output) can source 3 A peak currents into capacitive loads and pin 6 (sink output) can sink 1.5 A peak currents from a capacitive load.

4.0 APPLICATIONS INFORMATION

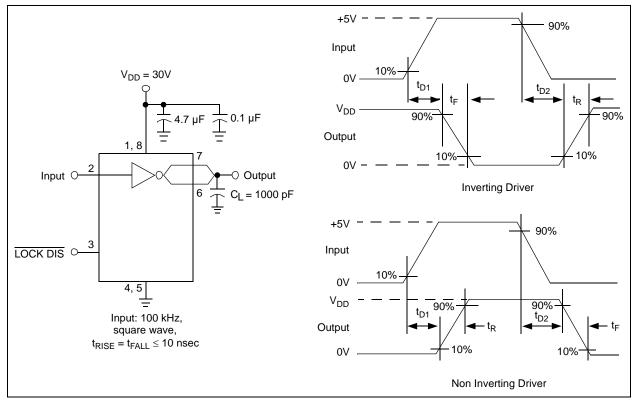


FIGURE 4-1: Switching Time Test Circuit.

5.0 PACKAGING INFORMATION

5.1 Package Marking Information

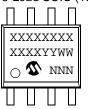
8-Lead CERDIP (300 mil)



8-Lead PDIP (300 mil)



8-Lead SOIC (150 mil)



Example:



Example:





Legend: XX...X Customer-specific information
Year code (last digit of calendar year)

YY Year code (last digit of calendar year)
YY Year code (last 2 digits of calendar year)
WW Week code (week of January 1 is week '01')

NNN Alphanumeric traceability code

e3 Pb-free JEDEC designator for Matte Tin (Sn)

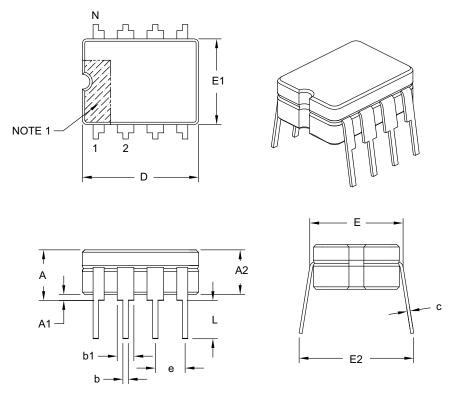
This package is Pb-free. The Pb-free JEDEC designator (e3)

can be found on the outer packaging for this package.

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.

8-Lead Ceramic Dual In-Line (JA) - .300" Body [CERDIP]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



	Units		INCHES		
	Dimension Limits	MIN	NOM	MAX	
Number of Pins	N		8		
Pitch	е		.100 BSC		
Top to Seating Plane	A	_	_	.200	
Standoff §	A1	.015	_	_	
Ceramic Package Height	A2	.140	_	.175	
Shoulder to Shoulder Width	E	.290	_	.320	
Ceramic Package Width	E1	.230	.248	.300	
Overall Length	D	.370	.380	.400	
Tip to Seating Plane	L	.125	_	.200	
Lead Thickness	С	.008	_	.015	
Upper Lead Width	b1	.045	_	.065	
Lower Lead Width	b	.015	_	.023	
Overall Row Spacing	E2	.314	_	.410	

Notes:

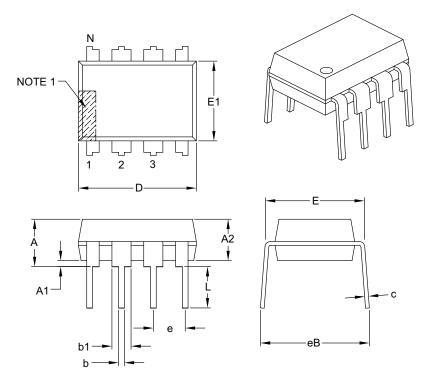
- 1. Pin 1 visual index feature may vary, but must be located within the hatched area.
- 2. § Significant Characteristic.
- 3. Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-001B

8-Lead Plastic Dual In-Line (P) - 300 mil Body [PDIP]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



	Units		INCHES	
	Dimension Limits	MIN	NOM	MAX
Number of Pins	N		8	
Pitch	е	.100 BSC		
Top to Seating Plane	А	_	_	.210
Molded Package Thickness	A2	.115	.130	.195
Base to Seating Plane	A1	.015	_	_
Shoulder to Shoulder Width	E	.290	.310	.325
Molded Package Width	E1	.240	.250	.280
Overall Length	D	.348	.365	.400
Tip to Seating Plane	L	.115	.130	.150
Lead Thickness	С	.008	.010	.015
Upper Lead Width	b1	.040	.060	.070
Lower Lead Width	b	.014	.018	.022
Overall Row Spacing §	eB	_	_	.430

Notes:

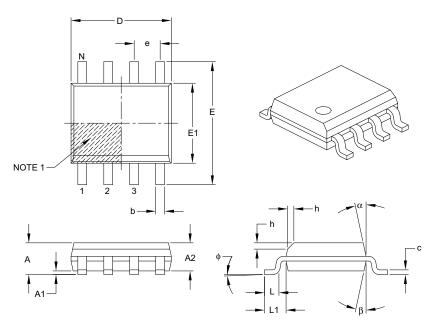
- 1. Pin 1 visual index feature may vary, but must be located with the hatched area.
- 2. § Significant Characteristic.
- 3. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .010" per side.
- 4. Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-018B

8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm Body [SOIC]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



	Units		MILLIMETERS		
	Dimension Limits	MIN	NOM	MAX	
Number of Pins	N		8		
Pitch	е	1.27 BSC			
Overall Height	A	_	_	1.75	
Molded Package Thickness	A2	1.25	_	_	
Standoff §	A1	0.10	_	0.25	
Overall Width	E	6.00 BSC			
Molded Package Width	E1	3.90 BSC			
Overall Length	D		4.90 BSC		
Chamfer (optional)	h	0.25	_	0.50	
Foot Length	L	0.40	_	1.27	
Footprint	L1		1.04 REF		
Foot Angle	ф	0°	_	8°	
Lead Thickness	С	0.17	_	0.25	
Lead Width	b	0.31	_	0.51	
Mold Draft Angle Top	α	5°	_	15°	
Mold Draft Angle Bottom	β	5°	_	15°	

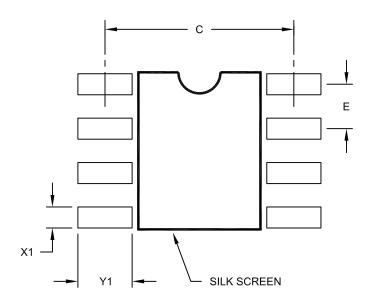
Notes:

- 1. Pin 1 visual index feature may vary, but must be located within the hatched area.
- 2. § Significant Characteristic.
- 3. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15 mm per side.
- 4. Dimensioning and tolerancing per ASME Y14.5M.
 - BSC: Basic Dimension. Theoretically exact value shown without tolerances.
 - REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-057B

8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm Body [SOIC]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



RECOMMENDED LAND PATTERN

	MILLIMETERS			
Dimension	Dimension Limits		NOM	MAX
Contact Pitch	E		1.27 BSC	
Contact Pad Spacing	С		5.40	
Contact Pad Width (X8)	X1			0.60
Contact Pad Length (X8)	Y1			1.55

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing No. C04-2057A

APPENDIX A: REVISION HISTORY

Revision D (December 2007)

The following is the list of modifications:

- Section 1.0 "Electrical Characteristics": Added V temperature information to Temperature Characteristics table.
- 2. Added Revision History.
- 3. Added V temperature range to Product identification System page.

Revision C (May 2003)

The following is the list of modifications:

1. Undocumented changes

Revision B (May 2002)

The following is the list of modifications:

1. Undocumented changes

Revision A (April 2002)

• Original Release of this Document.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO.	<u> </u>	Examples:
_ •	 erature Package nge	 a) TC4431COA: 1.5A MOSFET driver, SOIC package, 0°C to +70°C. b) TC4431EJA: 1.5A MOSFET driver,
Device:	TC4431: 1.5A High-Speed 30V MOSFET Driver, Inverting TC4432: 1.5A High-Speed 30V MOSFET Driver, Non Inverting	CERDIP package, -40°C to +85°C. c) TC4431VPA: 1.5A MOSFET driver, PDIP package, -40°C to +125°C.
Temperature Range:	C = 0°C to +70°C E = -40°C to +85°C V = -40°C to +125°C	 a) TC4432CPA: 1.5A MOSFET driver, PDIP package, 0°C to +70°C. b) TC4432EPA: 1.5A MOSFET driver, PDIP package, -40°C to +85°C. c) TC4432VOA713: Tape and Reel, 1.5A
Package:	JA = Ceramic Dual In-line (300 mil Body), 8-lead * OA = Plastic SOIC, (150 mil Body), 8-lead OA713 = Plastic SOIC, (150 mil Body), 8-lead (Tape and Reel) PA = Plastic DIP (300 mil Body), 8-lead* *	MOSFET driver, SOIC package, -40°C to +125°C.
	* Offered in E-temp range only. * * The only package offered in the V temp range.	

NOTES:

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the
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Taiwan - Taipei

Tel: 886-2-2500-6610 Fax: 886-2-2508-0102

Thailand - Bangkok

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EUROPE

Austria - Wels

Tel: 43-7242-2244-39 Fax: 43-7242-2244-393

Denmark - Copenhagen

Tel: 45-4450-2828 Fax: 45-4485-2829

France - Paris

Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79

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Tel: 49-89-627-144-0 Fax: 49-89-627-144-44

Italy - Milan

Tel: 39-0331-742611 Fax: 39-0331-466781

Netherlands - Drunen Tel: 31-416-690399

Fax: 31-416-690340

Spain - Madrid

Tel: 34-91-708-08-90 Fax: 34-91-708-08-91 **UK - Wokingham**

Tel: 44-118-921-5869 Fax: 44-118-921-5820

10/05/07



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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