

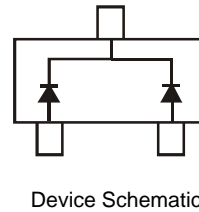
## DUAL SURFACE MOUNT SCHOTTKY BARRIER DIODE

### Features

- Low Forward Voltage Drop
- Common Cathode Configuration
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Notes 3 & 4)**

### Mechanical Data

- Case: SC59
- Case Material: Molded Plastic, "Green" Molding Compound, Note 5. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.008 grams (approximate)

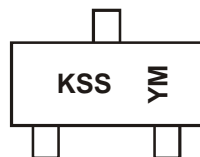


### Ordering Information (Note 5)

Part Number	Case	Packaging
SDM20E40C-7-F	SC59	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.
  5. For Packaging Details, go to our website at <http://www.diodes.com>.

### Marking Information



KSS = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: N = 2002)  
 M = Month (ex: 9 = September)

#### Date Code Key

Year	2002	2003	2004	...	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	N	P	R	...	V	W	X	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$	40	V
Working Peak Reverse Voltage	$V_{RWM}$		
DC Blocking Voltage	$V_R$		
RMS Reverse Voltage	$V_{R(RMS)}$	28	V
Forward Continuous Current (Note 6)	$I_{FM}$	0.4	A
Non-Repetitive Peak Forward Surge Current @ $t = 8.3\text{ms}$	$I_{FSM}$	2	A
Repetitive peak Forward Current	$I_{FRM}$	500	mA

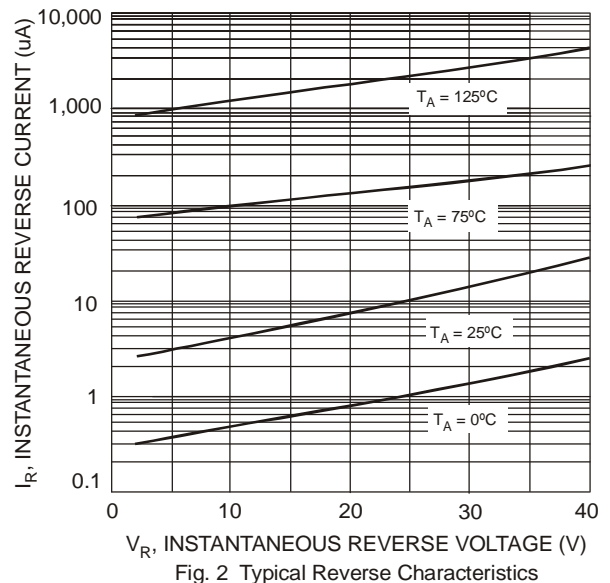
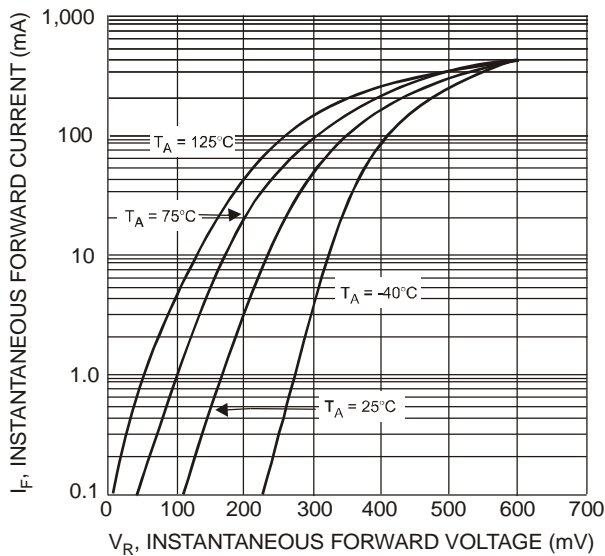
**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	$P_D$	400	mW
Operating Temperature Range	$T_{OP}$	-30 to +85	$^\circ\text{C}$
Junction Temperature Range	$T_J$	-30 to +125	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-40 to +125	$^\circ\text{C}$

**Electrical Characteristics** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	40	—	—	V	$I_R = 500\mu\text{A}$
Forward Voltage	$V_F$	—	—	300 500	mV	$I_F = 10\text{mA}$ $I_F = 200\text{mA}$
Leakage Current (Note 7)	$I_R$	—	—	70	$\mu\text{A}$	$V_R = 25\text{V}$
Total Capacitance	$C_T$	—	—	100	pF	$V_R = 0\text{V}, f = 1.0\text{MHz}$

Notes: 6. Mounted on FR4 PC Board with recommended pad layout which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.  
7. Short duration pulse test used to minimize self-heating effect.



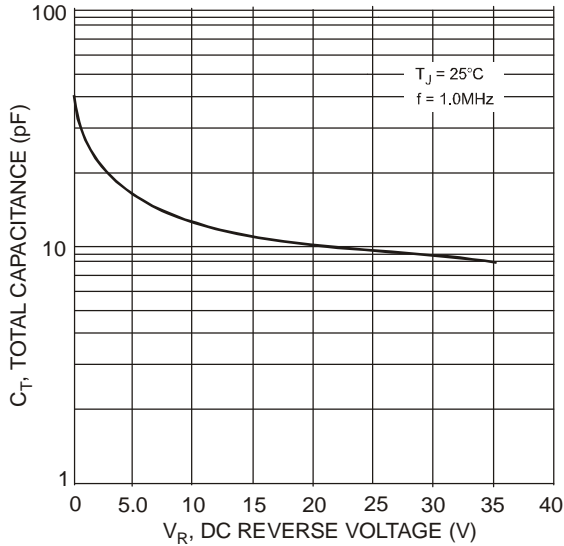


Fig. 3 Total Capacitance vs. Reverse Voltage

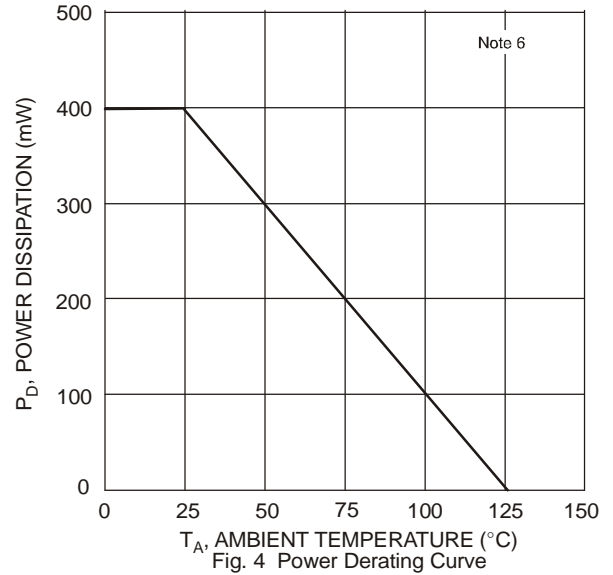
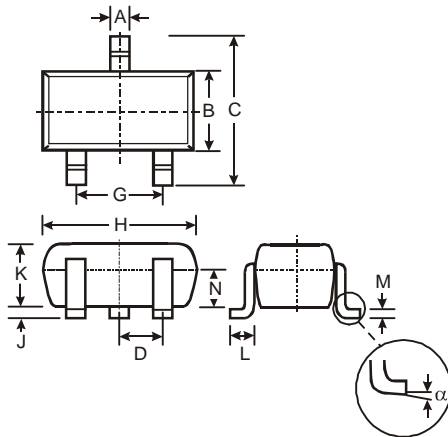


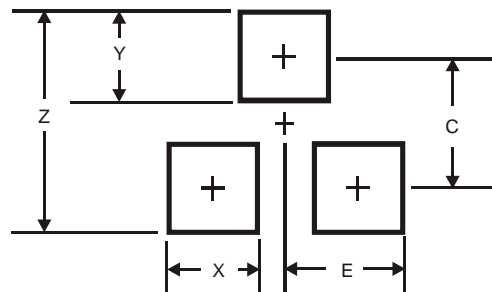
Fig. 4 Power Derating Curve

**Package Outline Dimensions**



SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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#### Как с нами связаться

**Телефон:** 8 (812) 309 58 32 (многоканальный)

**Факс:** 8 (812) 320-02-42

**Электронная почта:** [org@eplast1.ru](mailto:org@eplast1.ru)

**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.