



# ±8Gauss, High Performance, Low Cost 3-axis Magnetic Sensor

## MMC5883MA

### FEATURES

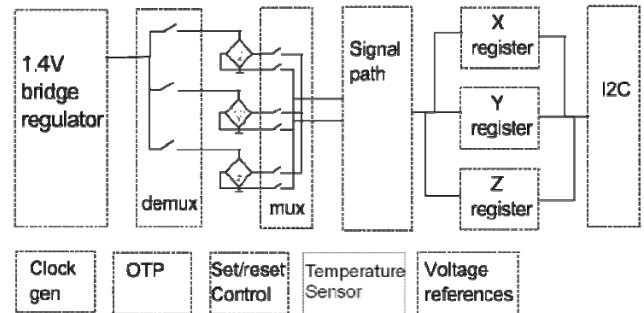
- Fully integrated 3-axis magnetic sensor and electronic circuits requiring fewer external components
- Superior Dynamic Range and Accuracy:
  - ✓ ±8G FSR with 16bits operation
  - ✓ 0.25mG per LSB resolution
  - ✓ 0.4mG total RMS noise
  - ✓ Enables heading accuracy of ±1°
- Max output data rate of 600Hz
- Industrial Standard Low profile package 3.0x3.0x1.0mm
- Degaussing with built-in SET/RESET function
  - ✓ Eliminates thermal variation induced offset error (Null field output)
  - ✓ Clears the residual magnetization resulting from strong external fields
- On-chip sensitivity compensation
- On-chip temperature sensor
- Motion\_Detection and Data\_Ready Interrupt
- Low power consumption
- 1µA power down current
- I<sup>2</sup>C Slave, FAST(≤400KHz) mode
- 2.5V single low power supply
- 1.8V I<sup>2</sup>C interface
- RoHS compliant
- P2P compatible with HMC5883L

### APPLICATIONS

- Electronic Compass & Navigation
- Position Sensing
- General Purpose Magnetic Field Measurements

### DESCRIPTION

The MMC5883MA is a complete 3-axis magnetic sensor with on-chip signal processing and integrated I<sup>2</sup>C bus suitable for use in various applications. The device can be connected directly to a microprocessor, eliminating the need for A/D converters or timing resources. It can measure magnetic fields within the full scale range of ±8Gauss (G), with 0.25mG per LSB resolution at 16bits operation mode and 0.4mG total



FUNCTIONAL BLOCK DIAGRAM

RMS noise level, enabling heading accuracy of ±1° in electronic compass applications. Contact MEMSIC for access to advanced calibration and tilt-compensation algorithms.

An integrated SET/RESET function provides for the elimination of error due to Null Field output change with temperature. Temperature information from the integrated temperature sensor is available over the I<sup>2</sup>C Interface. The SET/RESET function can be performed for each measurement, periodically, or when the temperature changes by a predetermined amount as the specific application requires.

In addition, the SET/RESET function clears the sensors of any residual magnetic polarization resulting from exposure to strong external magnets.

The MMC5883MA is packaged in a low profile LGA package (3.0 x 3.0 x 1.0 mm) and an operating temperature range from -40°C to +85°C.

The MMC5883MA provides an I<sup>2</sup>C digital output with 400 KHz, fast mode operation.

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**SPECIFICATIONS** (Measurements @ 25°C, unless otherwise noted; V<sub>DD</sub>=2.5V unless otherwise specified)

Parameter	Conditions	Min	Typ	Max	Units
Field Range (Each Axis)	Total applied field		±8		G
Supply Voltage	VDD	2.16	2.5	3.6	V
	VDDIO(I <sup>2</sup> C interface and INT)	1.62	1.8	3.6	V
Supply Voltage Rise Time				5.0	ms
Supply Current <sup>1</sup> (7 measurements/second)	BW=00		120		µA
	BW=01		60		µA
	BW=10		30		µA
	BW=11		20		µA
Power Down Current			1.0		µA
Operating Temperature		-40		85	°C
Storage Temperature		-55		125	°C
Linearity Error (Best fit straight line)	FS=±8G H <sub>applied</sub> =±4G		0.2		%FS
Hysteresis	3 sweeps across ±8G		0.2		%FS
Repeatability Error	3 sweeps across ±8G		0.2		%FS
Alignment Error			±1.0	±3.0	Degrees
Transverse Sensitivity			±2.0		%
Total RMS Noise	BW=00		0.4		mG
	BW=01		0.6		mG
	BW=10		1.0		mG
	BW=11		1.2		mG
Output Resolution			16		Bits
Max Output data rate	BW=00		100		Hz
	BW=01		200		Hz
	BW=10		400		Hz
	BW=11		600		Hz
Heading Accuracy <sup>2</sup>			±1.0		Degrees
Sensitivity	±8 G		±5		%
			4096		counts/G
Sensitivity Change Over Temperature <sup>3</sup>	-40~85 °C Delta from 25 °C ±8 G		±5		%
Null Field Output			±0.1		G
			32768		Counts
Null Field Output Change Over Temperature Using SET/RESET	-40~85 °C Delta from 25 °C		±2		mG
Temperature Sensor Output			128		Counts
			0.7		°C/Count
Disturbing Field <sup>4</sup>			10		G
Maximum Exposed Field				10,000	G
SET/RESET Repeatability			2		mG

<sup>1</sup> Supply current is proportional to how many measurements performed per second

<sup>2</sup> MEMSIC product enables users to utilize heading accuracy to be 1.0 degree typical when using MEMSIC's proprietary software or algorithm

<sup>3</sup> Determined by 77pcs x 3 lot characterization

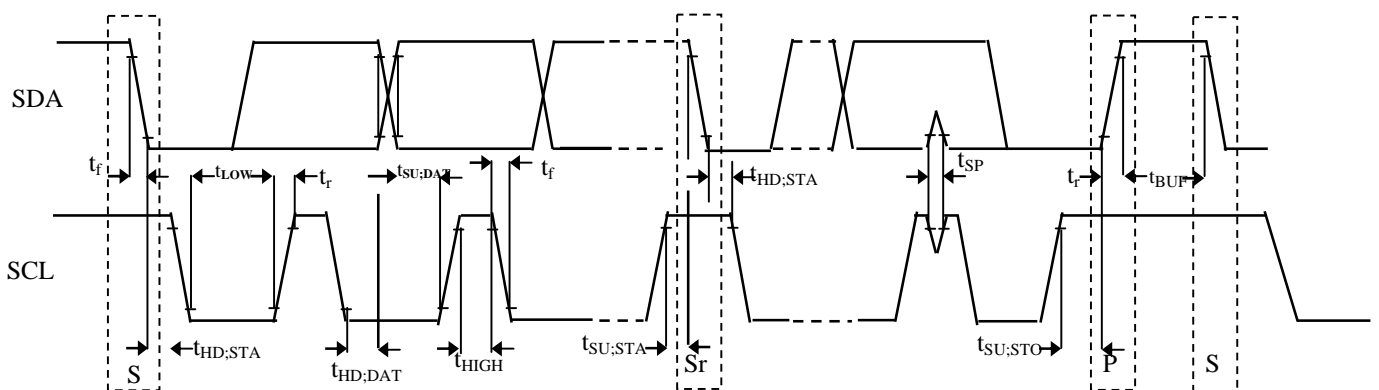
<sup>4</sup> This is the magnitude of external field that can be tolerated without changing the sensor characteristics. If the disturbing field exceeds, a SET/RESET operation is required to restore proper sensor operation

### DIGITAL INTERFACE (VIO = 1.8V)

Symbol	Parameter (Units)	Minimum	Typical	Maximum
V <sub>IH</sub>	High Level Input Voltage (Volts)	0.7*V <sub>IO</sub>		
V <sub>IL</sub>	Low Level Input Voltage (Volts)			0.3*V <sub>IO</sub>
V <sub>HYS</sub>	Hysteresis of Schmitt Trigger Input (Volts)	0.1		
I <sub>IL</sub>	Input Leakage, All Inputs (uA)	-10		10
V <sub>OH</sub>	High Level Output Voltage (Volts)	0.8*V <sub>IO</sub>		
V <sub>OL</sub>	Low Level Output Voltage (Volts)			0.2*V <sub>IO</sub>

### I<sup>2</sup>C INTERFACE I/O CHARACTERISTICS (VIO=1.8V)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Logic Input Low Level	V <sub>IL</sub>		-0.5		0.3* V <sub>IO</sub>	V
Logic Input High Level	V <sub>IH</sub>		0.7*V <sub>IO</sub>		V <sub>IO</sub>	V
Hysteresis of Schmittinput	V <sub>hys</sub>		0.2			V
Logic Output Low Level	V <sub>OL</sub>				0.4	V
Input Leakage Current	I <sub>i</sub>	0.1V <sub>IO</sub> <V <sub>in</sub> <0.9V <sub>IO</sub>	-10		10	μA
SCL Clock Frequency	f <sub>SCL</sub>		0		400	kHz
START Hold Time	t <sub>HD:STA</sub>		0.6			μS
START Setup Time	t <sub>SU:STA</sub>		0.6			μS
LOW period of SCL	t <sub>LOW</sub>		1.3			μS
HIGH period of SCL	t <sub>HIGH</sub>		0.6			μS
Data Hold Time	t <sub>HD:DAT</sub>		0		0.9	μS
Data Setup Time	t <sub>SU:DAT</sub>		0.1			μS
Rise Time	t <sub>r</sub>	From V <sub>IL</sub> to V <sub>IH</sub>			0.3	μS
Fall Time	t <sub>f</sub>	From V <sub>IH</sub> to V <sub>IL</sub>			0.3	μS
Bus Free Time Between STOP and START	t <sub>BUF</sub>		1.3			μS
STOP Setup Time	t <sub>SU:STO</sub>		0.6			μS



Timing Definition

**ABSOLUTE MAXIMUM RATINGS\***

Supply Voltage-0.5 to +5V  
 Storage Temperature -55°C to +125°C  
 Maximum Exposed Field 10000 G

**\*Note:** Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; the functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device's reliability.

**Pin Description: LGA Package**

Pin	Name	Description	I/O
1	SCL	Serial Clock Line for I <sup>2</sup> C bus	I
2	VDD	Power Supply	P
3	NC	Not Connected	NC
4	NC	Not Connected	NC
5	NC	Not Connected	NC
6	NC	Not Connected	NC
7	NC	Not Connected	NC
8	NC	Not Connected	NC
9	GND	Connect to Ground	P
10	CAP	Connect a 10uF capacitor for SET/RESET	P
11	GND	Connect to Ground	P
12	NC	Not Connected	NC
13	VDDIO	Power supply for I <sup>2</sup> C and INT	P
14	NC	Not Connected	NC
15	INT	Interrupt Output	O
16	SDA	Serial Data Line for I <sup>2</sup> C bus	I/O

All parts are shipped in tape and reel packaging with 5000pcs per 13" reel.

**Caution:**

This is an Electro-Static Discharge (ESD) sensitive device.

**Ordering Guide:**

MMC5883MA

Package Type:

Code	Type
A	LGA16 RoHS compliant

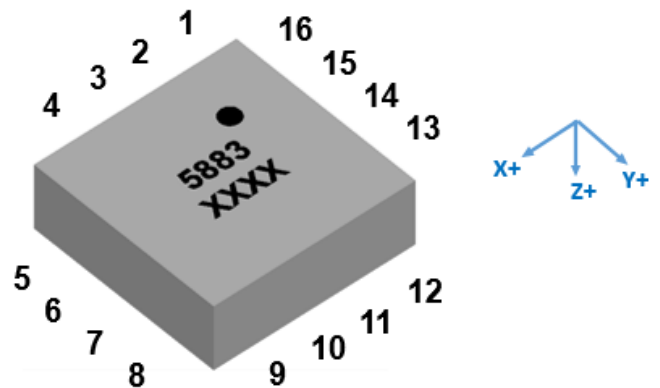
Performance Grade:

Code	Performance Grade
M	Temp Compensated

I<sup>2</sup>C Address

Code	7bits address
3	0110000

**Marking illustration:**



**Note:** Coordinates shown above indicate polarity after a SET operation.

The black dot on top-right marks the location of pin one (1). The 1<sup>st</sup> line represents the device's name. The 2<sup>nd</sup> line represents the device's lot number.

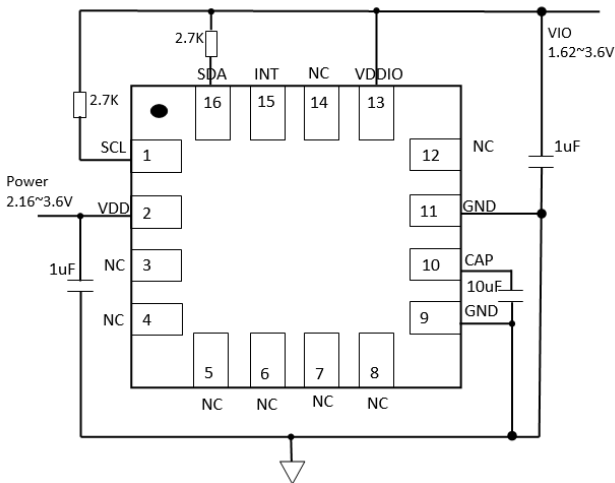
## THEORY OF OPERATION

The Anisotropic Magneto-Resistive (AMR) sensors are special resistors made of permalloy thin film deposited on a silicon wafer. During manufacturing, a strong magnetic field is applied to the film to orient its magnetic domains in the same direction, establishing a magnetization vector. Subsequently, an external magnetic field applied perpendicularly to the sides of the film causes the magnetization to rotate and change angle. This effect causes the film's resistance to vary with the intensity of the applied magnetic field. The MEMSIC AMR sensor is incorporated into a Wheatstone bridge configuration to maximize signal to Noise ratio. A change in magnetic field produces a proportional change in differential voltage across the Wheatstone bridge.

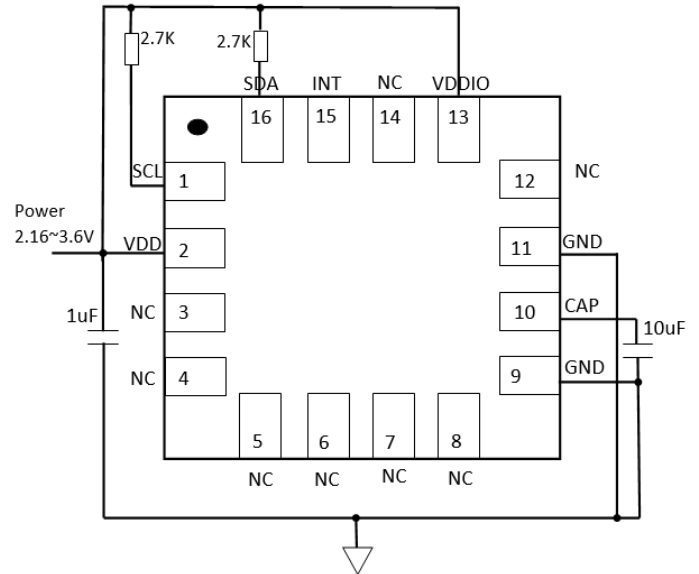
However, the influence of a strong magnetic field (more than 10 G) in any direction could upset, or flip, the polarity of the film, thus changing the sensor characteristics. A strong restoring magnetic field must be applied momentarily to restore, or set, the sensor characteristics. The MEMSIC magnetic sensor has an on-chip magnetically coupled strap: a SET/RESET strap pulsed with a high current, to provide the restoring magnetic field.

## EXTERNAL CIRCUITRY CONNECTION

The MMC5883MA was designed for flexible power connections to support existing and new designs. It can operate from a single 2.16V to 3.6V supply or from dual supplies using a 2.16V to 3.6V VDD supply and a 1.62V to 3.6V VDDIO supply. The circuit connection diagrams below illustrate power supply connection options.



<Top View>  
Dual Supply Connection



<Top View>  
Single Supply Connection

## PIN DESCRIPTIONS

**CAP** – This is the supply input for SET/RESET coil. MEMSIC recommends a minimum 10 uF low ESR capacitor be placed in close proximity to the VCAP pin.

**GND** – This is the ground pin for the magnetic sensor.

**SDA** – This pin is the I<sup>2</sup>C serial data line, and operates in FAST (400 KHz) mode.

**SCL** – This pin is the I<sup>2</sup>C serial clock line, and operates in FAST (400 KHz) mode.

**VDD** – This pin is the power supply of the ASIC.

**VDDIO** – This pin is the power supply of the digital interface of the ASIC.

**INT** – Interrupt output. High effective. This pin is held as hi-Z until an interrupt function is enabled.

## HARDWARE DESIGN CONSIDERATIONS

- ✓ Provide adequate separation distance to devices that contain permanent magnets or generate magnetic fields (e.g. speakers, coils, inductors). The combined magnetic field to be measured and interference magnetic field should be less than the full scale range of the MMC5883MA.
- ✓ Provide adequate separation distance to current carrying traces. Do not route current carrying traces under the sensor or on the other side of the PCB opposite to the device.
- ✓ Do not cover the sensor with magnetized material or materials that may become magnetized, (e.g., shield box, LCD, battery, iron bearing materials).
- ✓ Do not place the device opposite to magnetized material or materials that may become magnetized located on the other side of the PCB.

Please refer to MEMSIC magnetic sensor layout guideline.

## POWER CONSUMPTION

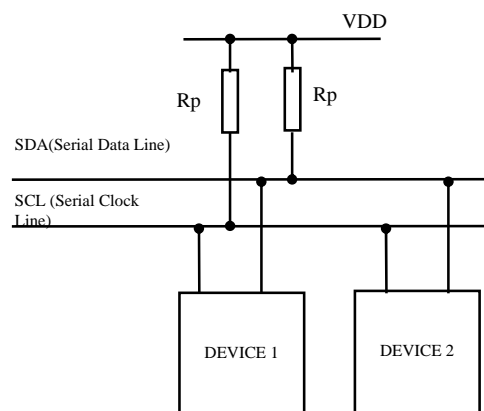
The power consumed by the device is proportional to the number of measurements taken per second. For example, when  $BW<1:0>=00$ , that is, 16-bit mode with 10ms per measurement, MMC5883MA consumes 120  $\mu$ A (typical) at 1.8 V with 7 measurements per second. If only 2 measurements are performed per second, the current will be  $120 \cdot 2/7 = 34 \mu$ A.

## I<sup>2</sup>C INTERFACE DESCRIPTION

A slave mode I<sup>2</sup>C circuit has been implemented into the MEMSIC magnetic sensor as a standard interface for customer applications. The A/D converter functionality has been added to the MEMSIC sensor, thereby increasing ease-of-use, and lowering power consumption, footprint and total solution cost.

The I<sup>2</sup>C (or Inter IC bus) is an industry standard bi-directional two-wire interface bus. A master I<sup>2</sup>C device can operate READ/WRITE and controls up to 128 devices by device addressing. The MEMSIC magnetic sensor operates only in a slave mode, i.e. only responding to calls to its address by a Master device.

## I<sup>2</sup>C BUS CHARACTERISTICS



The two wires in the I<sup>2</sup>C bus are called SDA (serial data line) and SCL (serial clock line). In order for a data transfer to start, the bus has to be free, which is defined by both wires in a HIGH output state. Due to the open-drain / pull-up resistor structure and wired Boolean “AND” operation, any device on the bus can pull lines low and overwrite a HIGH signal. The data on the SDA line has to be stable during the HIGH period of the SCL line. In other words, valid data can only change when the SCL line is LOW.

Note: R<sub>p</sub> selection guide: 4.7k ohm for a short I<sup>2</sup>C bus length (less than 10 cm), and 10k ohm for a bus length less than 5 cm.

## REGISTER MAP

Register Name	Address	Description
Xout Low	00H	Xout LSB
Xout High	01H	Xout MSB
Yout Low	02H	Yout LSB
Yout High	03H	Yout MSB
Zout Low	04H	Zout LSB
Zout High	05H	Zout MSB
Temperature	06H	Temperature output
Status	07H	Device status
Internal control 0	08H	Control register 0
Internal control 1	09H	Control register 1
Internal control 2	0AH	Control register 2
X Threshold	0BH	Motion detection threshold of X
Y Threshold	0CH	Motion detection threshold of Y
Z Threshold	0DH	Motion detection threshold of Z
Product ID 1	2FH	Product ID

## REGISTER DETAILS

### Xout High, Xout Low

Xout Low	7	6	5	4	3	2	1	0
Addr: 00H	Xout[7:0]							
Mode	R							

Xout High	7	6	5	4	3	2	1	0
Addr: 01H	Xout[15:8]							
Mode	R							

X-axis output, unsigned format.

### Yout High, Yout Low

Yout Low	7	6	5	4	3	2	1	0
Addr: 02H	Yout[7:0]							
Mode	R							

Yout High	7	6	5	4	3	2	1	0
Addr: 03H	Yout[15:8]							
Mode	R							

Y-axis output, unsigned format.

### Zout High, Zout Low

Zout Low	7	6	5	4	3	2	1	0
Addr: 04H	Zout[7:0]							
Mode	R							

Zout High	7	6	5	4	3	2	1	0
Addr: 05H	Zout[15:8]							
Mode	R							

Z-axis output, unsigned format.

### Temperature Out

Temperature	7	6	5	4	3	2	1	0
Addr: 06H	Tout[7:0]							
Mode	R							

Temperature output, unsigned format. The range is -75~125°C, about 0.7°C/LSB, 00000000 stands for -75°C.

**Status**

Device Status	7	6	5	4	3	2	1	0
Addr: 07H	Reserved			OTP_Rd_Done	Pump On	Motion Detected	Meas_T_Done	Meas_M_Done
Reset Value	0	0	0	0	0	0	0	0
Mode	R/W							

Bit Name	Description
Meas_M_Done	Indicates a measurement event of magnetic field is completed. This bit should be checked before reading the output. When the new measurement command is occurred, this bit turns to "0". When the measurement is finished, this bit will remain "1" till next measurement. Writing 1 into this bit will clear the corresponding interrupt.
Meas_T_Done	Indicates a measurement event of temperature is completed. When the new measurement command is occurred, this bit turns to "0". When the measurement is finished, this bit will remain "1" till next measurement. Writing 1 into this bit will clear the corresponding interrupt.
Motion Detected	Indicates a motion is detected. Writing 1 into this bit will clear the corresponding interrupt.
Pump On	Indicates the charge pump status.
OTP_Rd_Done	Indicates the chip was able to successfully read its memory.

**Internal Control 0**

Control Register 0	7	6	5	4	3	2	1	0
Addr: 08H	Test_Pin_Sel	OTP Read	Reserved	Reset	Set	Start_MD_T	TM_T	TM_M
Reset Value	0	0	0	0	0	0	0	0
Mode	W	W	W	W	W	W	W	W

Bit Name	Description
TM_M	Take magnetic field measurement, set '1' will initiate measurement. This bit will be automatically reset to 0.
TM_T	Take Temperature measurement, set '1' will initiate measurement. This bit will be automatically reset to 0. Need work respectively with TM_M.
Start_MDT	Start the motion detector. Set "1" will perform an initial magnetic measurement and use this number as the baseline, then the magnetic measurements are done periodically, and compare the new data with the baseline. If the difference exceeds the threshold, the bit "Motion Detected" in 07H is set to "1". The frequency of the periodic measurements is set by CM_Freq in 0AH, and the thresholds are set in 0BH, 0CH, and 0DH. Once a motion is detected, this bit is reset to 0.
Set	Writing "1" will SET the sensor by passing a large current through SET/RESET Coil
Reset	Writing "1" will RESET the sensor by passing a large current through SET/RESET Coil in a reversed direction
Reserved	This bit should NEVER be written to 1
OTP Read	Writing "1" will let the device to read the OTP data again. This bit will be automatically reset to 0.
Test_Pin_Sel	Factory-used bit.



**Internal Control 1**

Control Register 1	7	6	5	4	3	2	1	0
Addr: 09H	SW_RST	OTP_BL_SEL1	OTP_BL_SEL0	Z-inhibit	Y-inhibit	X-inhibit	BW1	BW0
Reset Value	0	0	0	0	0	0	0	0
Mode	W	W	W	W	W	W	W	W
Bit Name	Description							
BW0& BW1	Output resolution							
	BW1	BW0	Output Resolution	Measurement Time	ODR			
	0	0	16bits	10ms	100Hz			
	0	1	16bits	5ms	200Hz			
	1	0	16bits	2.5ms	400Hz			
	1	1	16 bits	1.6ms	600Hz			
Note: X/Y/Z channel measurements are taken sequentially. Delay Time among those measurements is 1/3 of the Measurement Time defined in the table.								
X-inhibit	Writing "1" will disable this channel, and reduce Measurement Time and total charge per measurement.							
Y-inhibit								
Z-inhibit								
OTP_BL_SEL0 & OTP_BL_SEL1	Factory-use bits.							
SW_RST	Writing "1" will cause the part to reset, similar to power-up. It will clear all registers and also re-read OTP as part of its startup routine. The power on time is 5mS.							

**Internal Control 2**

Control Register 2	7	6	5	4	3	2	1	0
Addr: 0AH	ULP_SEL	INT_Meas_Done_EN	INT_MDT_EN	Force_Vddl_Stby	CM_Freq3	CM_Freq2	CM_Freq1	CM_Freq0
Reset Value	0	0	0	0	0	0	0	0
Mode	W	W	W	W	W	W	W	W

Bit Name	Description	
CM_Freq[3:0]	These bits determine how often the chip will take measurements in Continuous Measurement Mode. The frequency is based on the assumption that BW[1:0] = 00.	
	CM_Freq [3:0]	
	0000	Frequency (Typical) Continuous Measurement Mode is off. Motion detector does not work in this case.
	0001	14Hz
	0010	5 Hz
	0011	2.2Hz
	0100	1 Hz
	0101	1/2 Hz
	0110	1/4 Hz
	0111	1/8 Hz
	1000	1/16 Hz
	1001	1/32 Hz
	1010	1/64 Hz
1011 and above	Reserved and not used.	
Force_Vddl_Stby	Factory-use bit.	
INT_MDT_EN	Writing "1" will enable the interrupt when a motion is detected by the internal motion detector.	
INT_Meas_Done_EN	Writing "1" will enable the interrupt when a magnetic or temperature measurement even is completed.	

ULP_SEL	Factory-use bit.
---------	------------------

**X Threshold**

<b>X Threshold</b>	7	6	5	4	3	2	1	0
Addr: 0BH	X_Mag_TH[7:0]							
Reset Value	0							
Mode	W	W	W	W	W	W	W	W

The threshold magnetic field of the motion detector in the x direction, with unsigned format. The step size is 1mG, and maximum threshold is 0.25G.

**Y Threshold**

<b>Y Threshold</b>	7	6	5	4	3	2	1	0
Addr: 0CH	Y_Mag_TH[7:0]							
Reset Value	0							
Mode	W	W	W	W	W	W	W	W

The threshold magnetic field of the motion detector in the y direction, with unsigned format. The step size is 1mG, and maximum threshold is 0.25G.

**Z Threshold**

<b>Z Threshold</b>	7	6	5	4	3	2	1	0
Addr: 0DH	Z_Mag_TH[7:0]							
Reset Value	0							
Mode	W	W	W	W	W	W	W	W

The threshold magnetic field of the motion detector in the z direction, with unsigned format. The step size is 1mG, and maximum threshold is 0.25G.

**Product ID1**

<b>Product ID 1</b>	7	6	5	4	3	2	1	0
Addr: 2FH	Product ID1[7:0]							
Reset Value	0	0	0	0	1	1	0	0
Mode	R	R	R	R	R	R	R	R

## DATA TRANSFER

A data transfer is started with a “START” condition and ended with a “STOP” condition. A “START” condition is defined by a HIGH to LOW transition on the SDA line while SCL line is HIGH. A “STOP” condition is defined by a LOW to HIGH transition on the SDA line while the SCL line is held HIGH. All data transfer in I<sup>2</sup>C system are 8-bits long. Each byte has to be followed by an acknowledge bit. Each data transfer involves a total of 9 clock cycles. Data is transferred starting with the most significant bit (MSB).

After a START condition, the Master device calls a specific slave device by sending its 7-bit address with the 8<sup>th</sup> bit (LSB) indicating that either a READ or WRITE operation will follow, [1] for READ and [0] for WRITE. The MEMSIC device 7-bit device address is [0110000].

The initial addressing of the slave is always followed by the master writing the number of the slave register to be read or written, so this initial addressing always indicates a WRITE operation by sending [01100000]. After being addressed, the MEMSIC device being called should respond by an “Acknowledge” signal by pulling SDA line LOW. Subsequent communication bytes can either be

- a) The data to be written to the device register, or
- b) Another START condition followed by the device address indicating a READ operation [01100001], and then the master reads the register data.

Multiple data bytes can be written or read to numerically sequential registers without the need of another START condition. Data transfer is terminated by a STOP condition or another START condition. Two detailed examples of communicating with the MEMSIC device are listed below for the actions of acquiring a magnetic field measurement and magnetizing the sensor.

## EXAMPLE MEASUREMENT

**1<sup>st</sup> cycle:** A START condition is established by the Master device followed by a call to the slave address [0110000] with the eighth bit held low to indicate a WRITE request.

**2<sup>nd</sup> cycle:** After an acknowledge signal is received by the Master device (MEMSIC device pulls SDA line low during 9<sup>th</sup> SCL pulse), the Master device sends the address of Control Register 0 or [00001000] as the target register to be written. The MEMSIC device should acknowledge receipt of the address (9<sup>th</sup> SCL pulse, SDA pulled low).

**3<sup>rd</sup> cycle:** The Master device writes to the Internal Control Register 0 the code [00000001] (TM\_M high) to initiate data acquisition. The MEMSIC device should send an Acknowledge and internally initiate a measurement (collect x, y and z data). A STOP condition indicates the end of the write operation.

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**4<sup>th</sup> cycle:** The Master device sends a START command followed by the MEMSIC device’s seven bit address, and finally the eighth bit set low to indicate a WRITE. An Acknowledge should be send by the MEMSIC device in response.

**5<sup>th</sup> cycle:** The Master device sends the MEMSIC device’s Status Register [00000111] as the address to read.

**6<sup>th</sup> cycle:** The Master device sends a START command followed by the MEMSIC device’s seven bit address, and finally the eighth bit set high to indicate a READ. An Acknowledge should be send by the MEMSIC device in response.

**7<sup>th</sup> cycle:** The Master device cycles the SCL line. This causes the Status Register data to appear on SDA line. Continuously read the Status Register until the Meas\_M\_Done bit (bit 0) is set to ‘1’.or check INT pin status. This indicates that data for the x, y, and z sensors is available to be read.

**8<sup>th</sup> cycle:** The Master device sends a START command followed by the MEMSIC device’s seven bit address, and finally the eighth bit set low to indicate a WRITE. An Acknowledge should be send by the MEMSIC device in response.

**9<sup>th</sup> cycle:** The Master device sends a [00000000] (Xout LSB register address) as the register address to read.

**10<sup>th</sup> cycle:** The Master device calls the MEMSIC device’s address with a READ (8<sup>th</sup> SCL cycle SDA line high). An Acknowledge should be send by the MEMSIC device in response.

**11<sup>th</sup> cycle:** Master device continues to cycle the SCL line, and each consecutive byte of data from the X, Y and Z registers should appear on the SDA line. The internal memory address pointer automatically moves to the next byte. The Master device acknowledges each. Thus:

**12<sup>th</sup> cycle:** LSB of X channel.

**13<sup>th</sup> cycle:** MSB of X channel.

**14<sup>th</sup> cycle:** LSB of Y channel.

**15<sup>th</sup> cycle:** MSB of Y channel.

**16<sup>th</sup> cycle:** LSB of Z channel.

**17<sup>th</sup> cycle:** MSB of Z channel.

Master ends communications by NOT sending an ‘Acknowledge’ and also follows with a ‘STOP’ command.

### EXAMPLE OF SET

**1<sup>st</sup> cycle:** A START condition is established by the Master device followed by a call to the slave address [0110000] with the eighth bit held low to indicate a WRITE request.

**2<sup>nd</sup> cycle:** After an acknowledge signal is received by the Master device (The MEMSIC device pulls the SDA line low during the 9<sup>th</sup> SCL pulse), the Master device sends [00001000] as the target address (Internal Control Register 0). The MEMSIC device should acknowledge receipt of the address (9<sup>th</sup> SCL pulse).

**3<sup>rd</sup> cycle:** The Master device writes to the MEMSIC device's Internal Control 0 register the code [00001000] (SET bit) to initiate a SET action. The MEMSIC device should send an Acknowledge.

### EXAMPLE OF RESET

**1<sup>st</sup> cycle:** A START condition is established by the Master device followed by a call to the slave address [0110000] with the eighth bit held low to indicate a WRITE request

**2<sup>nd</sup> cycle:** After an acknowledge signal is received by the Master device (The MEMSIC device pulls the SDA line low during the 9<sup>th</sup> SCL pulse), the Master device sends [00001000] as the target address (Internal Control Register 0). The MEMSIC device should acknowledge receipt of the address (9<sup>th</sup> SCL pulse).

**3<sup>rd</sup> cycle:** The Master device writes to the MEMSIC device's Internal Control 0 register the code [00010000] (RESET bit) to initiate a RESET action. The MEMSIC device should send an Acknowledge.

At this point, the MEMSIC AMR sensors have been conditioned for optimum performance and data measurements can commence.

**Note:** The RESET action can be skipped for most applications

### USING SET AND RESET TO REMOVE BRIDGE OFFSET

The integrated SET and RESET functions of the MMC5883MA enables the user to remove the error associated with bridge Offset change as a function of temperature, thereby enabling more precise heading measurements over a wider temperature than competitive technologies. The SET and RESET functions effectively alternately flip the magnetic sensing polarity of the sensing elements of the device.

1) The most accurate magnetic field measurements can be obtained by using the protocol described as follows: Perform SET. This sets the internal magnetization of the sensing resistors in the direction of the SET field.

2) Perform MEASUREMENT. This measurement will contain not only the sensors response to the external magnetic field, H, but also the Offset; in other words,

**Output1 = +H + Offset.**

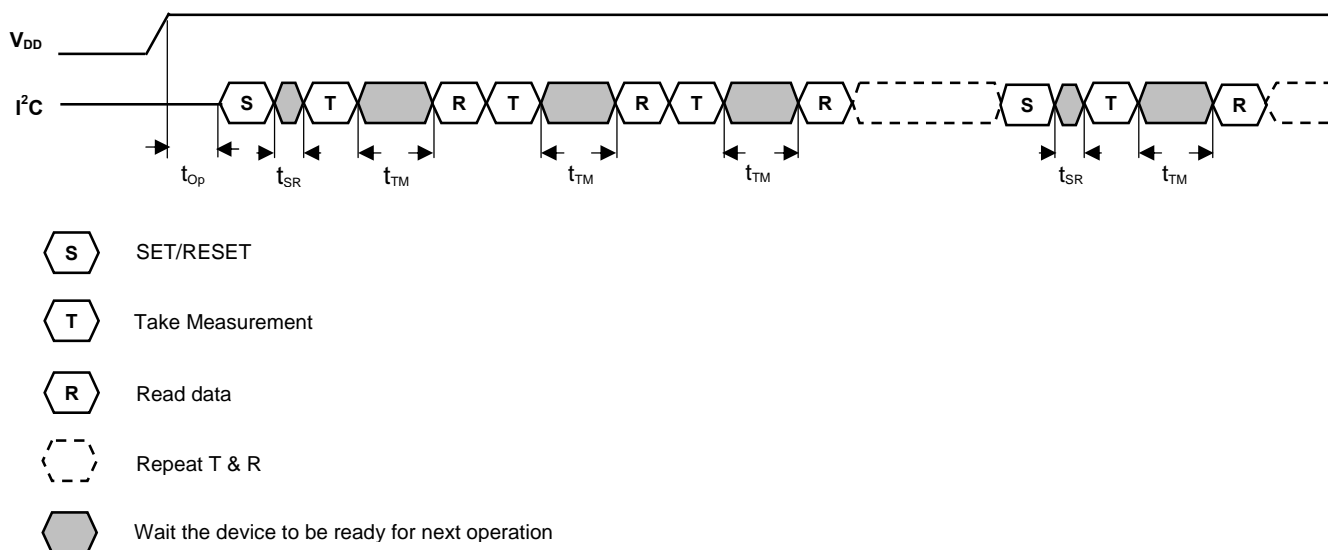
3) Perform RESET. This resets the internal magnetization of the sensing resistors in the direction of the RESET field, which is opposite to the SET field (180° opposed).

4) Perform MEASUREMENT. This measurement will contain both the sensors response to the external field and also the Offset. In other words, **Output2 = -H + Offset.**

5) Finally, calculate H by subtracting the two measurements and dividing by 2. This procedure effectively eliminates the Offset from the measurement and therefore any changes in the Offset over temperature.

**Note:** To calculate and store the offset; add the two measurements and divide by 2. This calculated offset value can be subtracted from subsequent measurements to obtain H directly from each measurement.

## OPERATING TIMING

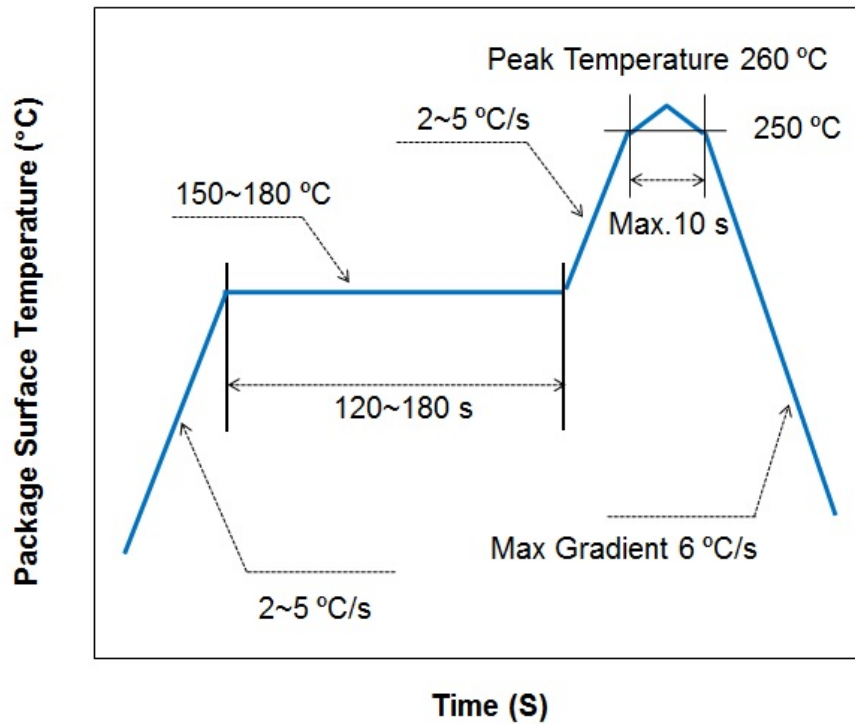


Operating Timing Diagram

Parameter	Symbol	Min.	Max.	Unit
Time to operate device after $V_{DD}$ valid	$t_{Op}$	5		ms
Minimum wait time after SET or RESET	$t_{SR}$	1		ms
Wait time to complete measurement	$t_{TM}$ BW=00	10		ms
	$t_{TM}$ BW=01	5		ms
	$t_{TM}$ BW=10	2.5		ms
	$t_{TM}$ BW=11	1.6		ms

### SOLDERING RECOMMENDATIONS

MEMSIC magnetic sensor is capable of withstanding an MSL3 / 260°C solder reflow. Following is the reflow profile:



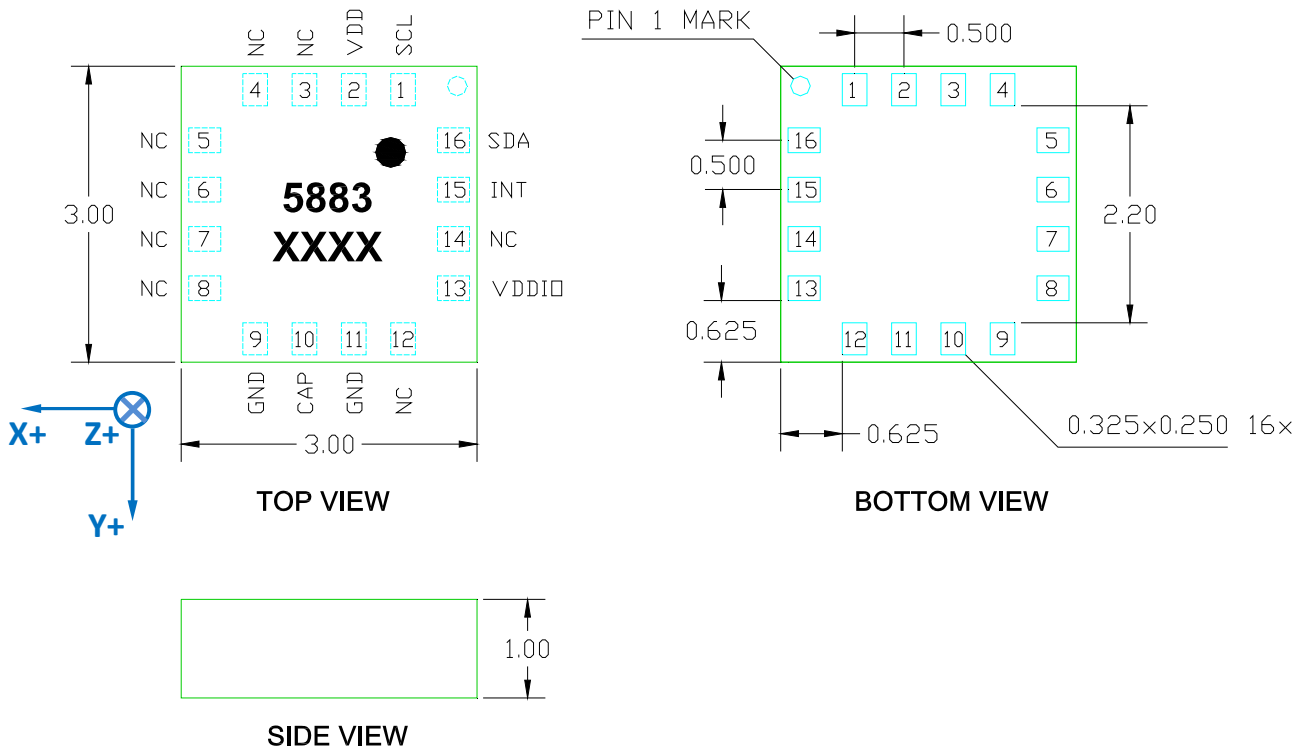
**Note:**

- The second reflow cycle should be applied after device has been cooled down to 25°C (room temperature)
- This is the reflow profile for Pb free process
- The peak temperature on the sensor surface should be limited under 260°C for no more than 10 seconds.
- Reflow conditions recommended by solder paste should be followed to get the best SMT quality.

If the part is mounted manually, please ensure the temperature does not exceed 260°C for 10 seconds.

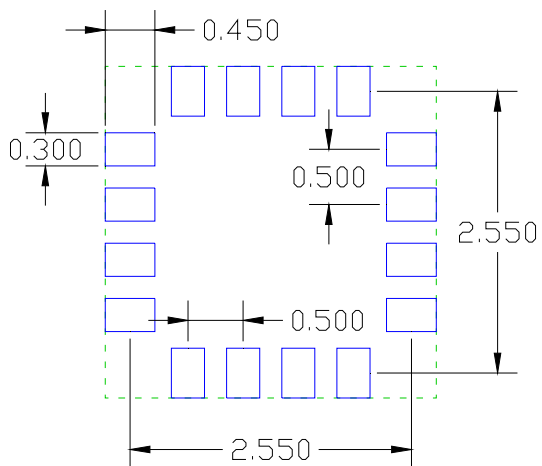
# PACKAGE DRAWING (LGA package)

Unit: mm



## LAND PATTERN

Unit: mm





Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

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- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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