**Product data sheet** 

# 1 Product profile

### 1.1 General description

Two planar PIN diodes in common anode configuration in an SOT323 small SMD plastic package.

### 1.2 Features and benefits

- Two elements in common anode configuration in a small SMD plastic package
- · Low diode capacitance
- · Low diode forward resistance
- AEC-Q101 qualified

### 1.3 Applications

· General RF applications

# 2 Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol
1	cathode 1		
2	cathode 2	3	_
3	common cathode	1 2 sot323_so	2 1 aaa-029922

# 3 Ordering information

**Table 2. Ordering information** 

- abio 21 Oracinig information						
Type number	Package	Package				
	Name	Description	Version			
BAP51-06W	-	plastic surface-mounted package; 3 leads	SOT323			



**BAP51-06W** 

Silicon PIN diode

# 4 Marking

Table 3. Marking

Type number	Marking code	Description
BAP51-06W	W7%	% = p: made in Hong Kong
		% = t: made in Malaysia

# 5 Limiting values

### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage		-	50	V
I <sub>F</sub>	forward current		-	50	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 90 °C	-	240	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

## 6 Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Тур	Unit
(J OP)	thermal resistance from junction to solder point		250	K/W

## 7 Characteristics

### **Table 6. Characteristics**

 $T_i$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
$V_{F}$	forward voltage	I <sub>F</sub> = 50 mA	-	0.95	1.1	V		
I <sub>R</sub>	reverse current	V <sub>R</sub> = 50 V	-	-	100	nA		
C <sub>d</sub>	diode capacitance	f = 1 MHz (see Figure 1)						
		V <sub>R</sub> = 0 V	-	0.4	-	pF		
		V <sub>R</sub> = 1 V	-	0.3	0.55	pF		
		V <sub>R</sub> = 5 V	-	0.2	0.35	pF		
$r_D$	diode forward resistance	f = 100 MHz (see Figure 2)						
		I <sub>F</sub> = 0.5 mA	[1] _	5.3	9	Ω		
		I <sub>F</sub> = 1 mA	[1] _	3.5	6.5	Ω		
		I <sub>F</sub> = 10 mA	[1] _	1.5	2.5	Ω		
ISL	isolation	V <sub>R</sub> = 0 V (see <u>Figure 4</u> )				-		
		f = 900 MHz	-	17	-	dB		
		f = 1800 MHz	-	13	-	dB		
		f = 2450 MHz	-	12	-	dB		
L <sub>ins</sub>	insertion loss	I <sub>F</sub> = 0.5 mA (see <u>Figure 3</u> )	I <sub>F</sub> = 0.5 mA (see <u>Figure 3</u> )					
		f = 900 MHz	-	0.44	-	dB		
		f = 1800 MHz	-	0.50	-	dB		
		f = 2450 MHz	-	0.54	-	dB		
		I <sub>F</sub> = 1 mA						
		f = 900 MHz	-	0.33	-	dB		
		f = 1800 MHz	-	0.39	-	dB		
		f = 2450 MHz	-	0.43	-	dB		
		I <sub>F</sub> = 10 mA						
		f = 900 MHz	-	0.19	-	dB		
		f = 1800 MHz	-	0.24	-	dB		
		f = 2450 MHz	-	0.28	-	dB		
τι	charge carrier life time	when switched from $I_F$ = 10 mA to $I_R$ = 6 mA; $R_L$ = 100 $\Omega$ ; measured at $I_R$ = 3 mA	-	0.55	-	μs		
L <sub>S</sub>	series inductance	I <sub>F</sub> = 100 mA; f = 100 MHz	-	1.6	-	nH		

<sup>[1]</sup> Guaranteed on AQL basis; inspection level S4, AQL 1.0

# 8 Graphical data

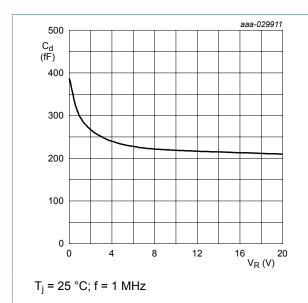
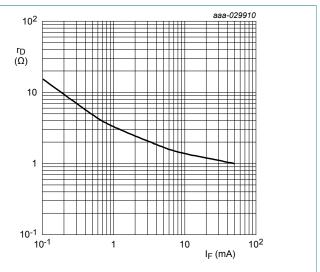
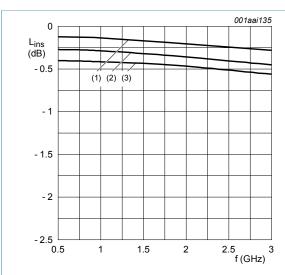


Figure 1. Diode capacitance as a function of reverse voltage (typical values)



 $T_j$  = 25 °C; f = 100 MHz.

Figure 2. Diode forward resistance as a function of forward current (typical values)



Diode inserted in series with a 50  $\Omega$  strip line circuit and biased via the analyzer T-network.

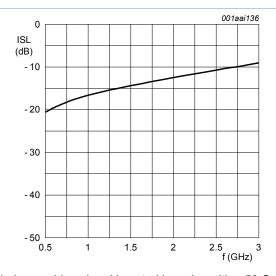
 $T_{amb}$  = 25 °C; f = 1 MHz

(1)  $I_F = 10 \text{ mA}$ 

(2)  $I_F = 1 \text{ mA}$ 

(3)  $I_F = 0.5 \text{ mA}$ 

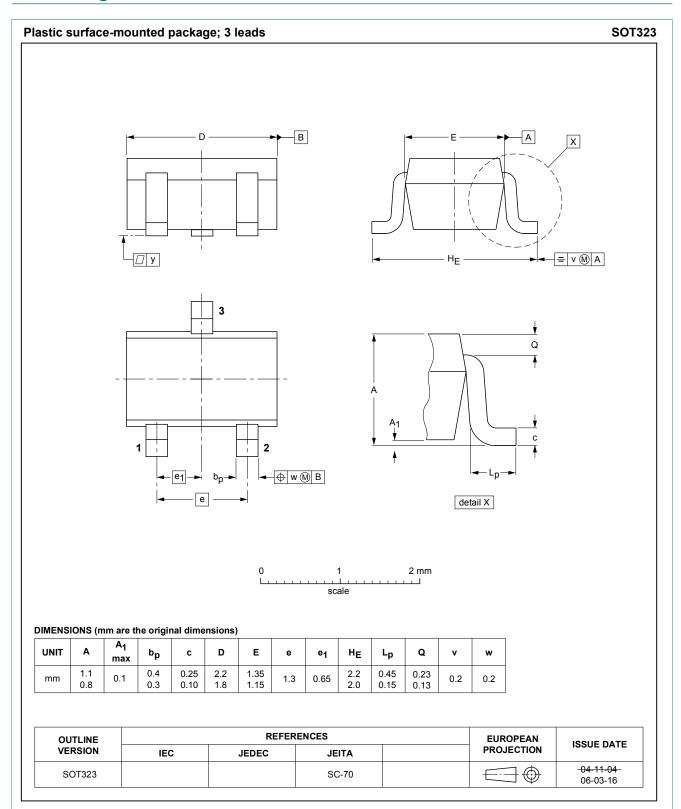
Figure 3. Insertion loss of the diode as a function of frequency (typical values)



Diode zero-biased and inserted in series with a 50  $\Omega$  strip line circuit  $T_{amb}$  = 25  $^{\circ}C$ 

Figure 4. Isolation of the diode as a function of frequency (typical values)

# 9 Package outline



## 10 Abbreviations

#### Table 7. Abbreviations

Acronym	Description
AQL	acceptable quality level
PIN	P-type, intrinsic, N-type
RF	radio frequency
S4	special inspection level 4
SMD	surface-mounted device

# 11 Revision history

### Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BAP51-06W v.2.1	20190208	Product data sheet	-	BAP51-06W v.2
Modifications:	aligned the title of	f the data sheet with the	description on the li	nternet
BAP51-06W v.2	20181126	Product data sheet	-	BAP51-06W v.1
Modifications:	<ul> <li>Section 1.2 "Features and benefits" has been updated.</li> <li>The "Legal information" pages have been updated.</li> </ul>			
BAP51-06W v.1	20080526	Product data sheet	-	-

## 12 Legal information

#### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Silicon PIN diode

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#### Как с нами связаться

**Телефон:** 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: <u>org@eplast1.ru</u>

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.