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December 2013

FDP75N08A

N-Channel UniFET™ MOSFET

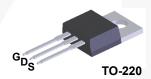
75 V, 75 A, 11 mΩ

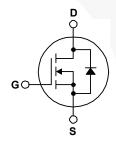
Features

- 75 A, 75 V, $R_{DS(on)}$ = 11 m Ω @ V_{GS} = 10 V Low Gate Charge (Typ. 145 nC)
- Low Crss (Typ. 86 pF)
- · Fast Switching
- · Improved dv/dt Capability

Description

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FDP75N08A	Unit	
V _{DSS}	Drain-Source Voltage		75	V	
I _D	Drain Current - Continuous (T _C = 25°C)		75	Α	
	- Continuous (T _C = 100°C)		47	Α	
I _{DM}	Drain Current - Pulsed	(Note 1)	300	А	
V _{GSS}	Gate-Source Voltage		± 20	V	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1738	mJ	
I _{AR}	Avalanche Current (N		75	A	
E _{AR}	Repetitive Avalanche Energy (Note 1		13.7	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns	
P _D	Power Dissipation (T _C = 25°C)		137	W	
	- Derate Above 25°C		1.09	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C	

Thermal Characteristics

Symbol	Parameter	FDP75N08A	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.91	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP75N08A	FDP75N08A	TO-220	Tube	N/A	50 units

Electrical Characteristics TC = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	75			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C		0.6		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 75 V, V _{GS} = 0 V			1	μА
		V _{DS} = 60 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Charact	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 37.5 A		9.4	11	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 37.5 A		15		S
Dynamic Cl	haracteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		3437	4468	pF
C _{oss}	Output Capacitance	f = 1.0 MHz	\	738	959	pF
C _{rss}	Reverse Transfer Capacitance			86	129	pF
Switching C	Characteristics					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 37.5 \text{ V}, I_D = 75A,$		43	95	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		212	434	ns
t _{d(off)}	Turn-Off Delay Time			273	556	ns
t _f	Turn-Off Fall Time	(Note 4)		147	303	ns
Qg	Total Gate Charge	$V_{DS} = 60 \text{ V}, I_{D} = 75\text{A},$		80	104	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	/	20		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		24		nC
Drain-Source	ce Diode Characteristics and Maximum Ratings	3		I	- 4	
I _S	Maximum Continuous Drain-Source Diode Forward Current				75	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				300	Α
V_{SD}	Drain-Source Diode Forward Voltage V _{GS} = 0 V, I _S = 75 A				1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{S} = 75 \text{ A},$		62		ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$		145		nC

 $[\]begin{tabular}{ll} \textbf{Notes:} \\ 1: & \textbf{Repetitive rating: pulse-width limited by maximum junction temperature.} \\ 2: & \textbf{L} = 206 \ \mu \textbf{H}, \ \textbf{I}_{AS} = 75 \ \textbf{A}, \ \textbf{V}_{DD} = 50 \ \textbf{V}, \ \textbf{R}_{G} = 25 \ \Omega, \ \textbf{starting} \ \textbf{T}_{J} = 25 ^{\circ} \textbf{C}. \\ 3: & \ \textbf{I}_{SD} \le 75 \textbf{A}, \ \textbf{d} \ \textit{i} \ \textit{d} \ \textit{t} \le 200 \ \textbf{A} \ \textit{i} \ \textit{s}, \ \textbf{V}_{DD} \le \textbf{B} \ \textbf{V}_{DS}, \ \ \textbf{starting} \ \textbf{T}_{J} = 25 ^{\circ} \textbf{C}. \\ 4: & \ \textbf{Essentially independent of operating temperature typical characteristics.} \\ \end{tabular}$

Typical Performance Characteristics

Figure 1. On-Region Characteristics

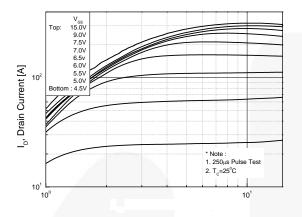


Figure 2. Transfer Characteristics

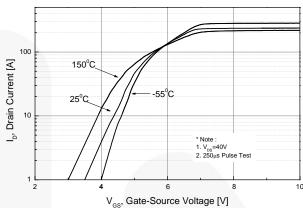
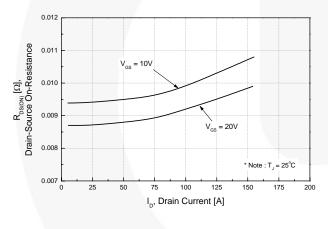


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue



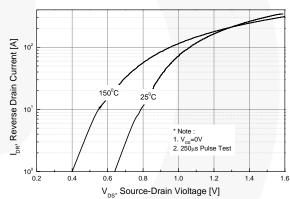


Figure 5. Capacitance Characteristics

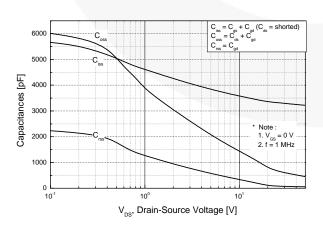
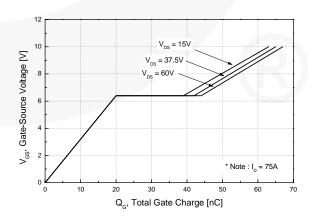
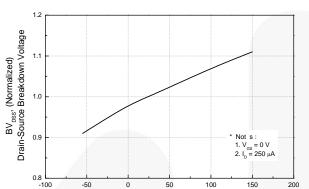


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature



T_,, Junction Temperature [°C]

Figure 8. On-Resistance Variation vs. Temperature

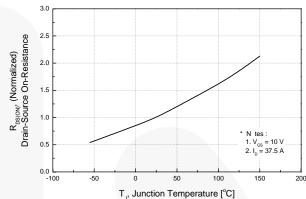


Figure 9. Maximum Safe Operating Area

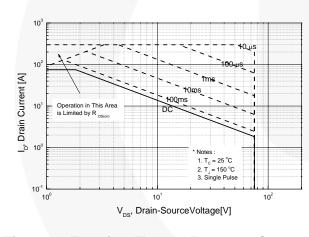


Figure 10. Maximum Drain Current vs. Case Temperature

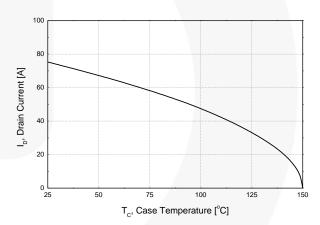


Figure 11. Transient Thermal Response Curve

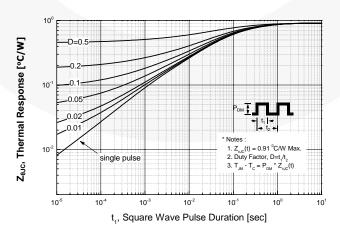


Figure 12. Gate Charge Test Circuit & Waveform

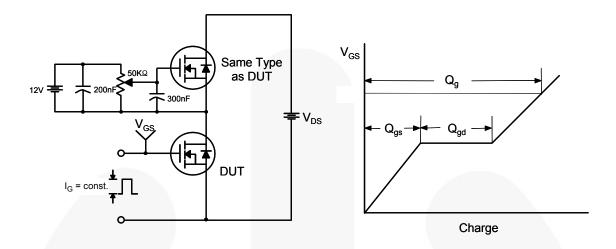


Figure 13. Resistive Switching Test Circuit & Waveforms

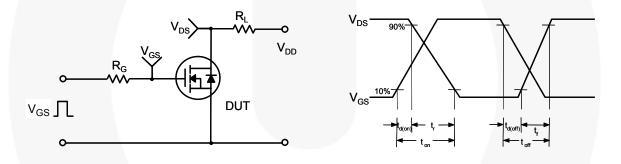
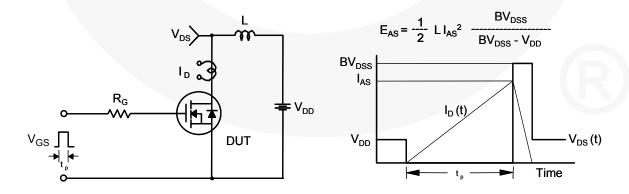


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



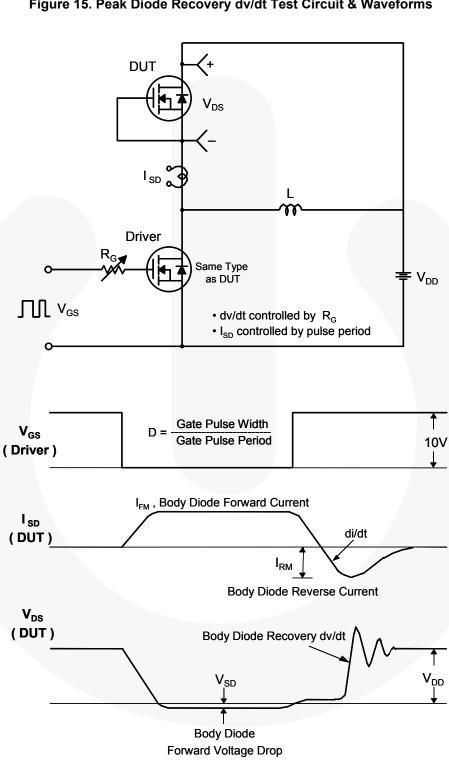


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

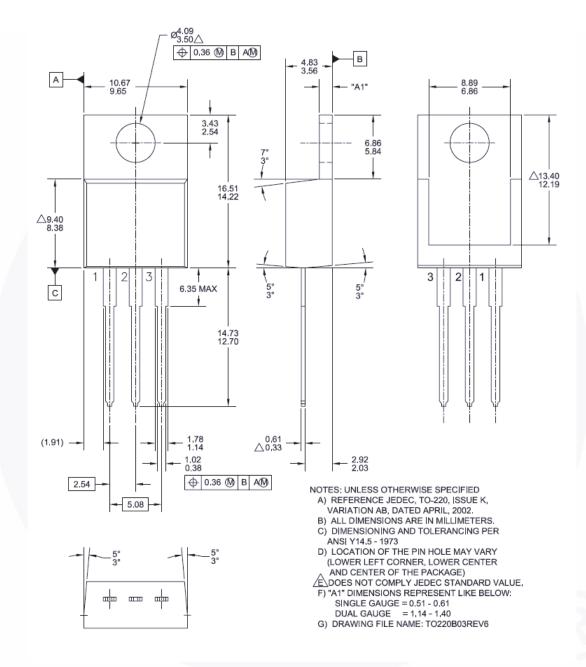


Figure 16. TO-220, Molded, 3Lead, Jedec Variation AB

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Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.