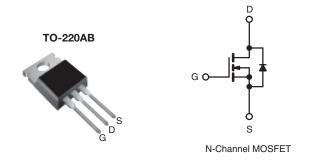


Power MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	60	60			
R _{DS(on)} (Ω)	V _{GS} = 10 V	0.028			
Q _g (Max.) (nC)	67	•			
Q _{gs} (nC)	18	}			
Q _{gd} (nC)	25	25			
Configuration	Sing	Single			



FEATURES

- Dynamic dV/dt Rating
- 175 °C Operating Temperature
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universially preferred for commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION		
Package	TO-220AB	
Load (Dh) from	IRFZ40PbF	
Lead (Pb)-free	SiHFZ40-E3	
SnPb	IRFZ40	
SHED	SiHFZ40	

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	60	V	
Gate-Source Voltage			V_{GS}	± 20	v	
Continuous Drain Current ^e	V _{GS} at 10 V	$T_C = 25 ^{\circ}C$	- I _D	50	А	
Continuous Drain Current		T _C = 100 °C		36		
Pulsed Drain Current ^a			I _{DM}	200		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	100	mJ	
Maximum Power Dissipation	T _C =	: 25 °C	P _D	150	W	
Peak Diode Recovery dV/dt ^c			dV/dt	4.5	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 175	°C	
Soldering Recommendations (Peak Temperature) ^d	for 10 s			300	1 %	
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
				1.1	N⋅m	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. V_{DD} = 25 V, starting T_J = 25 °C, L = 44 μ H, R_g = 25 Ω , I_{AS} = 51 Å (see fig. 12).
- c. $I_{SD} \le 51$ A, $dI/dt \le 250$ A/µs, $V_{DD} \le V_{DS}$, $T_J \le 175$ °C.
- d. 1.6 mm from case.
- e. Current limited by the package, (die current = 51 A).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	62	
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50	-	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	-	1.0	

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} =	60	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	Reference to 25 °C, I _D = 1 mA		0.060	-	V/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu A$		-	4.0	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}		= 60 V, V _{GS} = 0 V , V _{GS} = 0 V, T _J = 125 °C	-	-	25 250	μΑ
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V		-	-	0.028	Ω
Forward Transconductance	9 _{fs}	V_{DS}	= 25 V, I _D = 31 A	15	-	-	S
Dynamic							
Input Capacitance	C _{iss}	$V_{GS} = 0 \text{ V},$ $V_{DS} = 25 \text{ V},$ f = 1.0 MHz, see fig. 5		-	1900	-	pF
Output Capacitance	C _{oss}			-	920	-	
Reverse Transfer Capacitance	C _{rss}			-	170	-	
Total Gate Charge	Qg			-	-	67	nC
Gate-Source Charge	Q_{gs}	V _{GS} = 10 V	$I_D = 51 \text{ A}, V_{DS} = 48 \text{ V},$ see fig. 6 and 13^b	-	-	18	
Gate-Drain Charge	Q _{gd}			-	-	25	
Turn-On Delay Time	t _{d(on)}			-	14	-	
Rise Time	t _r	V_{DD} = 30 V, I_{D} = 51 A, R_{g} = 9.1 Ω , R_{D} = 0.55 Ω , see fig. 10 ^b		-	110	-	ns
Turn-Off Delay Time	t _{d(off)}			-	45	-	
Fall Time	t _f			-	92	-	
Internal Drain Inductance	L_D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	-11
Internal Source Inductance	L _S			-	7.5	-	- nH
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	50	A
Pulsed Diode Forward Current ^a	I _{SM}			-	-	200	^
Body Diode Voltage	V_{SD}	T _J = 25 °C, I _S = 51 A, V _{GS} = 0 V ^b		-	-	2.5	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 51 A, dl/dt = 100 A/μs		-	120	180	ns
Body Diode Reverse Recovery Charge	Q _{rr}			-	0.53	0.80	nC
Forward Turn-On Time	t _{on}	Intrinsic tu	rn-on is dominated by L _S and L _D)			L _D)	

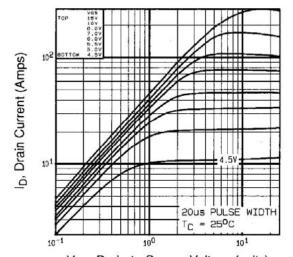
Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width \leq 300 µs; duty cycle \leq 2 %.

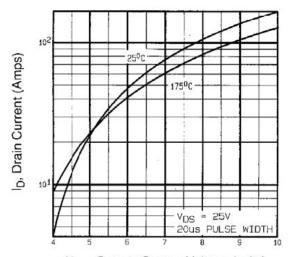




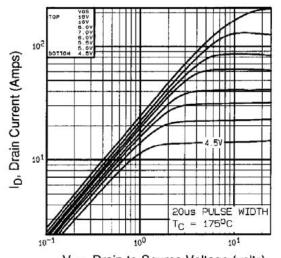
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



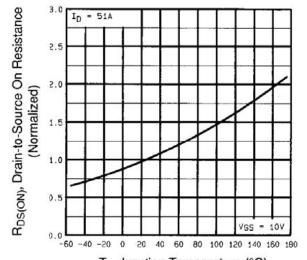
V_{DS}, Drain-to-Source Voltage (volts) Fig. 1 - Typical Output Characteristics, T_C = 25 °C



V_{GS}, Gate-to-Source Voltage (volts) Fig. 1 - Typical Transfer Characteristics



V_{DS}, Drain-to-Source Voltage (volts) Fig. 2 - Typical Output Characteristics, T_C = 175 °C



T_J, Junction Temperature (°C) Fig. 2 - Normalized On-Resistance vs. Temperature



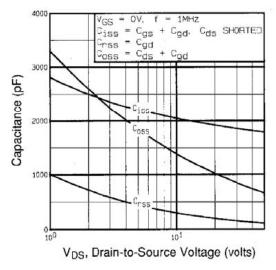


Fig. 3 - Typical Capacitance vs. Drain-to-Source Voltage

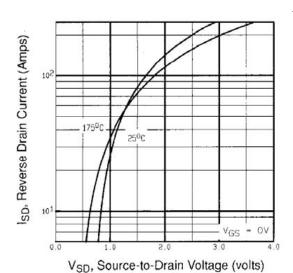


Fig. 5 - Typical Source-Drain Diode Forward Voltage

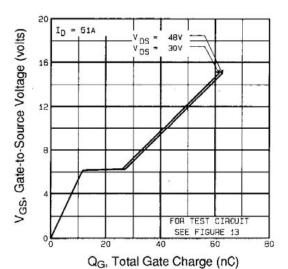


Fig. 4 - Typical Gate Charge vs. Gate-to-Source Voltage

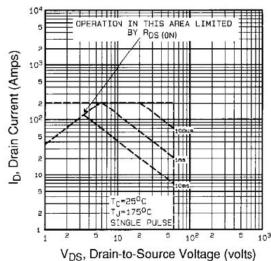


Fig. 3 - Maximum Safe Operating Area





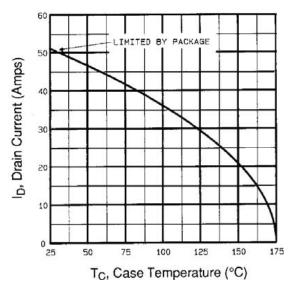


Fig. 9 - Maximum Drain Current vs. Case Temperature

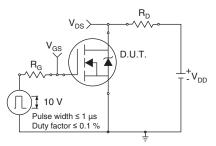


Fig. 10a - Switching Time Test Circuit

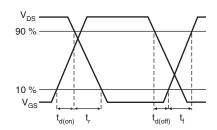


Fig. 10b - Switching Time Waveforms

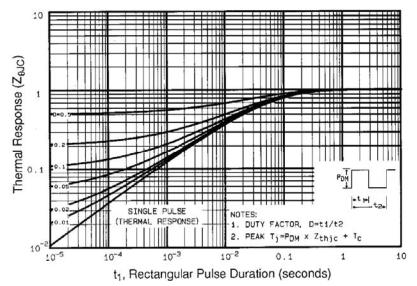


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

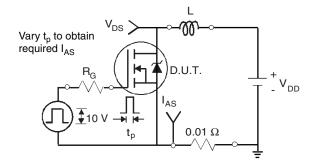


Fig. 12a - Unclamped Inductive Test Circuit

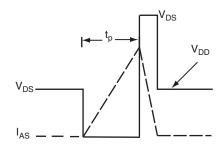


Fig. 12b - Unclamped Inductive Waveforms



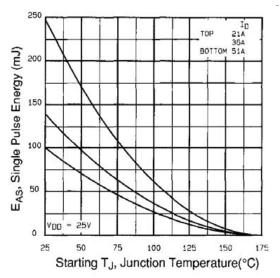


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

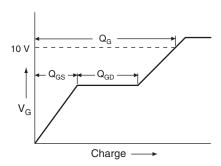


Fig. 13a - Basic Gate Charge Waveform

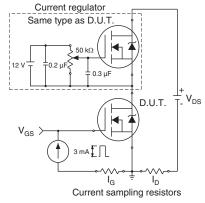
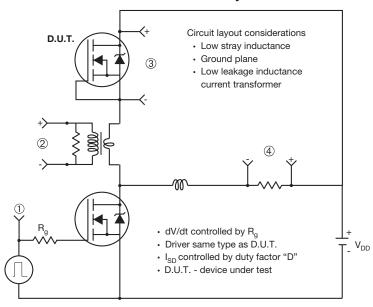


Fig. 13b - Gate Charge Test



Peak Diode Recovery dV/dt Test Circuit



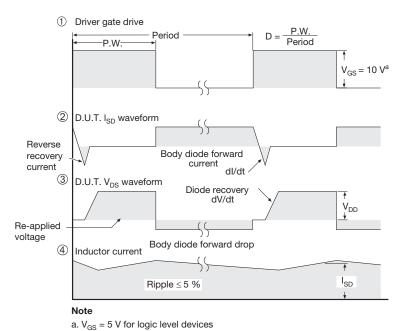


Fig. 14 - For N-Channel

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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
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Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.