Ordering number : EN6038A

SANYO

SANYO Semiconductors
DATA SHEET

LA1781M — Monolithic Linear IC For Car Radios Single-Chip Tuner IC

Overview

The LA1781M integrates all six blocks required in a car radio tuner on a single chip.

Features

- Improved noise reduction methods
  The FM front end provides excellent 3-signal characteristics equivalent to those of the LA1193M.
  Superlative listenability due to improved medium and weak field noise canceller characteristics.
  Improved separation characteristics.
  Anti-birdie filter.
  Improved AM and FM thermal characteristics.
  Excellent FM signal meter linearity.
  Modified N.C. circuit for improved noise rejection.
- Double conversion AM tuner (up conversion)
  Reduces the number of external components required as compared to earlier double conversion tuners, in particular, no crystal is required (when used in conjunction with the LC72144).
- Sample-to-sample variation reduction circuit built into the FM IF circuit.
  (Fixed resistors are used for the SD, keyed AGC, mute on adjustment, ATT, SNC, and HCC functions.)
- The LA1781 inherits the block arrangement of the LA1780M and supports pin-compatible designs.

Functions

- FM front end
- FM IF
- Noise canceller
- Multiplex
- AM up-conversion
- FM/AM switch
- MRC

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Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V_{CC}1 max	Pins 6, 40, and 61	9	V
	V_{CC}2 max	Pins 7, 45, 54, 59, and 60	12	V
Allowable power dissipation	Pd max	Ta ≤ 55°C	950	mW
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V_{CC}	Pins 6, 7, 40, 45, 54, 59, 60, and 61	8	V
	V_{CC}ST IND	Pin 26	5	V
Operating supply voltage range	V_{CC} op		7.5 to 9.0	V

Electrical Characteristics at Ta = 25°C, V_{CC} = 8V, in the specified test circuit for the FM IF input

Parameter	Symbol	Conditions	Ratings min	Ratings typ	Ratings max	Unit
FM characteristics At the FM IF input						
Current drain	I_{CCO}-FM	No input, I40 + I45 + I54 + I59 + I60 + I61	60	94	110	mA
Demodulation output	V_O-FM	10.7MHz, 100dBμ, 1kHz, 100%mod, The pin 15 output	205	310	415	mVrms
Pin 31 demodulation output	V_O-FM31	10.7MHz, 100dBμ, 1kHz, 100%mod, The pin 31 output	190	295	380	mVrms
Channel balance	CB	The ratio between pins 15 and 16 at 10.7MHz, 100dBμ, 1kHz	-1	0	+1	dB
Total harmonic distortion	THD-FM mono	10.7MHz, 100dBμ, 1kHz, 100% mod, pin 15		0.3	1.0	%
Signal-to-noise ratio : IF	S/N-FM IF	10.7MHz, 100dBμ, 1kHz, 100% mod, pin 15	75	82		dB
AM suppression ratio: IF	AMR IF	10.7MHz, 100dBμ, 1kHz, fm = 1kHz, 30% AM, pin 15	55	68		dB
Muting attenuation	Att-1	10.7MHz, 100 dBμ, 1kHz. The pin 15 attenuation when V33 goes from 0 to 2V	5	10	15	dB
	Att-2	10.7MHz, 100 dBμ, 1kHz. The pin 15 attenuation when V33 goes from 0 to 2V*[1]	15	20	25	dB
	Att-3	10.7MHz, 100 dBμ, 1kHz. The pin 15 attenuation when V33 goes from 0 to 2V*[2]	28	33	38	dB
Separation	Sep	10.7MHz, 100dBμ, L + R = 90%, pilot = 10%. The pin 15 output ratio	30	40		dB
Stereo on level	ST-ON	The pilot modulation such that V26 < 0.5V	2.1	4.1	6.5	%
Stereo off level	ST-OFF	The pilot modulation such that V26 > 3.5V	1.2	3.1		%
Main total harmonic distortion	THD-Main L	10.7MHz, 100dBμ, L + R = 90%, pilot = 10%. The pin 15 signal		0.3	1.2	%
Pilot cancellation	PCAN	10.7MHz, 100dBμ, pilot = 10%. The pin 15 signal/the pilot level leakage. DIN audio	20	30		dB
SNC output attenuation	AttSNC	10.7MHz, 100dBμ, L - R = 90%, pilot = 10%. V28 = 3V → 0.6V, pin 15	1	5	9	dB
HCC output attenuation	AttHCC-1	10.7MHz, 100dBμ, 10kHz, L + R = 90%, pilot = 10%. V29 = 3V → 0.6V, pin 15	0.5	4.5	8.5	dB
	AttHCC-2	10.7MHz, 100dBμ, 10kHz, L + R = 90%, pilot = 10%. V29 = 3V → 0.1V, pin 15	6	10	14	dB
Input limiting voltage	V_{IN}-LIM	100dBμ, 10.7MHz, 30% modulation. The IF input such that the input reference output goes down by 3dB	33	40	47	dBμ
Muting sensitivity	V_{IN}-MUTE	The IF input level (unmodulated) when V33 = 2V	27	35	43	dBμ
SD sensitivity	SD-sen1 FM	The IF input level (unmodulated) (over 100mV rms) such that the IF counter buffer output goes on	54	62	70	dBμ
	SD-sen2 FM		54	62	70	dBμ
IF counter buffer output	$V_{IFBUFF-FM}$	10.7MHz, 100dBμ, unmodulated. The pin 23 output	130	200	270	mVrms
Signal meter output	V_{SM}FM-1	No input. The pin 24 DC output, unmodulated	0.0	0.1	0.3	V
	V_{SM}FM-2	50dBμ. The pin 24 DC output, unmodulated	0.4	1.0	1.5	V
	V_{SM}FM-3	70dBμ. The pin 24 DC output, unmodulated	2.0	2.7	3.5	V
	V_{SM}FM-4	100dBμ. The pin 24 DC output, unmodulated	4.7	5.5	6.2	V
Muting bandwidth	BW-MUTE	100dBμ. The bandwidth when V33 = 2V, unmodulated	150	220	290	kHz
Mute drive output	V_{MUTE}-100	100dBμ, 0dBμ. The pin 33 DC output, unmodulated	0.00	0.03	0.20	V

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
FM FE Mixer input						
N-AGC on input	V_{NAGC}	83MHz, unmodulated. The input such that the pin 2 voltage is 2.0V or below	81	88	95	dBμ
W-AGC on input	V_{WAGC}	83MHz, unmodulated. The input such that the pin 2 voltage is 2.0V or below. (When the keyed AGC is set to 4.0V.)	104	110	116	dBμ
Conversion gain	A. V	83MHz, 80dBμ, unmodulated. The FE CF output	19	30	48	mVrms
Oscillator buffer output	V_{OSC}BUFF-FM	No input	85	110	165	mVrms
NC Block NC input (pin 30)						
Gate time	T_{GATE}	f = 1kHz, for a 1μs, 100-mVp-o pulse		55		μs
Noise sensitivity	SN	The level of a 1kHz, 1μs pulse input that starts noise canceller operation. Measured at pin 30.		40		mVp-o
NC effect	SN-NC	The pulse rejection effect provided by the noise canceller. For a repeated 1μs wide pulse, frequency = 10kHz, 150mVp-o. The ratio of the FM mode pin 15 output referenced to the AM mode pin 15 output (effective value)	5			
Multipath rejection circuit MRC input (pin 27)						
MRC output	V_{MRC}	V24 = 5V	2.2	2.3	2.4	V
MRC operating level	MRC-ON	The pin 32 input level at f = 70kHz such that pin 24 goes to 5V and pin 27 goes to 2V	10	15	20	mVrms
AM characteristics AM ANT input						
Practical sensitivity	S/N-30	1MHz, 30dBμ, fm = 1kHz, 30% modulation, pin 15	20			dB
Detector output	V_O-AM	1MHz, 74dBμ, fm = 1kHz, 30% modulation, pin 15	130	195	270	mVrms
Pin 31 detector output	V_O-AM31	1MHz, 74dBμ, fm = 1kHz, 30% modulation, pin 31	110	175	230	mVrms
AGC F.O.M.	$V_{AGC\text{-}FOM}$	1MHz, 74dBμ, referenced to the output, the input amplitude such that the output falls by 10dB. Pin 15	59	64	69	dB
Signal-to-noise ratio	S/N-AM	1MHz, 74dBμ, fm = 1kHz, 30% modulation	47	52		dB
Total harmonic distortion	THD-AM	1MHz, 74dBμ, fm = 1kHz, 80% modulation		0.3	1	%
Signal meter output	V_{SM}AM-1	No input	0.0	0.2	0.5	V
	V_{SM}AM-2	1MHz, 130dBμ, unmodulated	3.5	4.4	6.1	V
Oscillator buffer output	V_{OSC}BUFF-AM1	No input, the pin 15 output	185	230		mVrms
Wide band AGC sensitivity	W-AGCsen1	1.4MHz, the input when V46 = 0.7V	92	98	104	dBμ
	W-AGCsen2	1.4MHz, the input when V46 = 0.7V (seek mode)	83	89	95	dBμ
SD sensitivity	SD-sen1AM	1MHz, the ANT input level such that the IF counter output turns on.	24	30	36	dBμ
	SD-sen2AM	1MHz, the ANT input level such that the SD pin goes to the on state.	24	30	36	dBμ
IF buffer output	V_{IFBUFF}-AM	1MHz, 74dBμ, unmodulated. The pin 23 output	200	290		mVrms

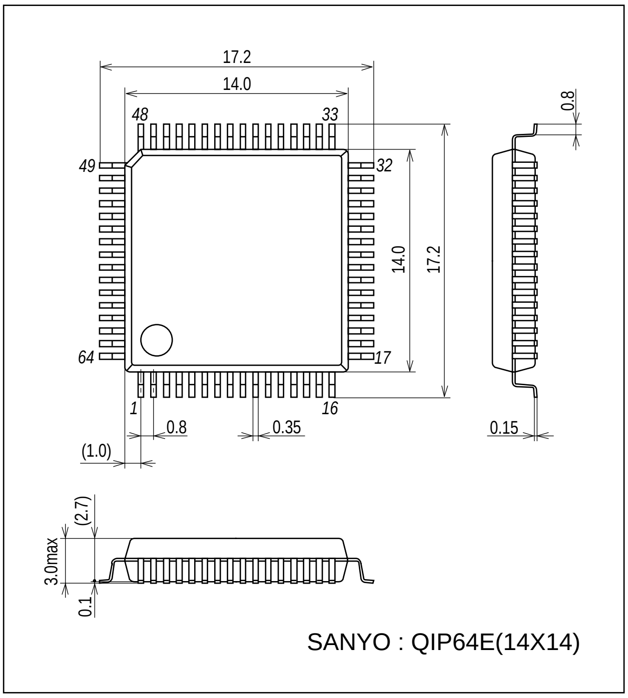
Note : These measurements must be made using the either the IC-51-0644-824 or KS8277 IC socket (manufactured by Yamaichi Electronics).

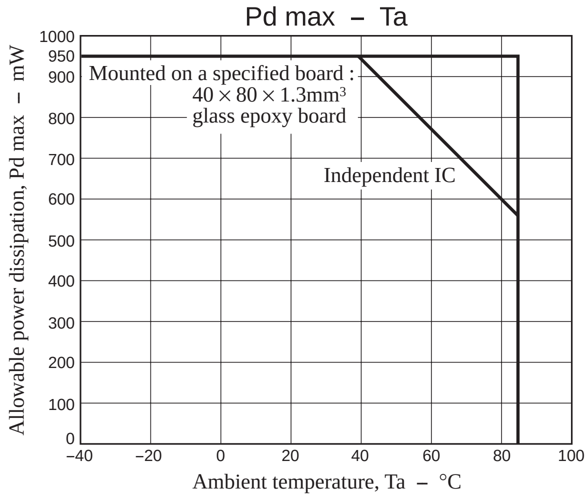
*1. When the resistor between pin 58 and ground is 200kΩ.

*2. When the resistor between pin 58 and ground is 30kΩ.

Package Dimensions

unit : mm (typ)
3159A





Function List

1. FM Front End (Equivalent to the Sanyo LA1193)
   (1) Double input type double balanced mixer
   (2) Pin diode drive AGC output
   (3) MOSFET second gate drive AGC output
   (4) Keyed AGC adjustment pin
   (5) Differential IF amplifier
   (6) Wide band AGC sensitivity setting pin, and narrow band AGC sensitivity setting pin
   (7) Local oscillator

2. FM IF
   (1) IF limiter amplifier
   (2) S-meter output (also used for AM) 6-stage pickup
   (3) Multipath detection pin (shared FM signal meter)
   (4) Quadrature detection
   (5) AF preamplifier
   (6) AGC output
   (7) Band muting
   (8) Weak input muting
   (9) Soft muting adjustment pin
   (10) Muting attenuation adjustment pin
   (11) IF counter buffer output (also used for AM)
   (12) SD (IF counter buffer on level) adjustment pin
   (13) SD output (active high) (also used for AM)

3. Noise Canceller
   (1) High-pass filter (first order)
   (2) Delay circuit based low-pass filter (fourth order)
   (3) Noise AGC
   (4) Pilot signal compensation circuit
   (5) Noise sensitivity setting pin
   (6) Function for disabling the noise canceller in AM mode

4. Multiplex Functions
   (1) Adjustment-free VCO circuit
   (2) Level follower type pilot canceller circuit
   (3) HCC (high cut control)
   (4) Automatic stereo/mono switching
   (5) VCO oscillation stop function (AM mode)
   (6) Forced monaural
   (7) SNC (stereo noise controller)
   (8) Stereo display pin
   (9) Anti-birdie filter

5. AM
   (1) Double balanced mixer (1st, 2nd)
   (2) IF amplifier
   (3) Detection
   (4) RF AGC (narrow/wide)
   (5) Pin diode drive pin
   (6) IF AGC
   (7) Signal meter output (also used for FM)
   (8) Local oscillator circuits (first and second)
   (9) Local oscillator buffer output
   (10) IF counter buffer output (also used by the FM IF)
   (11) SD (IF counter buffer on level) adjustment pin
   (12) SD output (active high) (also used for AM)
   (13) Wide AGC
   (14) Detection output frequency characteristics adjustment pin (low cut, high deemphasis)
   (15) AM stereo buffer

6. MRC (multipath noise rejection circuit)

7. AM/FM switching output (linked to the FM V_{CC})

Operating Characteristics and Symbols Used in the Test Circuit Diagrams

(1) Switches (SW)

Switch on = 1, SW off = 0

There are two switches that use signal transfer.

1) SW2 : switches between the mixer input and the IF input.

2) SW4 : switches between noise canceler input and IF output + noise canceler input.

(2) Types of SG used

PG1 (AC1)	Used for noise canceler testing. A pulse generator and an AF oscillator are required.
AC2	Used for FM front end testing. Outputs an 83MHz signal.
AC3	Used for FM IF, noise canceler, and MPX testing. Outputs a 10.7MHz signal. Stereo modulation must be possible.
AC4	Used for AM testing. Outputs 1 MHz and 1.4MHz signals.
AC5	Used with the MRC. Can also be used for AF and OSC.

(3) Power supply

V_{CC}	8V		
$V_{CC}1$	5V		SD, stereo, seek/stop
$V_{CC}2$	0.1V/0.7V/2V/4V	These levels must be variable.	Keyed AGC, Mute ATT
$V_{CC}3$	0.1V/0.6V/2V		HCC, SNC, SASC (MRC)

(4) (a) Switches

	Parameter	ON	OFF
SW1	AM/FM switching. The FE V_{CC} is supplied to pin 62.	FM	AM
SW2	FM IF switching. Pin 51/FE output	FE IF OUT (A)	AC3 (B)
SW3	For conversion gain testing	Conversion gain measurement (A)	Other/purposes
SW4	For switching between noise canceler input and IF output + noise canceler.	AC1 (A)	Other/purposes
SW5	High-speed SD	High-speed SD	Other/purposes
SW6	SEEK/STOP (IF BUFF ON/OFF)	STOP	Seek (IF buffer output)
SW7	MUTE ATT 200kΩ	MUTE 200kΩ	OFF
SW8	MUTE ATT 30kΩ	MUTE 30kΩ	OFF
SW9	For pilot cancellation testing	When pilot cancellation is used	When pilot cancellation is not used
SW10	Mute off (pin 33)	MUTE OFF	MUTE ON

(b) Trimmers (variable resistors)

VR1	Separation adjustment
VR2	Pilot cancellation adjustment

(5) Test Points

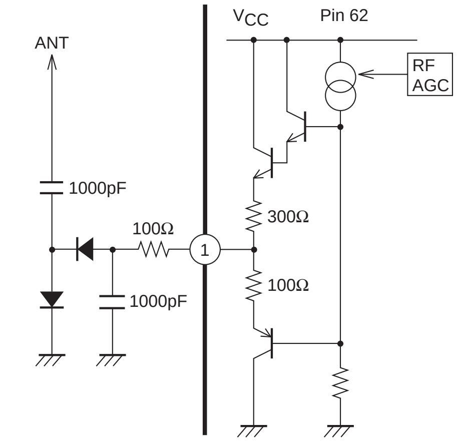
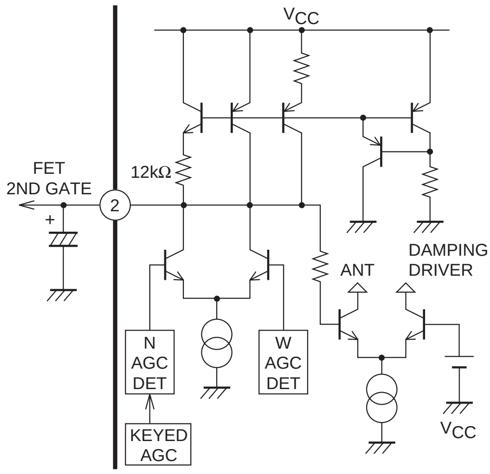
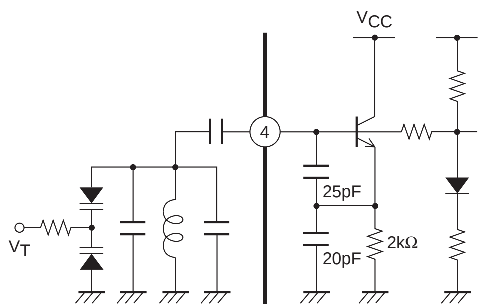
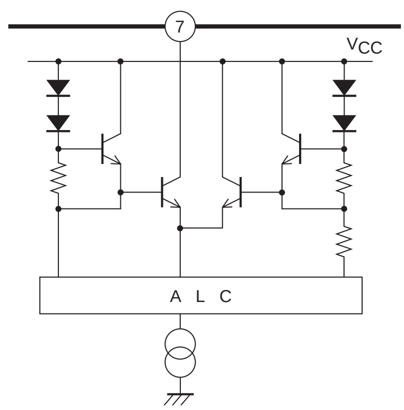
(a) DC voltages

VD1	FM RF AGC voltage	Pin 2
VD2	AM/FM SD, AM Tweet, FM stereo indicator	Pin 26
VD3	AM/FM S-meter	Pin 24
VD4	MRC output	Pin 27
VD5	Mute drive output	Pin 33
VD6	AM antenna damping voltage	Pin 46
VD7	N.C. Gate time	Pin 8

(b) AC voltages

VA1	AM/FM OSC Buff	Pin 4
VA2	First IF output	Pin 53 → CF → pin 51 load level (10.7MHz)
VA3	IF counter buffer	Pin 23 (10.7MHz/450kHz)
VA4	MPX OUT Left ch	Pin 15 (AF)
VA5	MPX OUT Right ch	Pin 16 (AF)

Pin Descriptions

Pin No.	Pin function	Pin description	Equivalent circuit
1	Antenna damping drive	An antenna damping current flows when the RF AGC voltage (pin 2) reaches V_{CC} - V_D.	
2	RF AGC	Used to control the FET second gate.	
3	F.E.GND		
4	OSC	Oscillator connection. The transistor and capacitors required for the oscillator circuit are integrated on the chip.	
7	AM OSC	AM first oscillator. This circuit can oscillator up to the SW band. An ALC circuit is included.	

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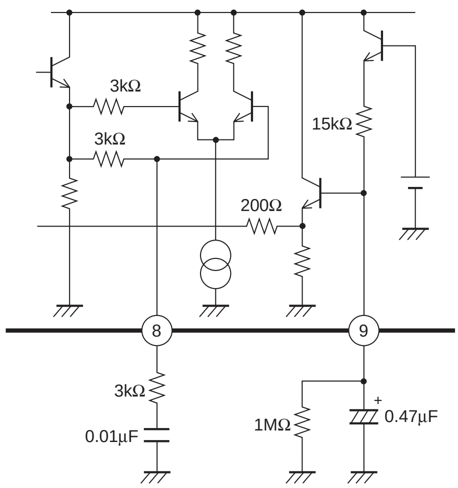
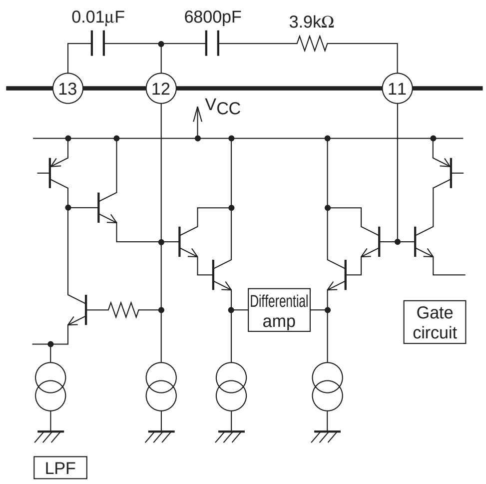
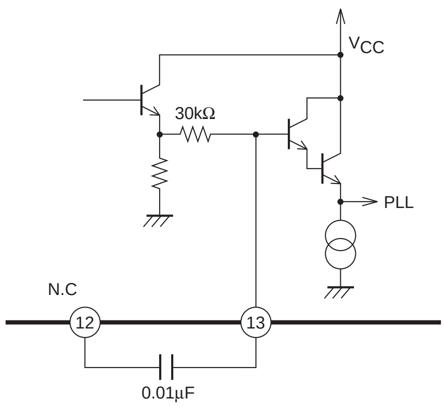
Pin No.	Pin function	Pin description	Equivalent circuit
8 9	Noise AGC sensitivity AGC adjustment	After setting up the medium field (about 50dBµ) sensitivity with the noise sensitivity setting pin (pin 8), set the weak field (about 20 to 30dBµ) sensitivity with the AGC adjustment pin (pin 9).	
11 12	Memory circuit connection	Recording circuit used during noise canceller operation.	
13	Pilot input	Pin 13 is the PLL circuit input pin.	
14	N.C, MPX, MRC, GND	Ground for the N.C., MPX, and MRC circuits.	

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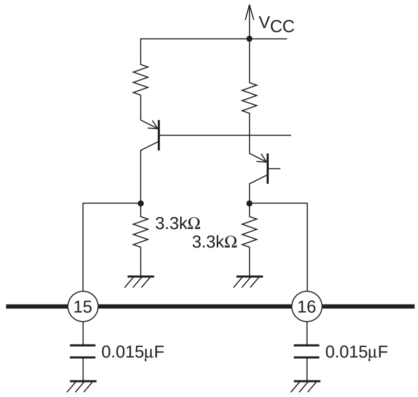
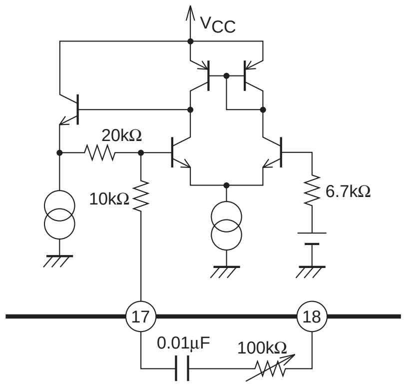
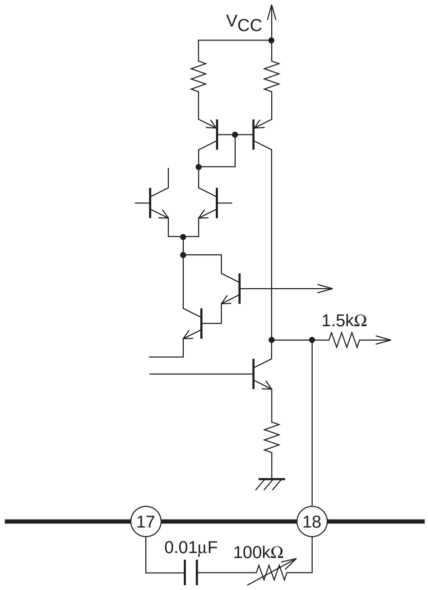
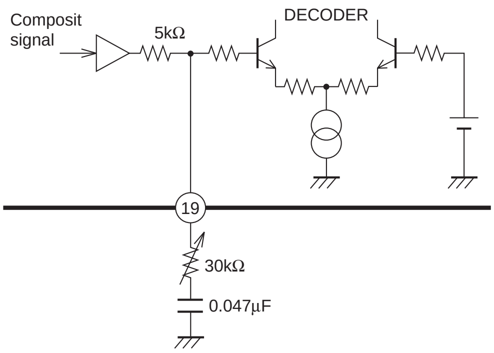
Pin No.	Pin function	Pin description	Equivalent circuit
15 16	MPX output (left) MPX output (right)	Deemphasis 50µs : 0.015µF 75µs : 0.022µF	
17	Pilot canceller signal output	Adjustment is required since the pilot signal level varies with the sample-to-sample variations in the IF output level and other parameters.	
18	Pilot canceller signal output	Pin 18 is the output pin for the pilot canceller signal.	
19	Separation adjustment pin	Use a trimmer to adjust the subdecoder input level. (The output level is not modified in mono and main modes.)	

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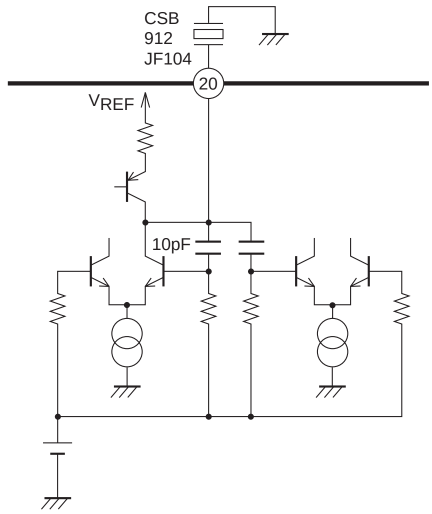
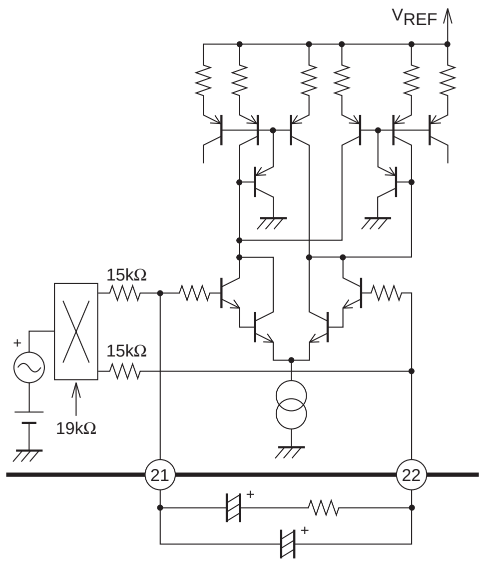
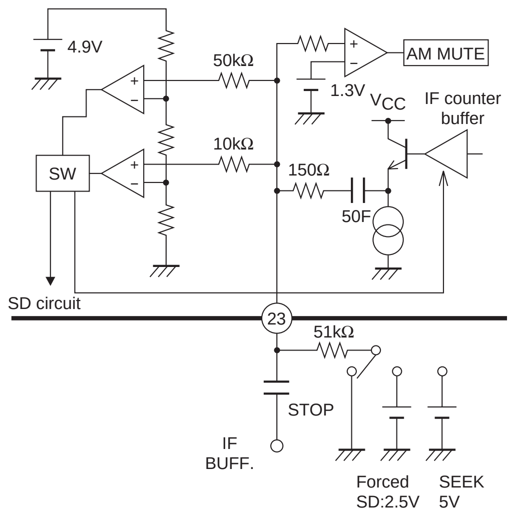
Pin No.	Pin function	Pin description	Equivalent circuit
20	VCO	The oscillator frequency is 912Hz. KBR-912F108 (Kyocera Corporation) CSB-912JF108 (Murata Mfg. Co., Ltd.)	
21 22	PHASE COMP. PHASE COMP.		
23	IF counter buffer seek/stop switching	This pin functions both as the IF counter buffer (AC output) and as the seek/stop switch pin. The voltage V23 switches between the following three modes. During FM reception : 5V : Seek mode 2.5V : Forced SD mode 0V : Reception mode AM reception (two modes : 0 and 5V) 5V : Seek mode 0V : Reception mode	

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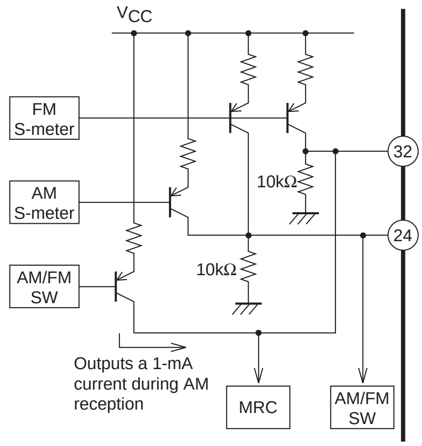
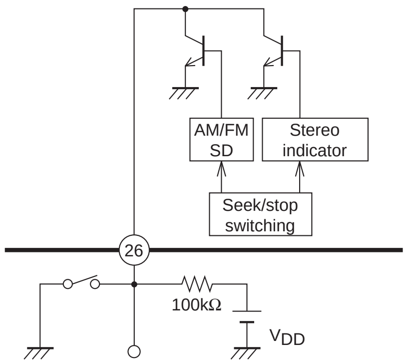
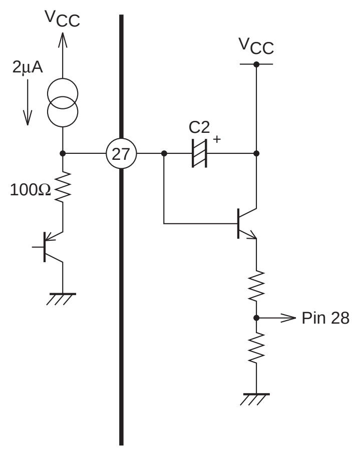
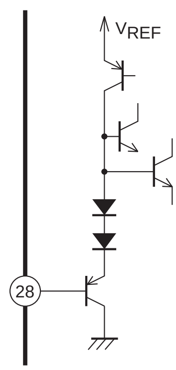
Pin No.	Pin function	Pin description	Equivalent circuit
24 32	AM/FM signal meter Dedicated FM signal meter	Fixed-current drive signal meter output. In AM mode, pin 32 outputs a 1mA current. Thus the HCC circuit is turned off.	
26	Stereo indicator for the SD pin	The voltage V23 switches between three modes as follows. FM reception : 5V : The SD pin operates linked to the IF counter buffer. 2.5V : Forced SD mode: operates as the SD pin. 0.7V : Reception mode: stereo indicator AM reception : (two modes : 0 and 5V) 5V : Operates as the seek SD pin. 0V : Reception mode. Not used.	
27	MRC control voltage time constant	The MRC detector time constant is determined by a 100Ω resistor and C2 when discharging and by the 2µA current and C2 when charging.	
28	SNC control input	The sub-output is controlled by a 0 to 1V input.	

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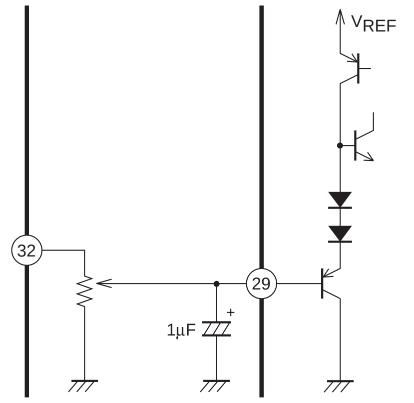
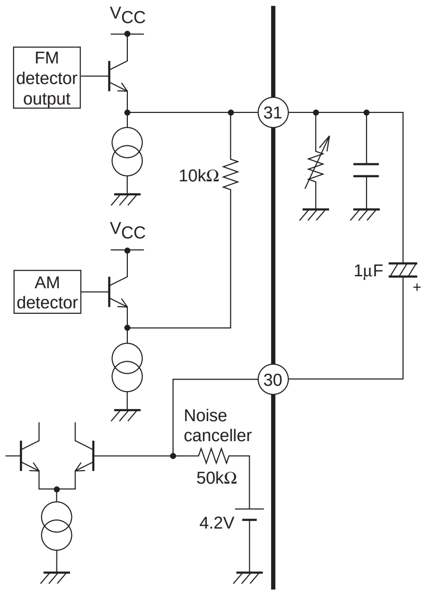
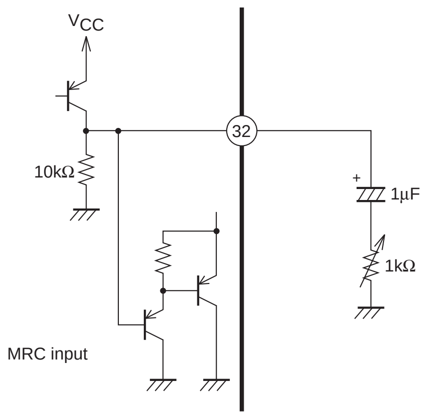
Pin No.	Pin function	Pin description	Equivalent circuit
29	HCC control input	The high band frequency output is controlled by a 0 to 1V input. It can also be controlled by the MRC output. Use a resistor of at least 100kΩ when controlling with the pin 32 FM S-meter signal.	
30 31	Noise canceller input AM/FM detector output	Pin 30 is the noise canceller input. The input impedance is 50kΩ. Pin 31 is the AM and FM detector output In FM mode, this is a low-impedance output. In AM mode, the output impedance is 10kΩ. To improve the low band separation, use a coupling capacitor of over 10μF.	
32	IF S-meter output and MRC DC input	FM S-meter output block MRC AC input block Adjust the external 1kΩ resistor to attenuate the MRC AC input and control the circuit.	

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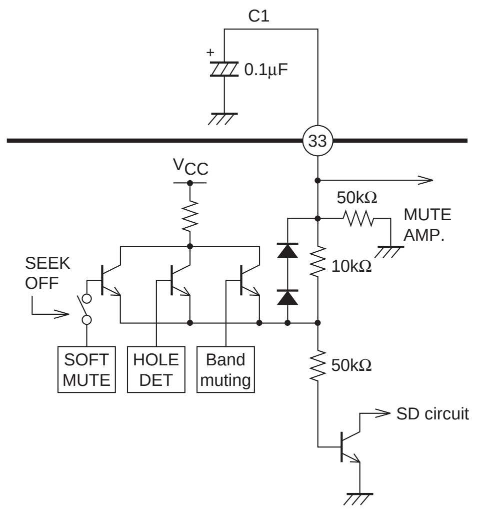
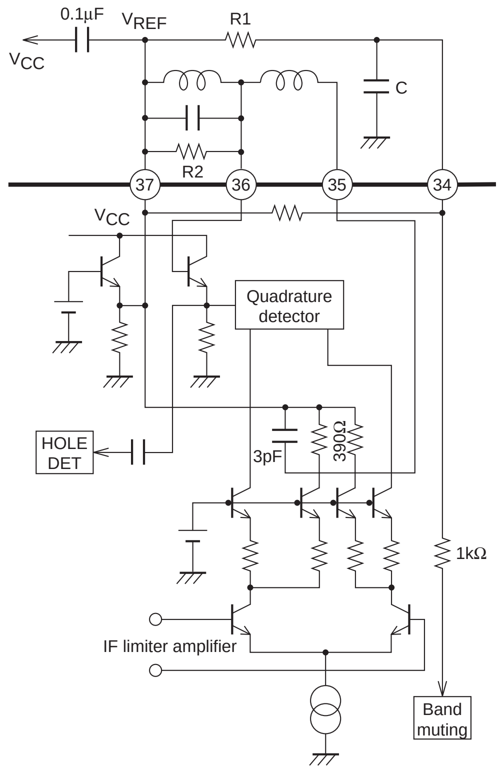
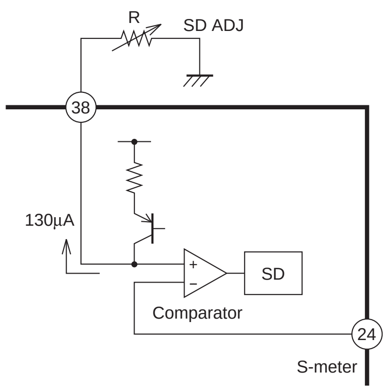
Pin No.	Pin function	Pin description	Equivalent circuit
33	Mute drive output	•The muting time constant is determined by an external RC circuit as described below. Attack time : T_A = 10kΩ × C1 Release time : T_R = 50kΩ × C1 •Noise convergence adjustment The noise convergence can be adjusted when there is no input signal by inserting a resistor between pin 33 and ground. •Muting off function Ground pin 33 through a 4kΩ resistor.	
34 35 36 37	AGC QD output QD input V_{REF}	•The resistor R1 determines the width of the band muting function. Increasing the value of R1 narrows the band. Reducing the value of R1 widens the band. •Null voltage When tuned, the voltage between pins 34 and 37, $V_{34\text{ - }37}$, will be 0V. The band muting function turns on when $\lvert V_{34\text{ - }37}\rvert \geq 0.7V$. V_{37} = 4.9V	
38	FM SD ADJ	A 130µA current flows from pin 38 and, in conjunction with the external resistance R, determines the comparison voltage.	

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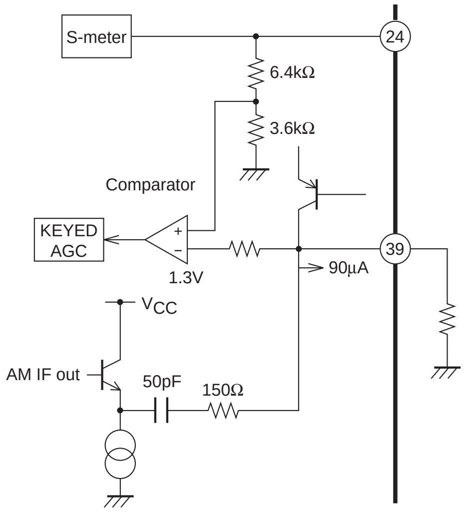
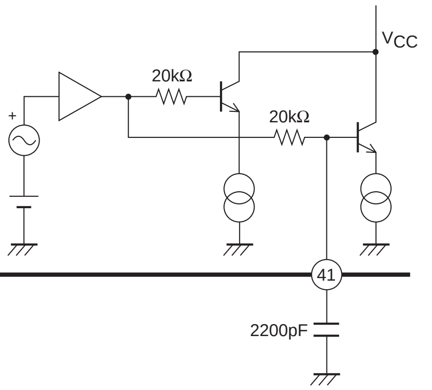
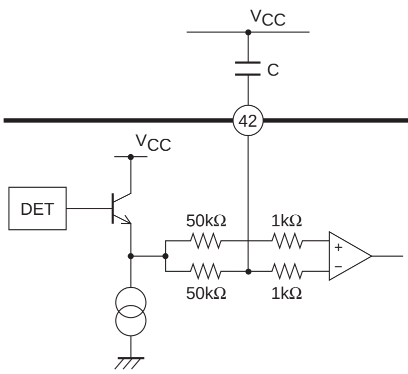
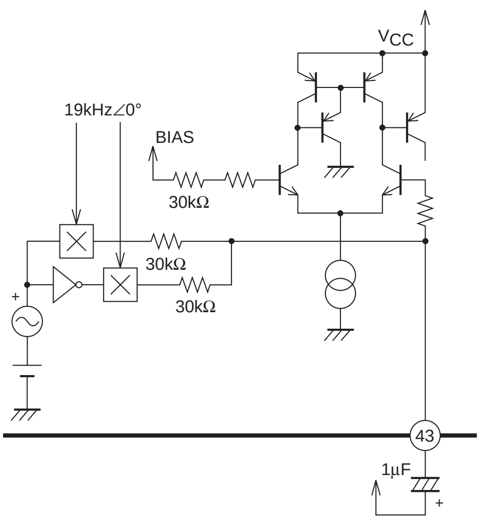
Pin No.	Pin function	Pin description	Equivalent circuit
39	Keyed AGC AM stereo buffer	The keyed AGC operates when the voltage created by dividing the pin 24 S-meter output voltage by the 6.4 and 3.6kΩ resistors becomes lower than the voltage determined by the resistor between pin 39 and ground. This pin also is used as the AM stereo IF buffer pin.	
41	HCC capacitor	The HCC frequency characteristics are determined by the external capacitor connected at this pin.	
42	AM L.C. pin	This pin is used to change the frequency characteristics of the unneeded audio band under 100Hz in AM mode to produce a clear audio signal. Note : The LC capacitor must be connected between this pin and V_{CC} (pin 40). This is because the detector circuit operates referenced to V_{CC}. The cutoff frequency f_C is determined by the following formula. $f_C = 1/2\pi \times 50k\Omega \times C$	
43	Pilot detector	Inserting a 1MΩ resistor between pin 43 and V_{CC} will force the IC to mono mode.	

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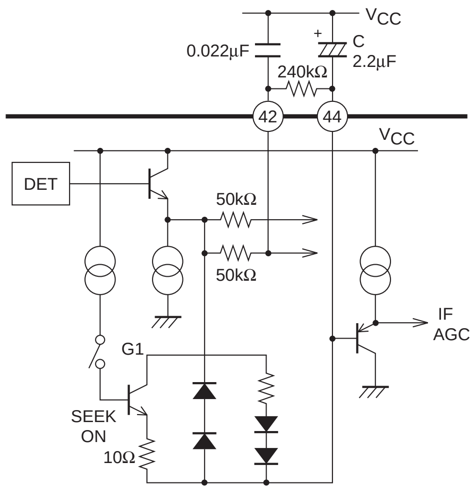
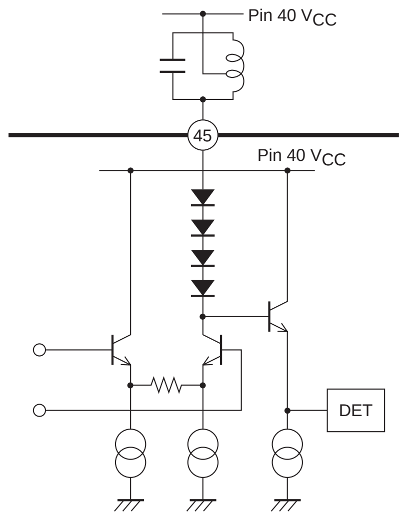
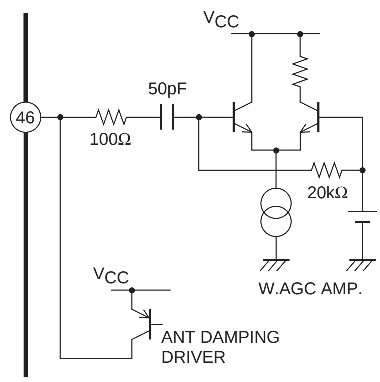
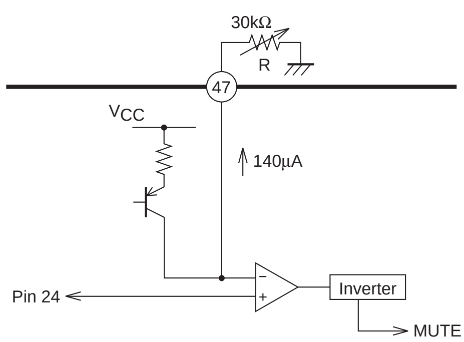
Pin No.	Pin function	Pin description	Equivalent circuit
44	IF AGC	G1 ; Used for time constant switching during seeks. • Reception $\tau = 2.2\mu F \times 300k\Omega$ • Seek $\tau = 2.2\mu F \times 10\Omega$ The external capacitors are connected to V_{CC}. This is because the IF amplifier operates referenced to V_{CC}.	
45	IF output	The IF amplifier load	
46	AM antenna damping drive output Wide band AGC input	I46 = 6mA (maximum) This is the antenna damping current.	
47	FM muting on level adjustment	Modify the value of the external resistor to adjust the muting on level.	

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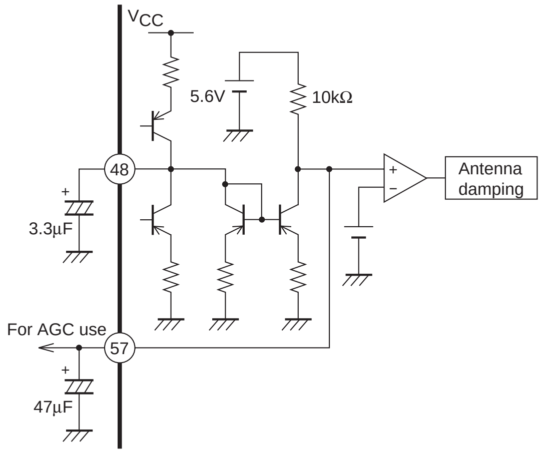
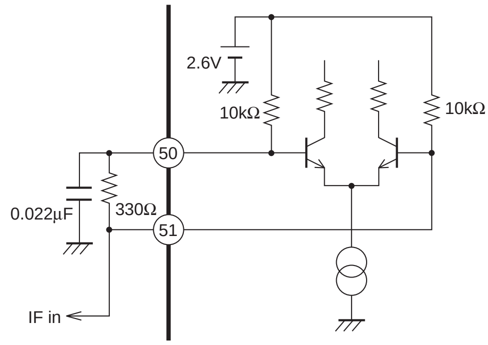
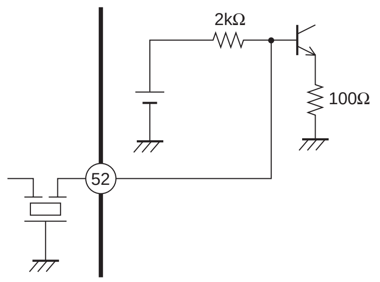
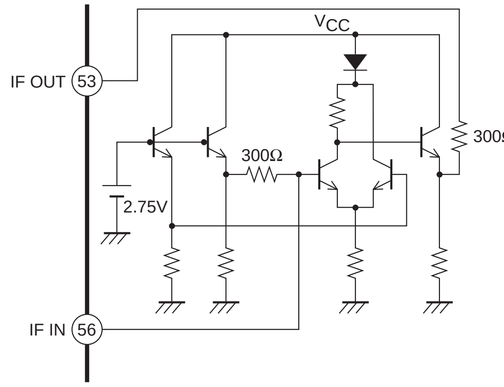
Pin No.	Pin function	Pin description	Equivalent circuit
48 57	RF AGC bypass RF AGC	RF AGC rectification capacitor The low frequency distortion is determined as follows : Increasing C48 and C57 improves the distortion but makes the response slower. Reducing C48 and C57 aggravates the distortion but makes the response faster.	
50 51	IF bypass FM IF input	Due to the high gain of the limiter amplifer, care must be taken when choosing the grounding point for the limiter amplifer input capacitor to prevent oscillation.	
52	IF input	The input impedance is 2kΩ.	
53 56	IF amplifier output IF amplifier input	• Input and output pin or the first IF amplifier • Inverting amplifier V56 = 2V Input impedance : R_{IN} = 330Ω V53 = 5.3V Output impedance R_{OUT} = 330Ω	

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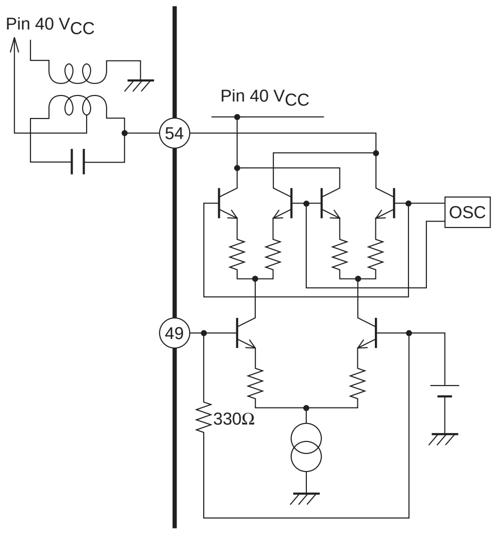
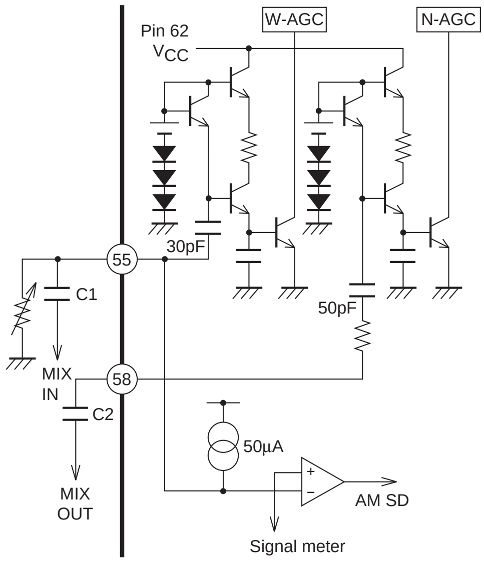
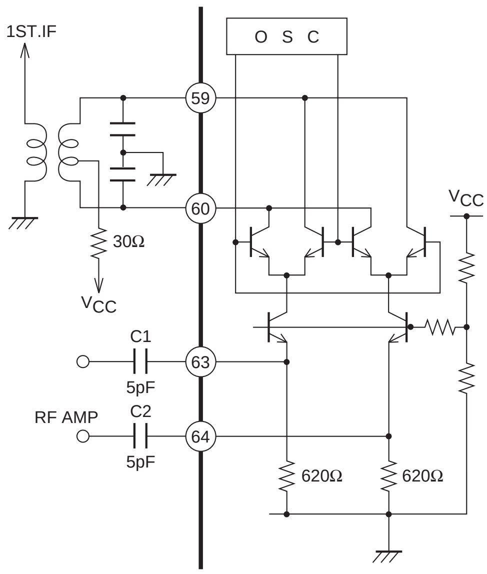
Pin No.	Pin function	Pin description	Equivalent circuit
54 49	Mixer output : 130µA Mixer input	The mixer coil connected to the pin 54 mixer output must be wired to V_{CC} (pin 40). The pin 49 mixer input Impedance is 330Ω	
55 58	W-AGC IN AM SD ADJ N-AGC IN Muting attenuation adjustment pin	Pins 55 and 58 include built-in DC cut capacitors. The AGC on level is determined by the values of the capacitors C1 and C2. Pin 55 functions as the SD sensitivity adjustment pin in AM mode. The output current I55 is 50µA, and V55 varies depending on the value of the external resistor. The SD function operates by comparing V55 with the S-meter voltage.	
59, 60 63, 64	Mixer output Mixer input	Double balanced mixer. Pins 59 and 60 are the mixer 10.7MHz output Pins 63 and 64 are the mixer input. This is an emitter insertion type circuit, and the amount of insertion is determined by the capacitors C1 and C2. Note :The lines for pins 63 and 64 must be kept separated from the lines for pins 59 and 60.	

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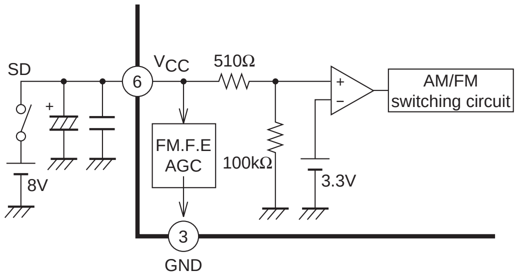
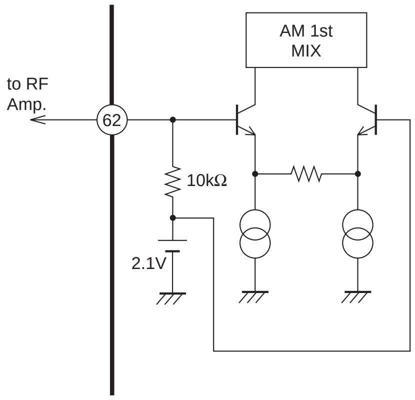
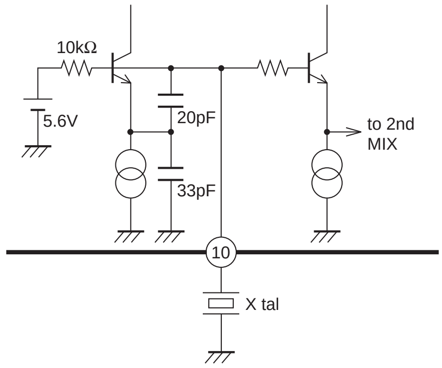
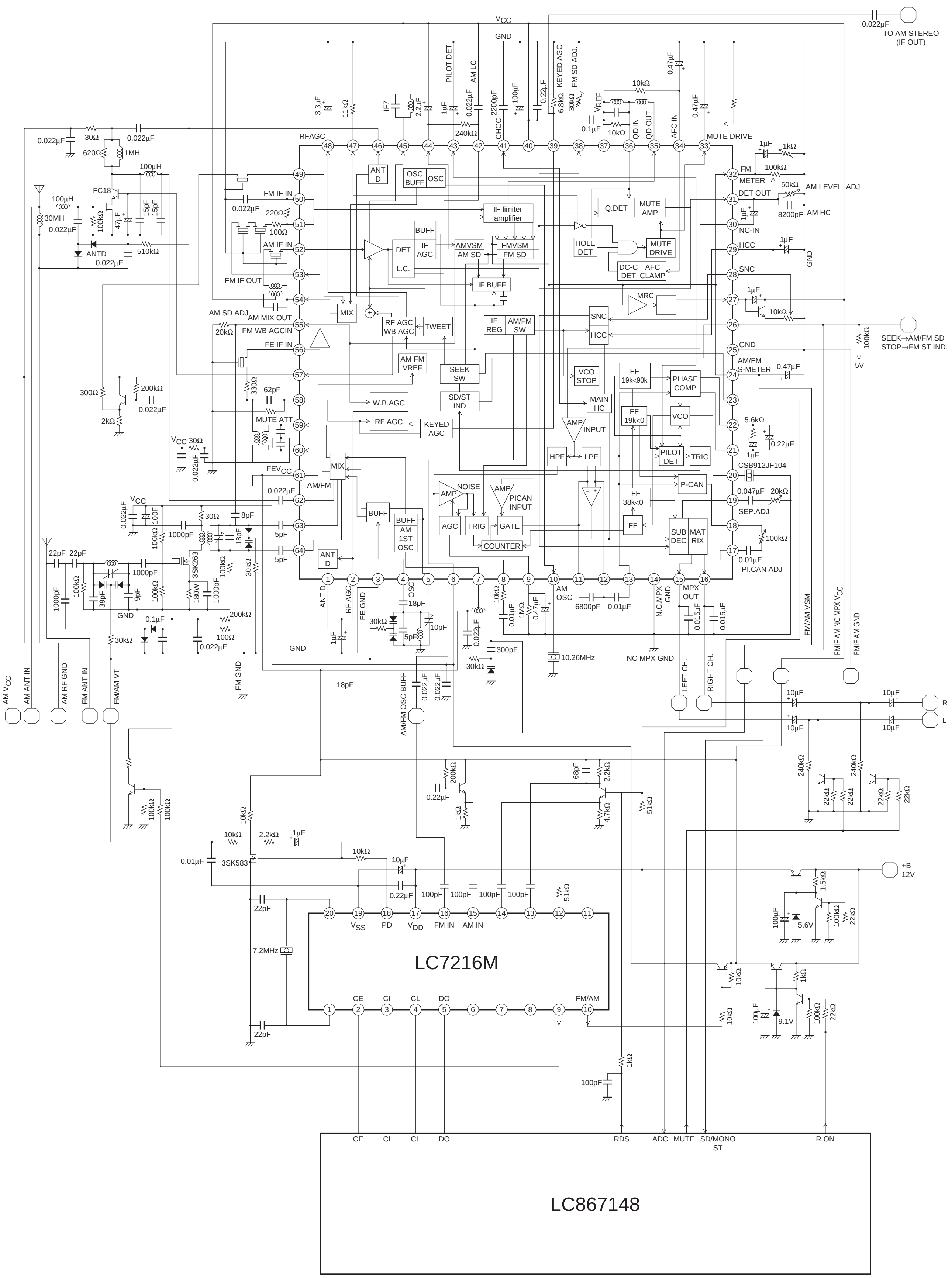
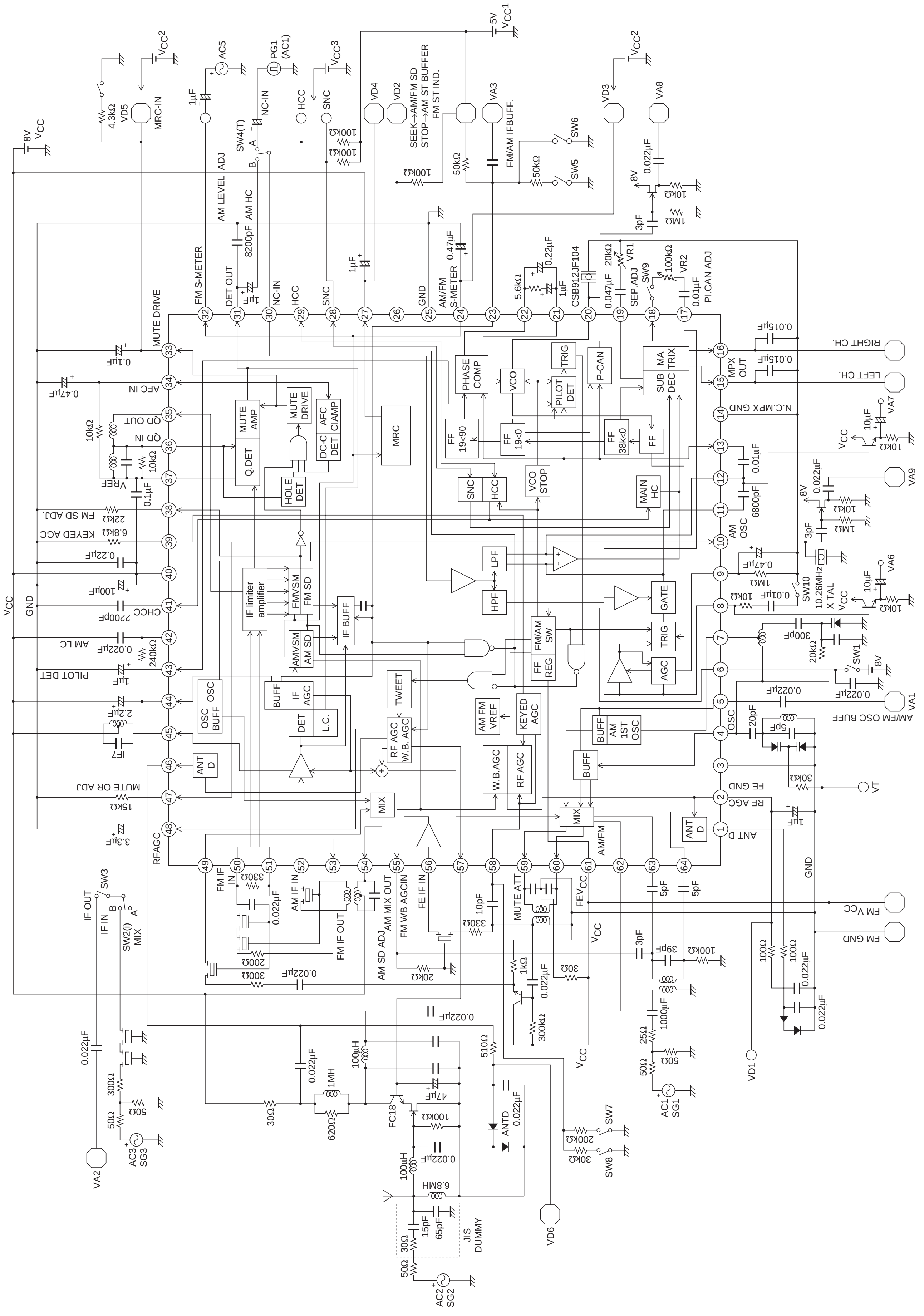
Pin No.	Pin function	Pin description	Equivalent circuit
6	Front end V_{CC} AM/FM switching	Pin 6 functions both as the FM front end V_{CC} and the AM/FM switching circuit. V6 voltage / Mode: When 8V → FM OPEN → AM	
62	1st MIX INPUT	First mixer input The input impedance is about 10kΩ.	
10	AM 2nd OSC	Crystal oscillator circuit The Kinseki, Ltd. HC-49/U-S and a C_L of 20pF must be used.	

Block Diagram



AC Characteristics Test Circuit



Test Conditions

Parameter	Symbol	Switch states									
		SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10
Current drain	I_{CCO}-FM	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Demodulation output	V_O-FM	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Pin 31 demodulation output	V_O-FM31	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Channel balance	CB	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Total harmonic distortion (FM)	THD-FM mono	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Signal-to-noise ratio : IF	S/N-FM IF	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
AM suppression ratio : IF	AMR IF	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Muting attenuation	Att-1	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
	Att-2	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
	Att-3	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Separation	Separation	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Stereo on level	ST-ON	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Stereo off level	ST-OFF	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Main total harmonic distortion	THD-Main L	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Pilot cancellation	PCAN	ON	b	OFF	b	-	ON	OFF	OFF	OFF/ON	-
SNC output attenuation	AttSNC	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
HCC output attenuation 1	AttHCC-1	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
HCC output attenuation 2	AttHCC-2	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Input limiting voltage	V_{IN}-LIM	ON	b	OFF	b	-	ON	OFF	OFF	ON	ON
Muting sensitivity	V_{IN}-MUTE	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
SD sensitivity 1	SD-sen1 FM	ON	b	OFF	b	OFF	OFF	OFF	OFF	ON	-
SD sensitivity 2	SD-sen2 FM	ON	b	OFF	b	ON	OFF	OFF	OFF	ON	-
IF counter buffer output	$V_{IFBUFF-FM}$	ON	b	OFF	b	OFF	OFF	OFF	OFF	ON	-
Signal meter output (FM)	V_{SM} FM-1	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
	V_{SM} FM-2	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
	V_{SM} FM-3	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
	V_{SM} FM-4	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Muting bandwidth	BW-MUTE	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
Mute drive output	$V_{MUTE-100}$	ON	b	OFF	b	-	ON	OFF	OFF	ON	-
N-AGC on input	V_{NAGC}	ON	a	ON	b	-	ON	OFF	OFF	-	-
W-AGC on input	V_{WAGC}	ON	a	ON	b	-	ON	OFF	OFF	-	-
Conversion gain	A.V	ON	a	ON	b	-	ON	OFF	OFF	-	-
Oscillator buffer output	$V_{OSCBUFF-FM}$	ON	a	ON	b	-	ON	OFF	OFF	-	-
Gate time 1	τGATE1	ON	-	OFF	a	-	ON	OFF	OFF	-	-
Noise sensitivity	SN	ON	-	OFF	a	-	ON	OFF	OFF	-	-
NC effect	SN-NC	ON/OFF	-	OFF	a	-	ON	OFF	OFF	-	-
MRC output	V_{MRC}	ON	-	OFF	b	-	ON	OFF	OFF	-	-
MRC operating level	MRC-ON	ON	-	OFF	b	-	ON	OFF	OFF	-	-
Practical sensitivity	S/N-30	OFF	-	OFF	b	ON	ON	-	-	-	-
Detection output	V_O-AM	OFF	-	OFF	b	ON	ON	-	-	-	-
Pin 31 detection output	V_O-AM31	OFF	-	OFF	b	ON	ON	-	-	-	-
AGC F.O.M.	$V_{AGC-FOM}$	OFF	-	OFF	b	ON	ON	-	-	-	-
Signal-to-noise ratio	S/N-AM	OFF	-	OFF	b	ON	ON	-	-	-	-
Total harmonic distortion (AM)	THD-AM	OFF	-	OFF	b	ON	ON	-	-	-	-
Signal meter output (AM)	V_{SM} AM-1	OFF	-	OFF	b	ON	ON	-	-	-	-
	V_{SM} AM-2	OFF	-	OFF	b	ON	ON	-	-	-	-
Oscillator buffer output	$V_{OSCBUFF-AM}$1	OFF	-	OFF	b	ON	ON	-	-	-	-
Wide band AGC sensitivity	W-AGCsen1	OFF	-	OFF	b	ON	ON	-	-	-	-
	W-AGCsen2	OFF	-	OFF	b	ON	ON	-	-	-	-
SD sensitivity	SD-sen1 AM	OFF	-	OFF	b	OFF	OFF	-	-	-	-
	SD-sen2 AM	OFF	-	OFF	b	OFF	OFF	-	-	-	-
IF buffer output	$V_{IFBUFF-AM}$	OFF	-	OFF	b	OFF	OFF	-	-	-	-

Usage Notes

1. Notes on V_{CC} and Ground

Pin 40	V_{CC} for the FM IF, AM, NC, MPX, and MRC blocks
Pin 25	Ground for the FM IF and AM blocks
Pin 14	Ground for the NC, MPX, and MRC blocks
Pin 61	V_{CC} for the FM front end, AM first mixer, and first oscillator blocks
*Pin 6	V_{CC} for the FM front end and AGC blocks, and the AM/FM switching pin
Pin 3	Ground for the FM front end, first mixer, and first oscillator blocks

2. Notes on AM Coil Connection
   The V_{CC} used for the first oscillator coil connected to pin 7 must be at the same potential as pin 61.
   Connect to the IFT connected with pin 45, and to the MIX coil connected with pin 54. V_{CC} must be at the same potential as pin 40.

3. AM/FM Switching
   Pin 6 is also used as the FM front end and RF AGC V_{CC}

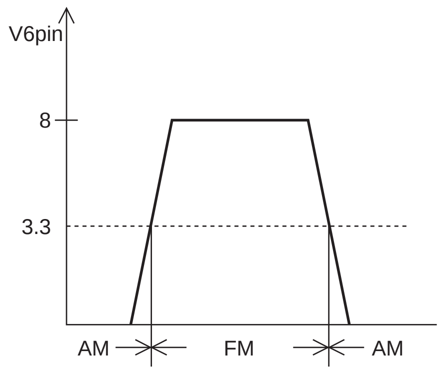


Fig. 1

Pin 6 voltage	Mode
8	FM
OPEN	AM

4. Notes on the FM Front End
   Notes on interference rejection characteristics
   • Intermodulation characteristics
   The LA1781M applies two high-band AGC functions to prevent IM (the generation of intermodulation). These are the narrow AGC (pin 58 : mixer input detection type) and the wide AGC (for the pin 55 input), and this results in the antenna frequency characteristics shown in figure 2. The levels at which the AGC functions turn on are determined by the capacitors attached at pins 55 and 58.

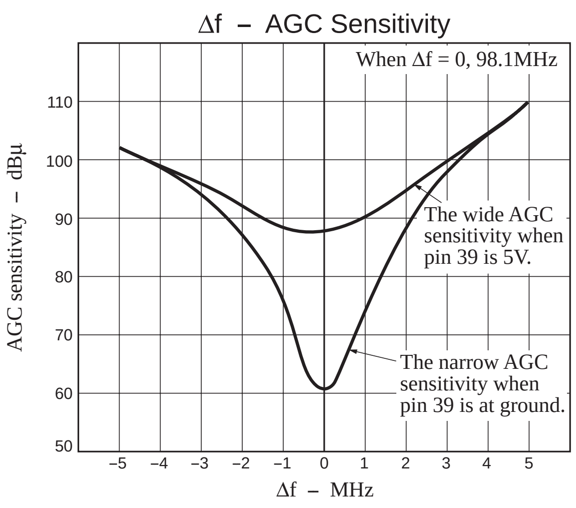


Fig. 2

- Notes on second-channel attenuation suppression

  Keyed AGC (3D AGC) is a technique for achieving good characteristics for both intermodulation and secondchannel attenuation at the same time. When the desired signal is faint or nonexistent, the high-band AGC level will be essentially 0, and as a result automatic tuning may malfunction and blocking oscillation may occur in the presence of strong interfering stations. Keyed AGC helps resolve these problems.

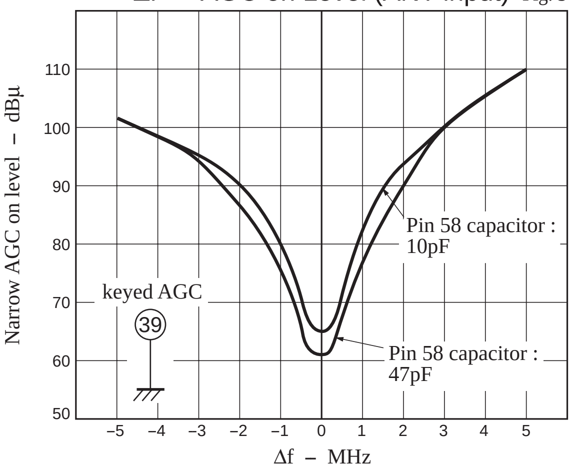
  This 3D AGC technique uses information that has the following three frequency characteristics and is a unique Sanyo-developed system for determining the high-band AGC level.

  RF and ANT circuit information : Mixer input AGC
  Mixer circuit information : Mixer output AGC
  CF selectivity information : S-meter output

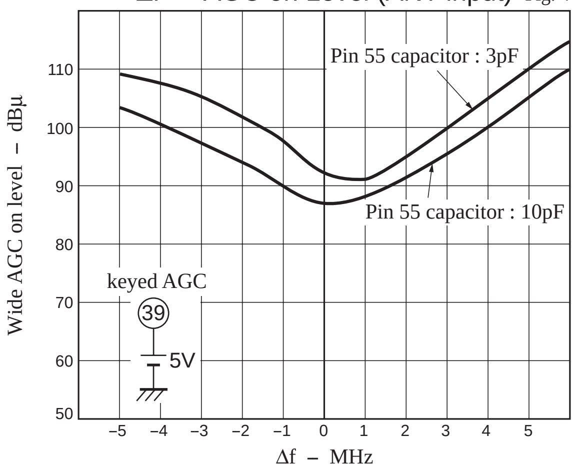
- 3D AGC Features

Feature	Merit
Only the narrow AGC sensitivity (operation at Δf < 1.5MHz) is controlled by the field strength of the desired station.	• Effective in resolving second-channel attenuation problems.
The narrow AGC sensitivity is controlled by a voltage (V_{23}) that is under 0.5V.	• Allows effective resolution of second-channel attenuation problems without under 0.5 V. degrading three-signal characteristics.
The wide AGC can operate even when V_{23} = 0 (when the desired station is not present).	• Seek operations may stop incorrectly due to the occurrence of intermodulation. • It is possible to prevent the occurrence of intermodulation in the RF tuning circuit and antenna in the presence of strong interfering stations, and blocking oscillation due to AGC operation can be prevented.
The narrow and wide AGC sensitivities can be set independently. (See figure 3 and 4.)	• Settings can be optimized for the field conditions.
The system has two AGC systems : narrow and wide AGC. (See figure 5.)	• Since the narrow AGC operates for the desired station and adjacent stations, the wide AGC sensitivity can be lowered and AGC malfunction due to local oscillator signal can be prevented.

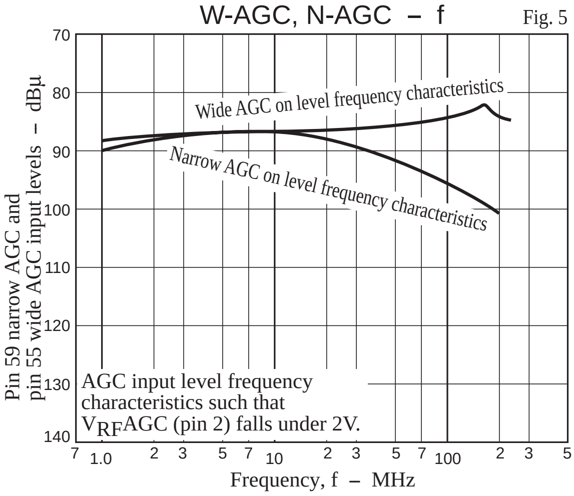
Δf – AGC on Level (ANT input) Fig. 3



Δf – AGC on Level (ANT input) Fig. 4



W-AGC, N-AGC – f Fig. 5



3D AGC Sensitivity Characteristics

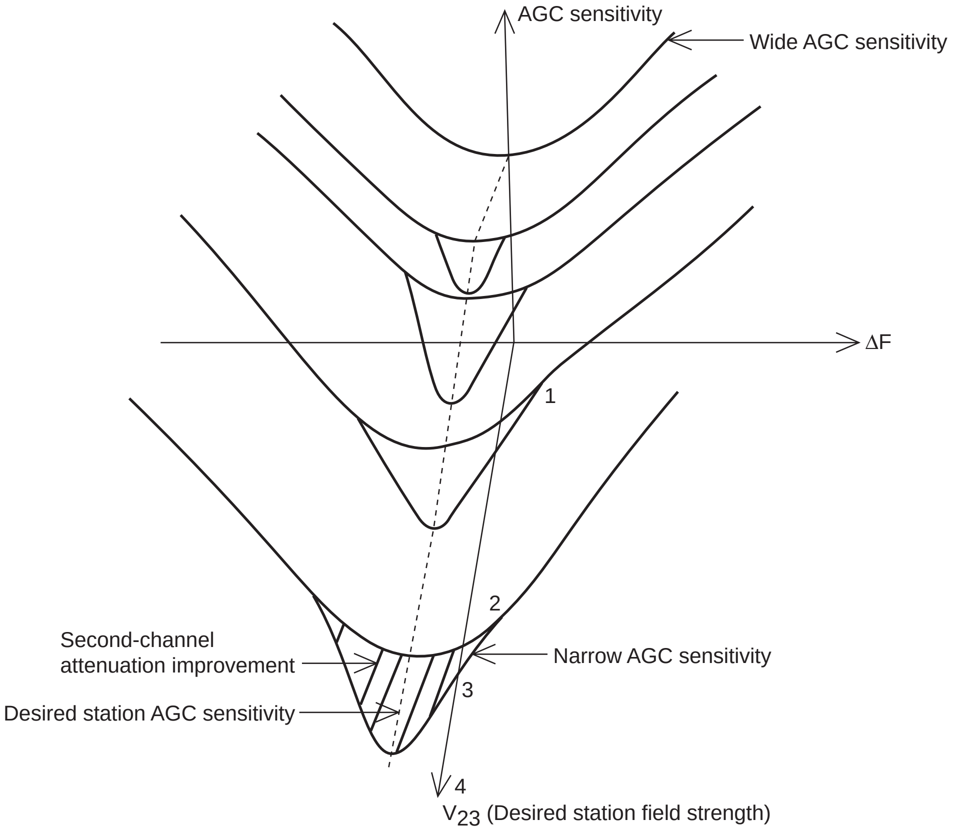


Fig. 6

Figure 6 3D AGC Sensitivity - Δf, V_{23} characteristics

- The wide AGC sensitivity is determined by the antenna and RF circuit selectivity, regardless of V23.
- The narrow AGC sensitivity is determined by the following.
  The total selectivity of the antenna, RF circuit, and mixer when $V_{23} \geq 0.5V$
  The above selectivity and V_{23} when $V_{23} < 0.5V$
- The improvement in the second-channel attenuation corresponds to the area occupied by the narrow AGC in the total AGC sensitivity area.
  Figure 8 on the next page shows the actual operation of the circuit.

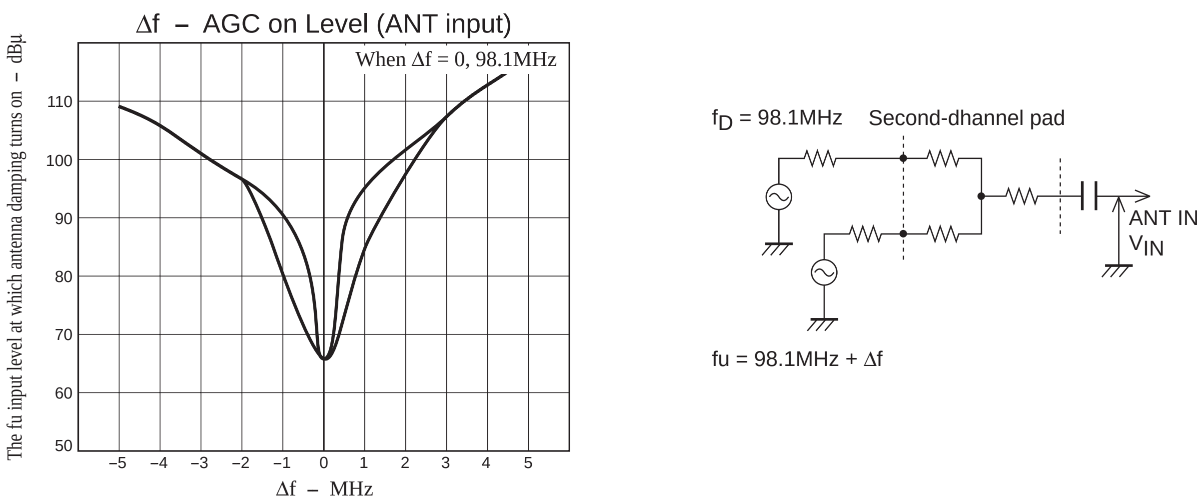


Fig. 7

Notes on 3D AGC (Keyed AGC)

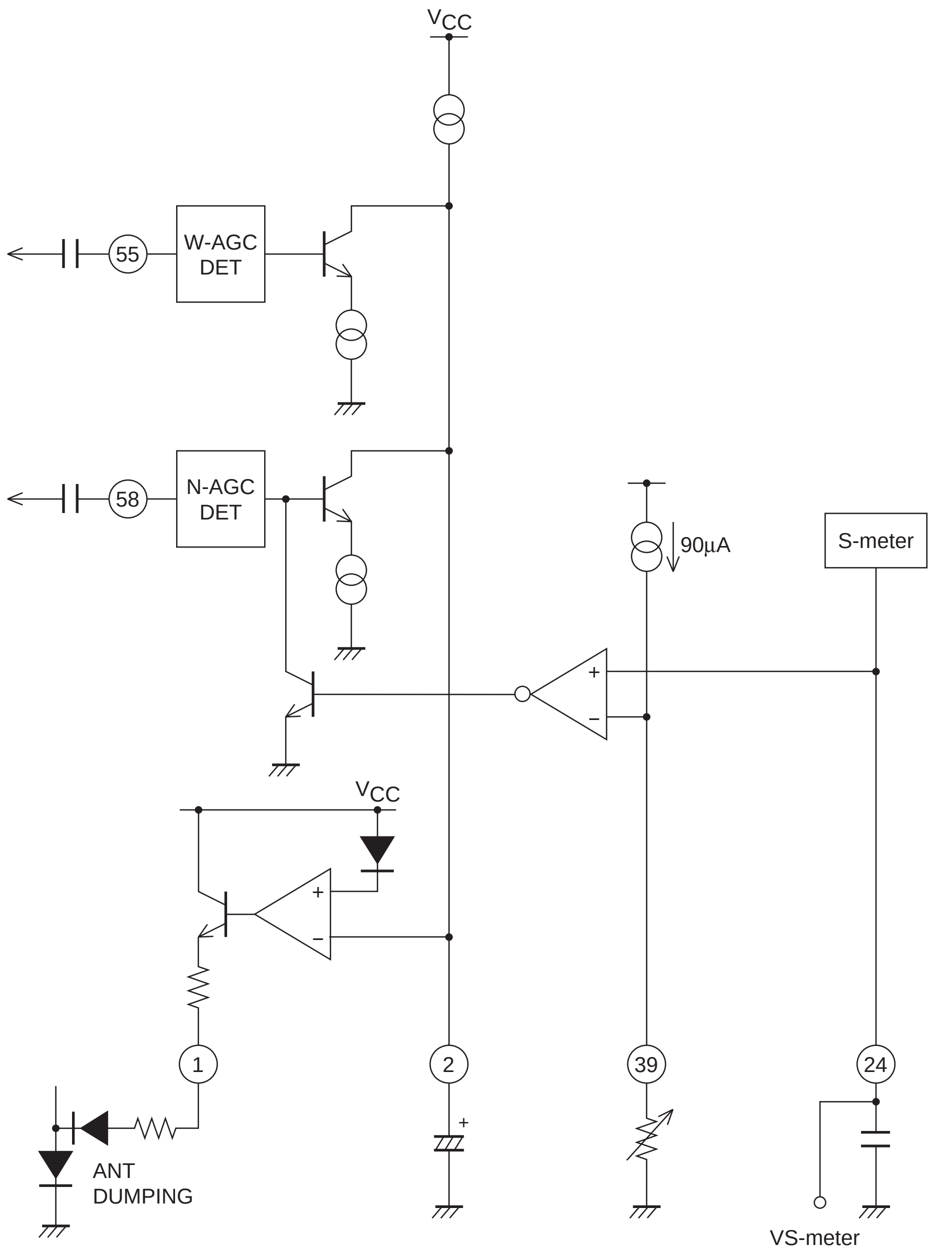


Fig. 8

- The antenna damping current from the pin due to the pin diode flows when the V2 pin reaches the V_{CC} - V_{BE} level.
- The narrow AGC operates as follows.
  When pin V39 > pin V24 : The narrow AGC turns off.
  When pin V39 < pin V24 : The narrow AGC turns on.

• The LA1781M includes two AGC circuits in its front end block.

(1) Antenna input limiter using a pin diode.

(2) FET second gate control

The AGC input pin is pin 59, and the AGC circuit turns on when a signal of about 30mVrms is input.

AGC activation

The pin diode drive circuit turns on when VCC - V2 is greater than or equal to about 1V, and input limitation is applied to the antenna circuit. In application circuits, there will be an attenuation of about 30 to 40dB. Next, when an adequate current flows in the antenna attenuator pin diode, the inductance falls, the FET second gate voltage drops, the FET gm falls, and the AGC operates. The recommended FET is the Sanyo 3SK263, which is an enhancement-type MOSFET. Therefore, full AGC is applied when the voltage, $V_{G2\text{-}S}$, between the second gate and the source is 0. Note that if a depletion-type MOSFET is used, AGC will not be applied unless $V_{G2\text{-}S}$ is less than 0.

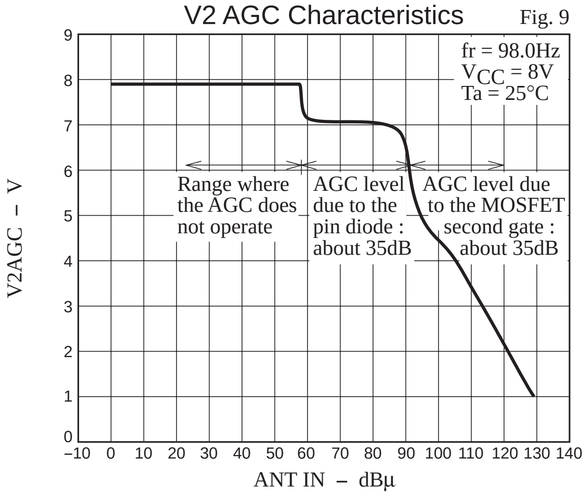


Fig. 9

• Mixer

The mixer circuit in this IC is a double-balanced mixer with both balanced input and balanced output.

Input circuit type

Emitter input

Input impedance : 25Ω

Due to optimized device geometry, emitter current, the bias, this IC achieves the following performance.

Mixer input usable sensitivity : 15dBμ

Mixer input IMQS : 90.5dBμ

(For an oscillator level of 200mVrms)

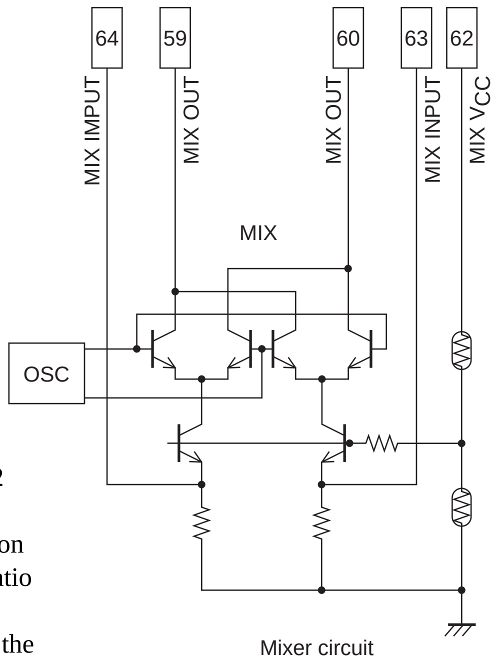
* The mixer input IMQS is defined as :

fr = 98.8MHz, no input

fu1 = 98.8MHz, 1kHz, 30% modulation

fu2 = 99.6MHz, no modulation

The interference 1 and 2 input levels such that generated intermodulation output signal-to-noise ratio becomes 30dB when an interference signal with the same level as the mixer input is input, and distortion occurs in the mixer.



Mixer circuit

Fig. 10

• Oscillator

Figure 11 shows the type of oscillator circuit used in this IC. It includes both an oscillator and an oscillator buffer.

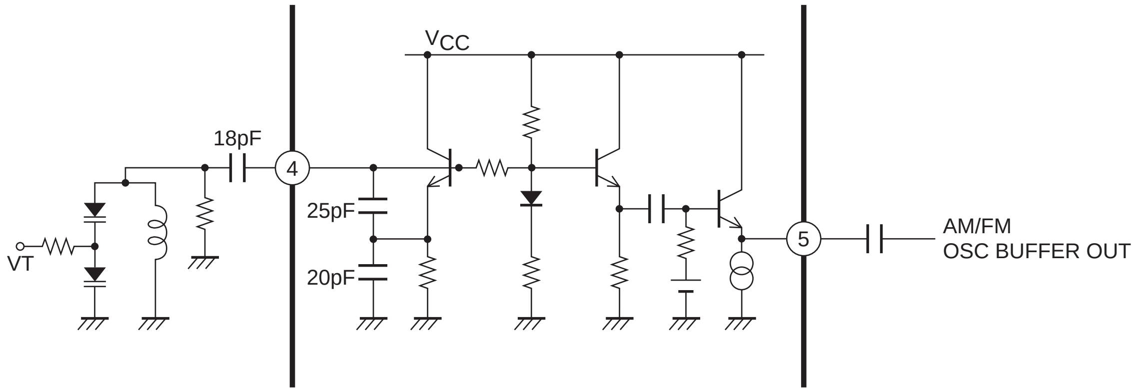


Fig. 11

• Figure 12 shows the type of FM first IF amplifier used in this IC. It is a differential single-stage amplifier.

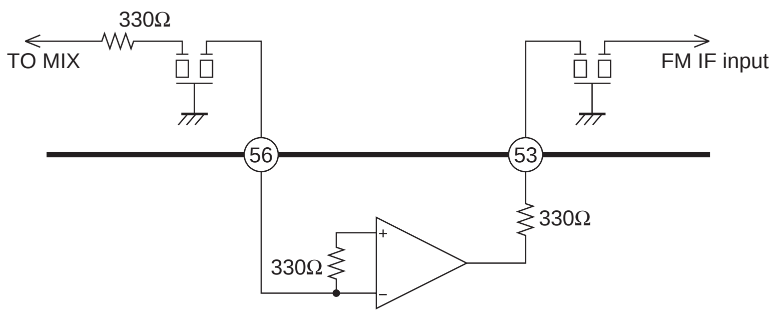


Fig. 12

Specifications

Input impedance : 330Ω

Output impedance : 330Ω

Gain : 20dB

5. FM IF

• Notes on the FM SD and SD adjustment

The figure below presents an overview of the FM SD and the IF count buffer.

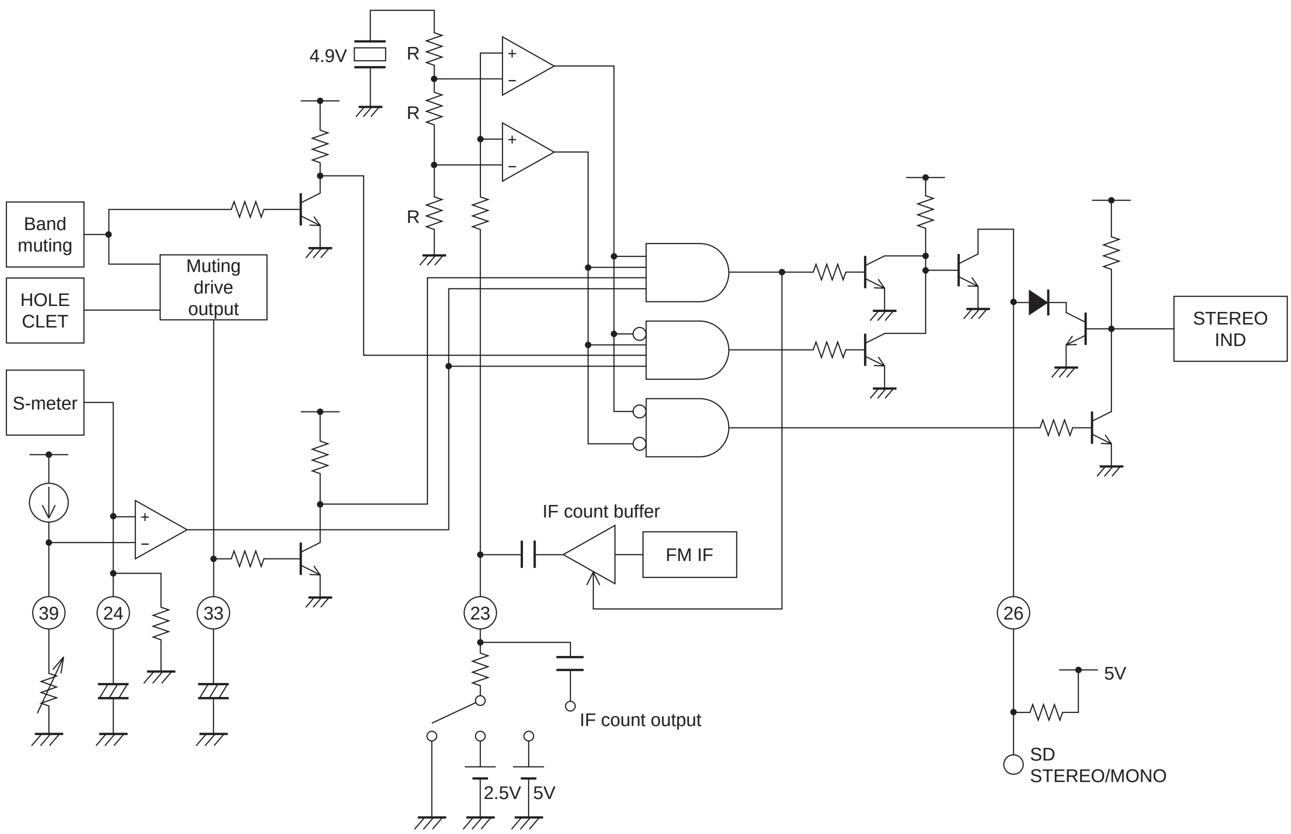


Fig. 13

Figure 14 shows the relationship between the FM SD, the IF count buffer output, the S-meter, and the muting drive output.

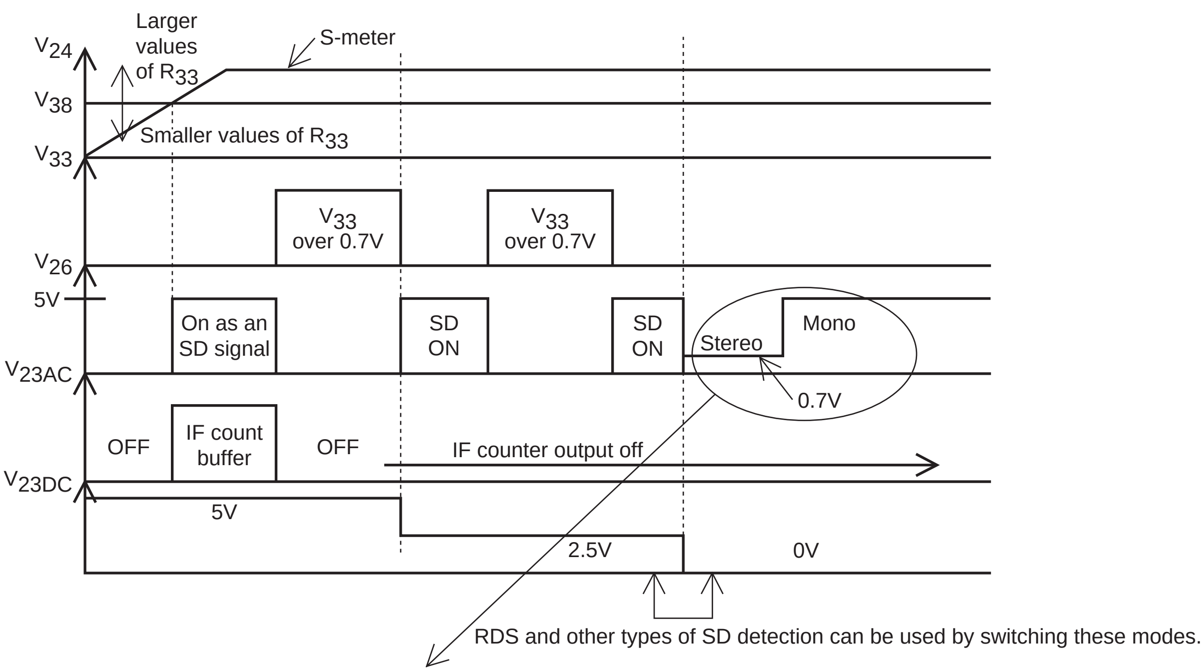


Fig. 14

- Transient response characteristics during automatic tuning

  The transient characteristics for SD and IF count buffer on/off operation are determined by the time constants of the RC circuits attached to the following pins.

  (1) Muting time constant : pin 33

  (2) S-meter time constant : pin 24

  (3) AFC time constant : pin 34

There are two points that require consideration when using fast tuning.

(1) The SD time constant due to the S-meter time constant

Since the current I24 (pin 24) varies with the field strength, the time constant also changes. There is no hysteresis in the comparator.

If C24 is made smaller and the pin 24 voltage is used for the keyed AGC pin 23, C23 must be chosen so that AGC during keyed AGC operation does not become unstable.

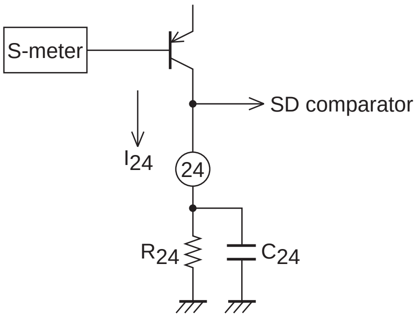
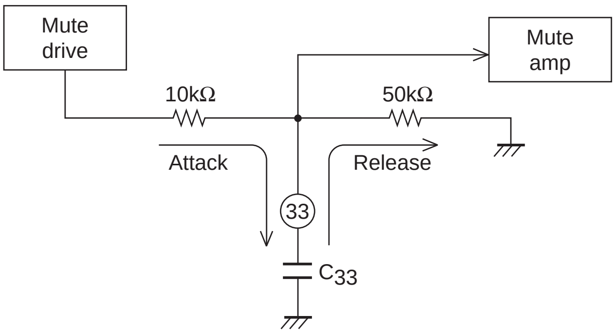


Fig. 15

(2) The SD time constant due to the pin 33 muting voltage time constant

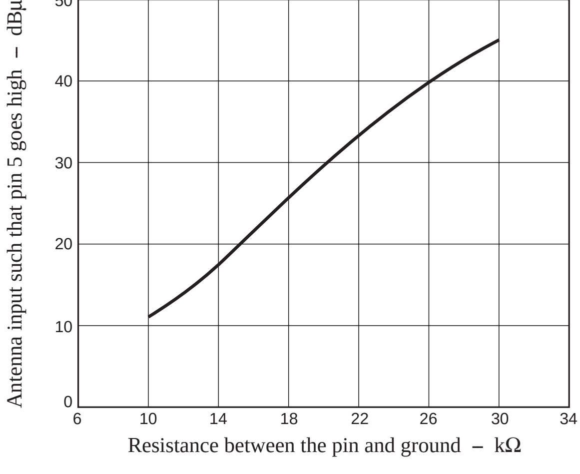
The changes in volume due to field fluctuation during weak field reception can be made smoother by setting the attack and release times during soft muting operation.



Muting time constants
Attack : 10kΩ × C33
Release : 50kΩ × C33

Fig. 16

SD Sensitivity Adjustment Fig. 17



However, when testing this stop sensitivity, note that when checking the waveform on the IF count buffer output (pin 23), there are cases, such as that shown below, where current in the test system may be seen as flowing to ground and cause oscillation that causes the IF count buffer output to go to the output state.

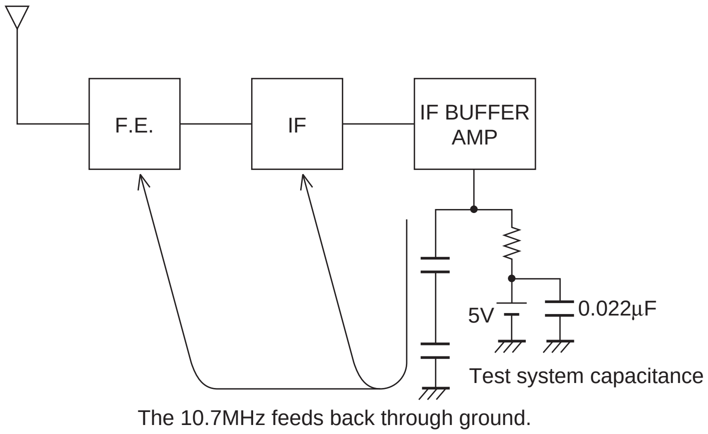


Fig. 18

• FM Muting control pin (pin 47) (R47 : 30kΩ variable resistor)
The -3dB limiting sensitivity can be adjusted with R47.

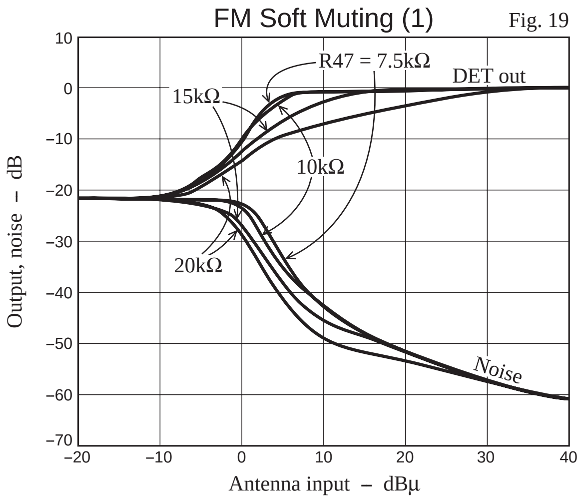
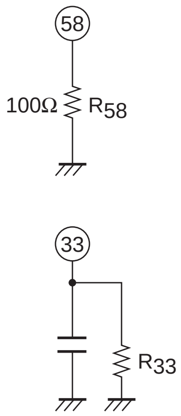


Fig. 19

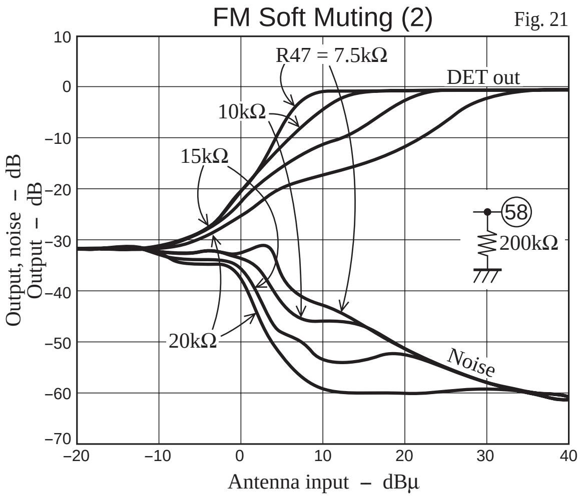
• FM muting attenuation adjustment (pin 58)
The muting attenuation can be switched between the three levels of -20, -30, and -40dB by the resistor inserted between pin 58 and ground. (Note that the exact values depend on the total tuner gain.)
The noise convergence with no input is determined by the pin 58 voltage.

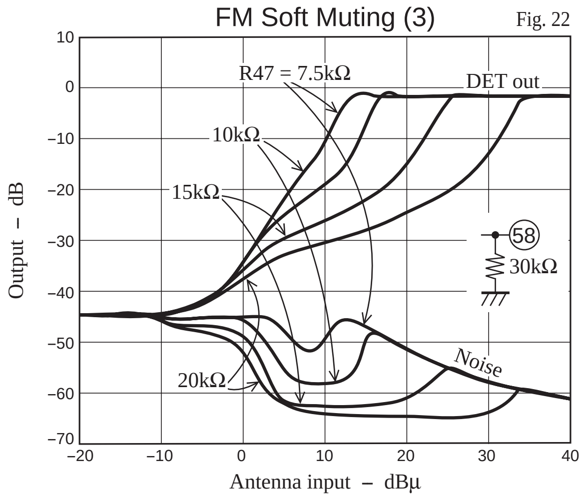


R58	Mute ATT
Open	-20dB
200kΩ	-30dB
30kΩ	-40dB

The attenuation can be set by making R33 smaller as listed in the table above.

Fig. 20





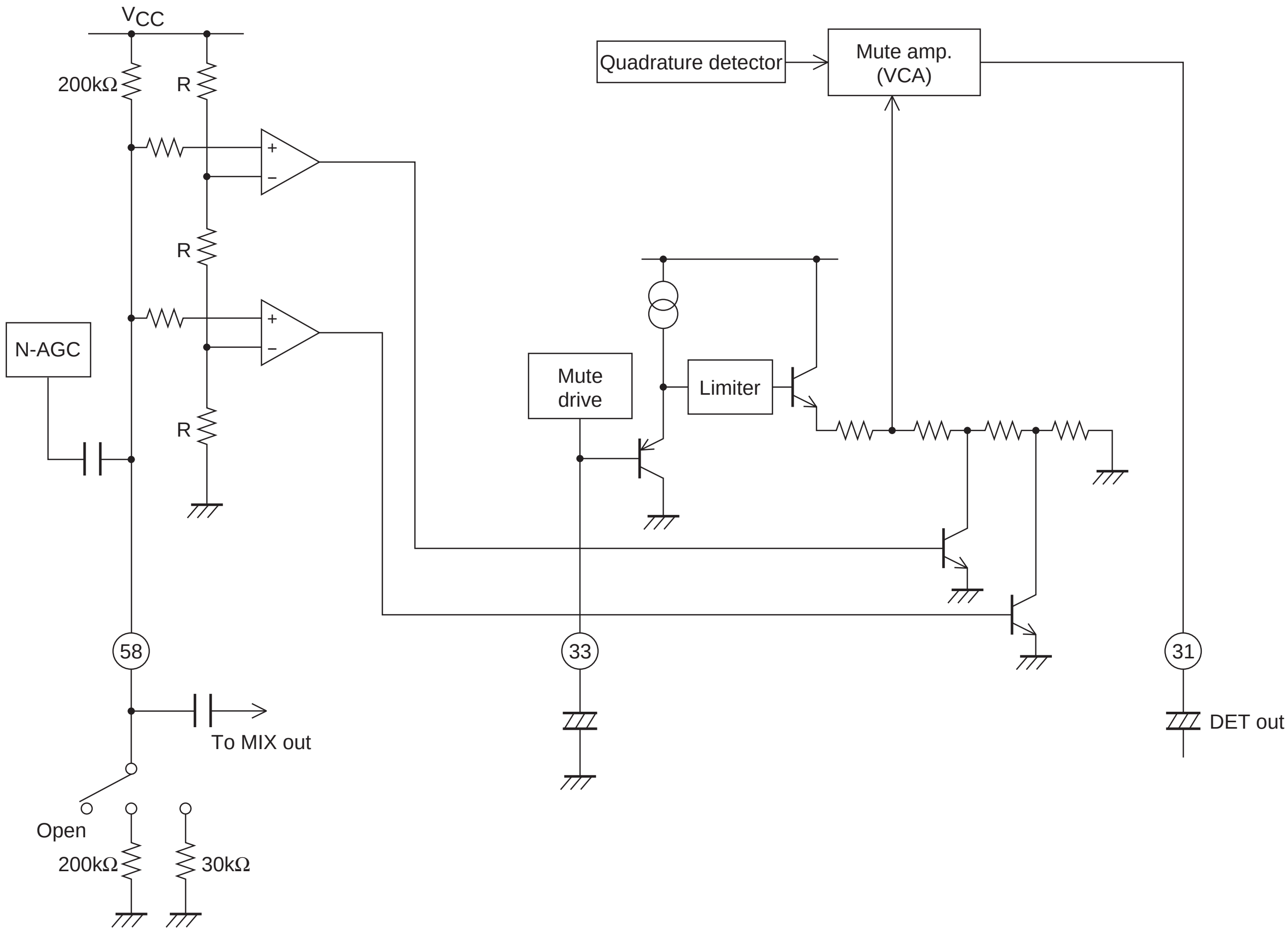


Fig. 23

• FM muting off function

Forcing this pin to the ground level turns muting off.

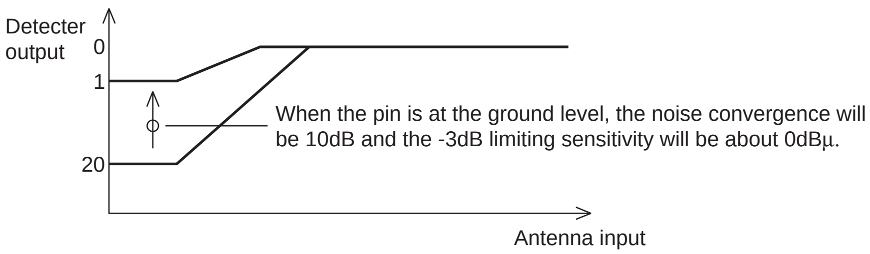


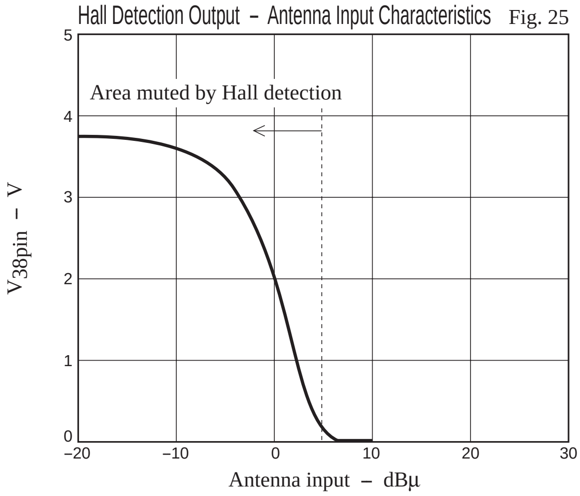
Fig. 24

• Hall detection

The Hall detection function detects the level of the pin 36 quadrature input signal and then applies peak detection to that result. The result is output from pin 33. This circuit has three effects.

(1) It assures that muting will be applied for weak inputs with an antenna input of under 5dBμ. The amount of attenuation is referenced to an antenna input of 60dBμ, fm = 1kHz, and a 22.5kHz dev output, and is variable from 10dB to 40dB when there is no input. Thus one feature of this circuit is that the weak input noise attenuation and the -3dB limiting sensitivity for over 5dBμ inputs can be set independently.

Hall Detection Output – Antenna Input Characteristics Fig. 25



(2) When the pin 36 quadrature input is a saturated input, the pin 36 noise level (Va) is detected and a peak-hold function is applied to pin 33 (Vb) for locations rapid field strength variations and severe multipath occurs for fields that result in an antenna input level of over 5dBμ.

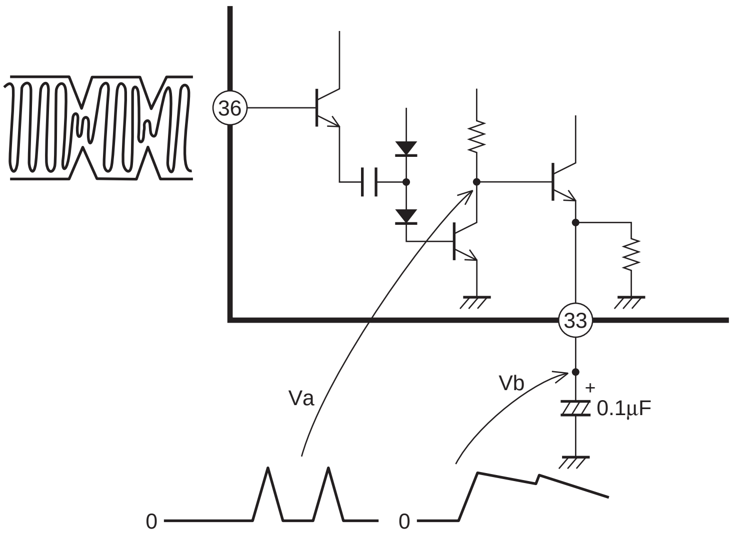
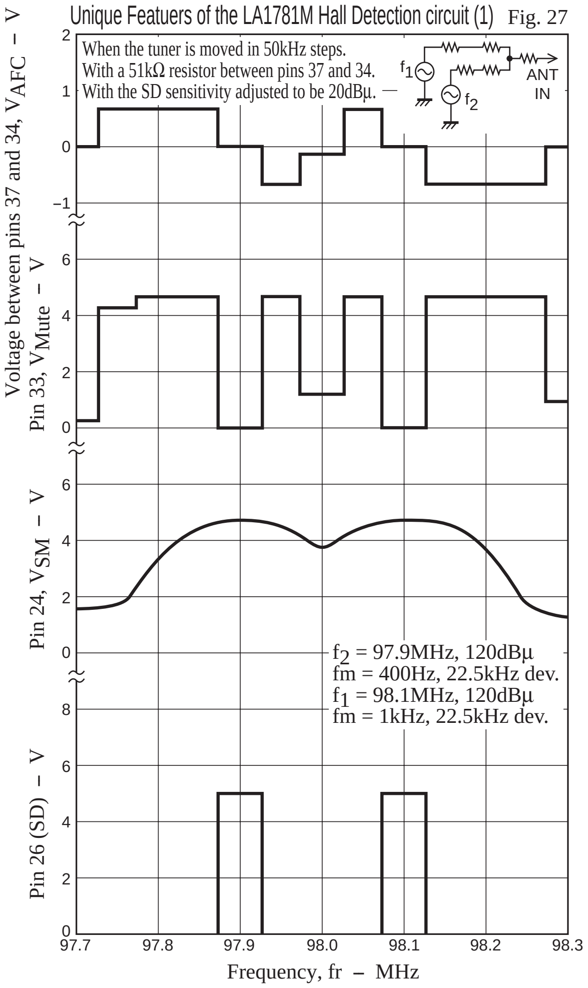


Fig. 26

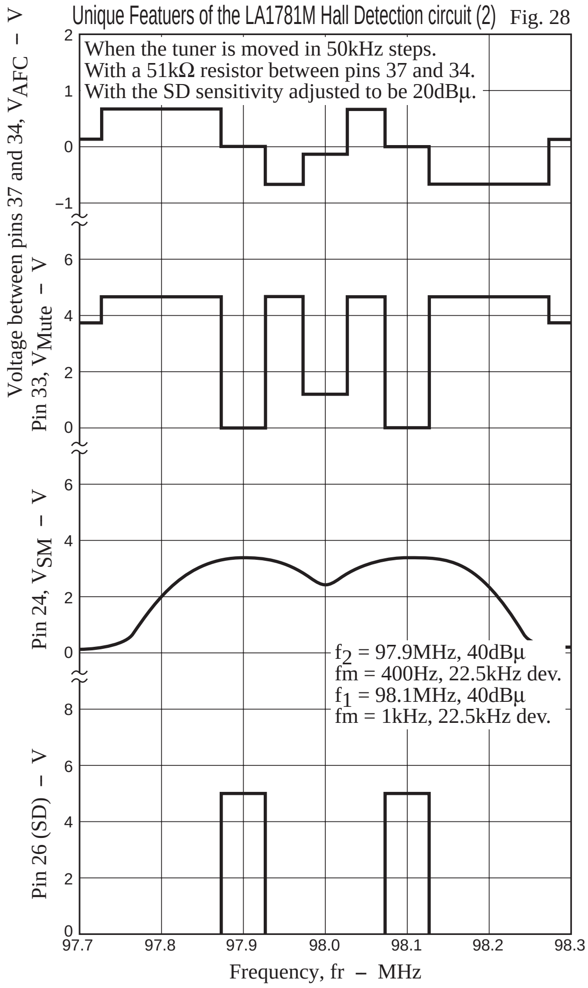
(3) Unique features

One unique feature of the LA1781M is that if there are adjacent stations such that f_1 = 98.1MHz and f_2 = 97.9MHz, a search operation will not stop at 98.0MHz. Since V_{AFC} = 0V and V_{SM} = 3.6V at 98.0MHz in the situations shown in figure 27 and 28, even though Hall detection would normally not operate and SD would be high, in this IC the Hall detection circuit will operate, V_{Mute} will be set to 1.2V (over 0.7V) and the SD signal will go low, thus preventing incorrect stopping of the search.

Unique Featuers of the LA1781M Hall Detection circuit (1) Fig. 27



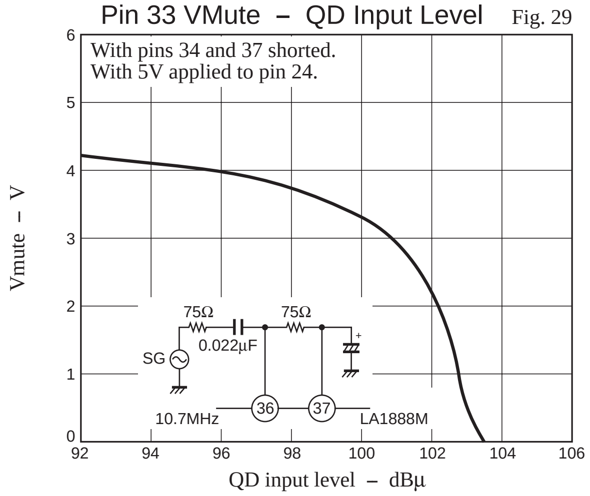
Unique Featuers of the LA1781M Hall Detection circuit (2) Fig. 28



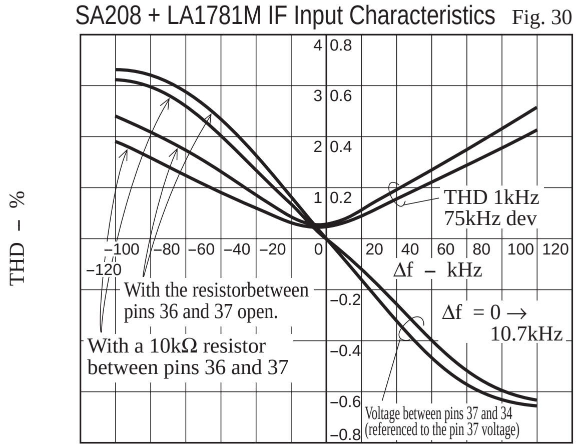
- Notes on the quadrature input level

  When a strong field is being received the quadrature signal input (pin 36) requires a 200mVrms input, and the detection transformer and the damping resistor between pins 36 and 37 must be designed.
  (We recommend the Sumida SA-208 transformer and a 10kΩ resistor between pins 36 and 37.)
  When the pin 36 input level falls below 160mVrms, the Hall detection circuit operates and the pin 33 mute drive output voltage increases. Therefore, when pin 36 input is from 160 to under 200mV rms during strong field reception, the muting circuit may or may not operate due to sample-to-sample variations between individual ICs.
  Furthermore, the SD function may not operate, and the audio output level may be reduced. Incorrect operation due to sample-to-sample variations and temperature characteristics can be prevented by keeping the pin 36 voltage at 200mVrms or higher.

Pin 33 VMute – QD Input Level Fig. 29



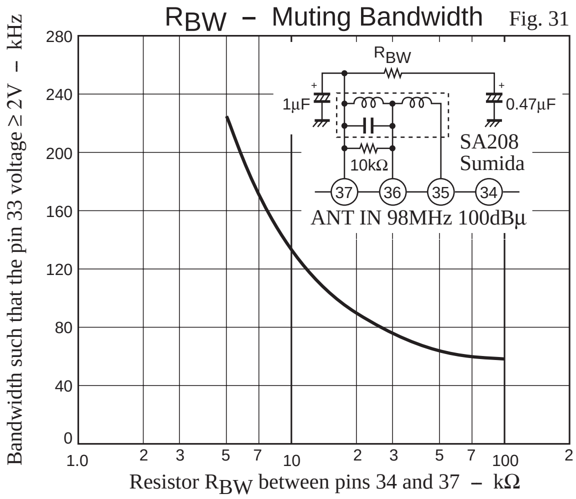
SA208 + LA1781M IF Input Characteristics Fig. 30



$R_{36\text{-}37}$	Detector output MPX OUT V_O	Pin 36 AC level QDIN
Open	330mVrms	235mVrms
10kΩ	280mVrms	200mVrms

- Band Muting Adjustment Procedure

  The muting bandwidth can be modified as shown in figure 31 with the resistor RBW between pin 34 and 37.



6. AM

- AM AGC system

  The LA1781M RF AGC circuit takes its input from three sources : the WIDE AGC pin (pin 46), the MIDDLE AGC pin (pin 49) and NARROW AGC. There is also an IF AGC circuit.

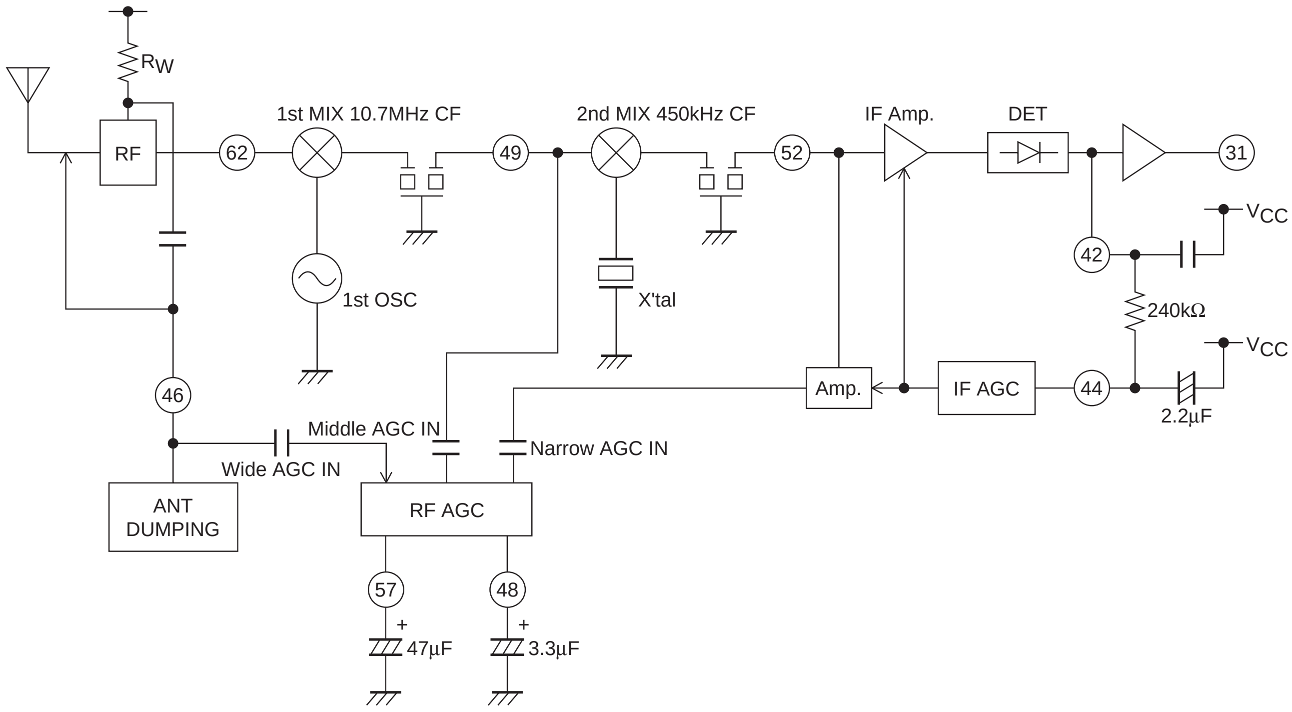
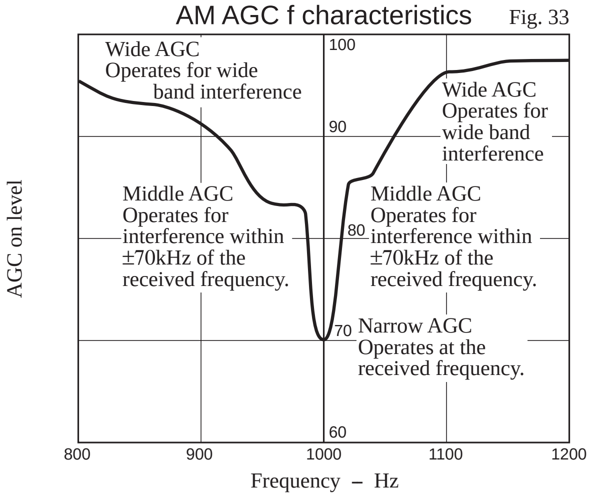
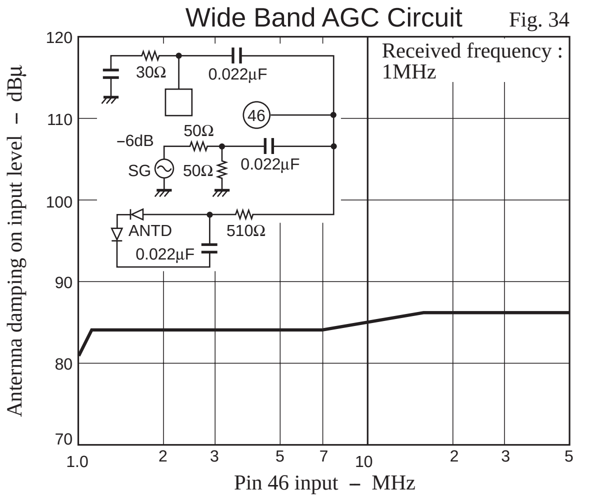


Fig. 32





The wide band AGC circuit in this IC has the frequency characteristics shown above. The pin 46 input frequency characteristics are identical to those of the RF amplifier gate. This AGC circuit serves to prevent distortion at the FET input when a strong signal is applied to the antenna circuit. The level at which the AGC circuit turns on can be adjusted to an arbitrary level with the wide band AGC adjustment resistor. A delayed AGC on level can be handled by reducing the value of the adjustment resistor.

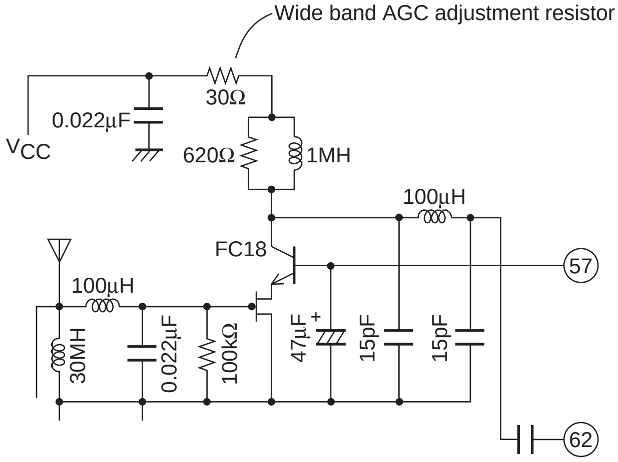


Fig. 35

• Notes on AM SD (pin 26) and the SD adjustment pin

SD and the IF buffer are operated by comparing the S-meter level (V24) and the 5V reference voltage as shown in figure 36.

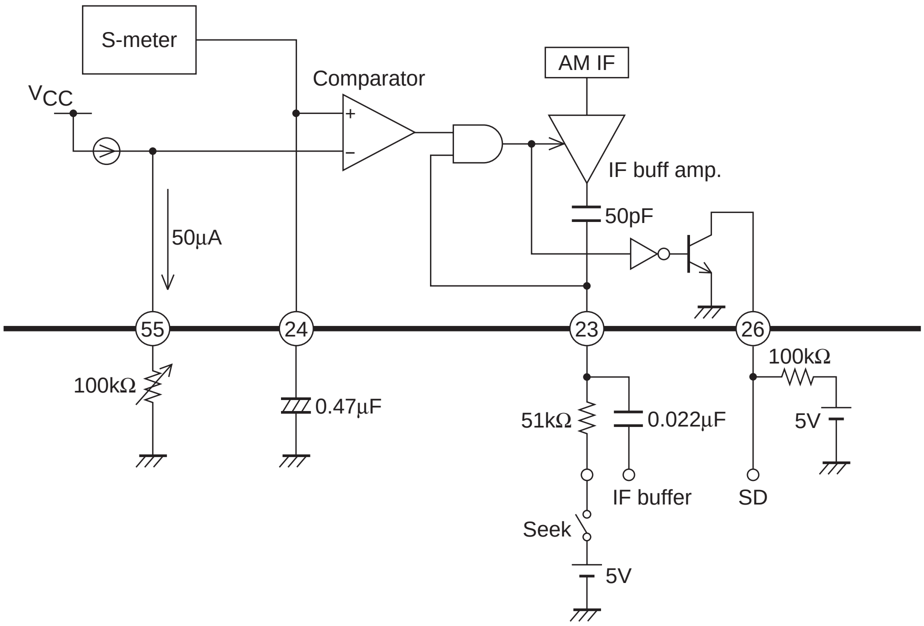


Fig.36

Figure 37 shows the relationship between the AM SD, the IF count buffer, and the S-meter.

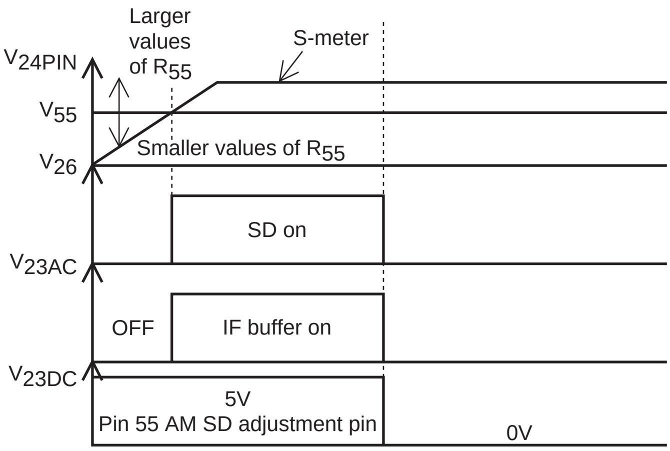


Fig.37

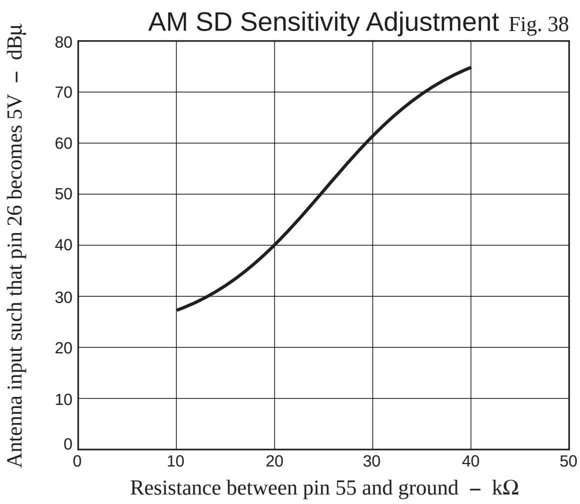


Fig. 38

• AM high band cut and detector output level adjustment methods

The pin 31 AM and FM tuner output has an impedance of 10kΩ in AM mode and a few tens of Ohms in FM mode. Therefore, R31 is used to lower the AM detector output level and C31 determines the AM high band frequency characteristics.

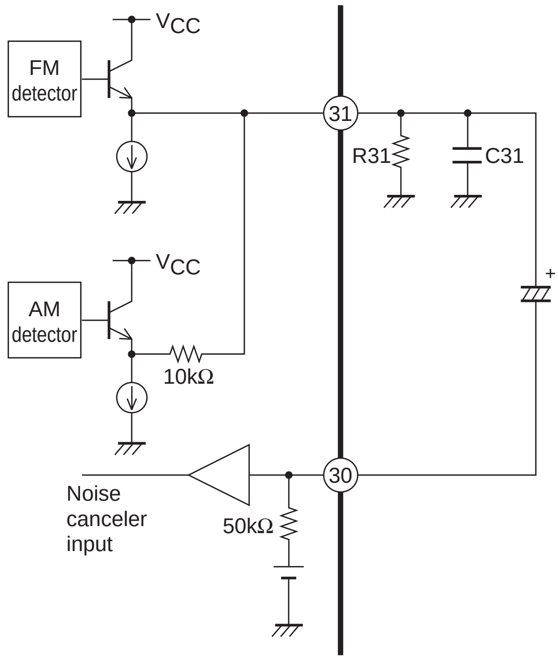


Fig. 39

• AM stereo system pins

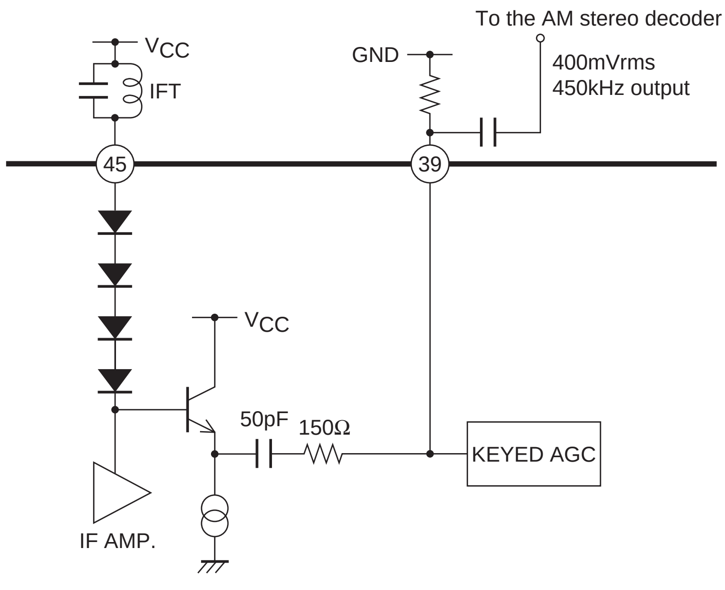


Fig. 40

• AM low band cut adjustment method

The AM low band frequency characteristics can be adjusted with C42, which is inserted between pin 42 and V_{CC}. Since the detector is designed with V_{CC} as the reference, C42 must be connected to V_{CC}.

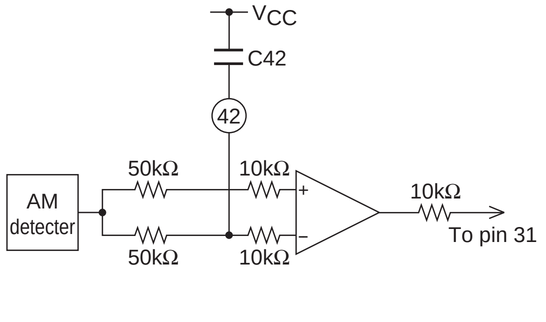


Fig. 41

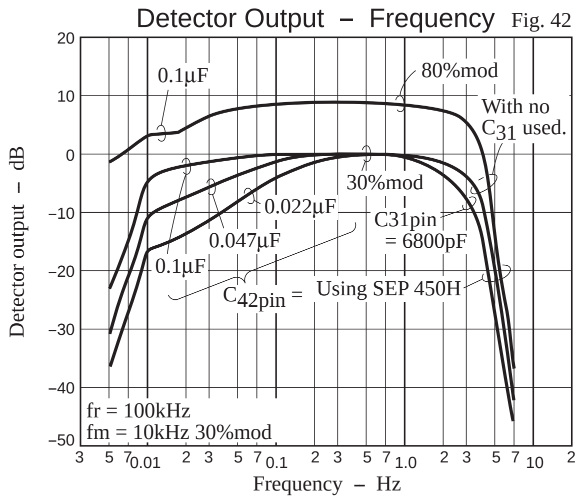


Fig. 42

7. Noise Canceler Block

• The noise canceler input (pin 30) has an input impedance of about 50kΩ. Check the low band frequency characteristics carefully when determining the value of the coupling capacitor used. Note that f_C will be about 3Hz when a 1µF capacitor is used in the application.

• Pins 8 and 9 are used to set the noise detector sensitivity and the noise AGC. It is advisable to first set the noise sensitivity for a medium field (an antenna input of about 50dBµ) with pin 8 (the noise sensitivity setting pin), and then set the AGC level for a weak field (20 to 30dBµ) with pin 9 (the AGC adjustment pin). If the noise sensitivity is increased, the AGC will become more effective but, inversely, the weak field sensitivity will be reduced.

Noise canceler 10kHz overmodulation malfunction may be a problem. In particular, when an overmodulated signal is input, the noise canceler may, in rare cases, malfunction. This is due to the fact that the IF detector output has a waveform of the type shown in figure 43 due to the bands of the IF ceramic filters as shown below. (Here, the antenna input is 60dBµ, the ceramic filters are 150kHz × 1 and 180kHz × 2, f = 10kHz, 180kHz dev.) The noise canceler reacts to the spikes (whiskers) generated due to this overmodulation, which results in distortion to the audio output. (The spike components due to overmodulation occur due to the bands of the ceramic filters in the tuner.) The following describes a method for resolving this problem. This incorrect operation due to overmodulation is prevented by removing the spike components due to this overmodulation with a low-pass filter consisting of a 1kΩ resistor and a 2200pF capacitor shown in figure 44. However, note that the FM separation characteristics in the high band and the AM frequency characteristics will change.

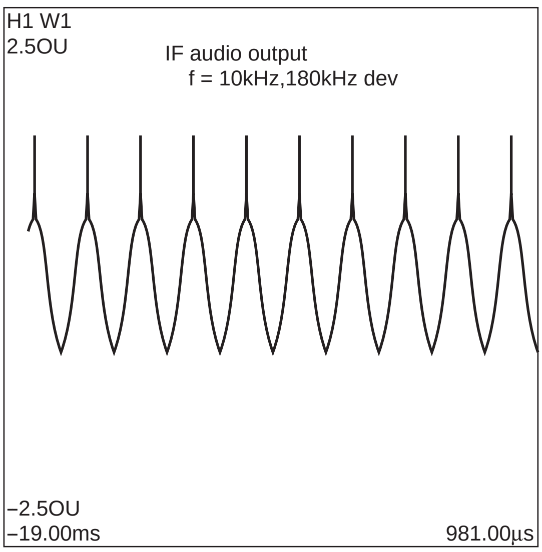


Fig. 43

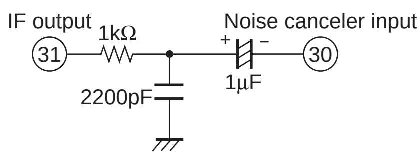


Fig.44

8. Multiplexer Block

• HCC (high cut control) frequency characteristics (pin 41)

When the HCC function operates, the frequency characteristics of the output signal are determined by the capacitance of the external capacitor connected to pin 41.

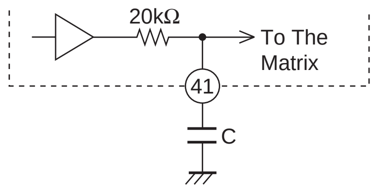


Fig. 45

$$f_C = \frac{1}{2\pi \times C \times 20k\Omega} \text{ [Hz]}$$

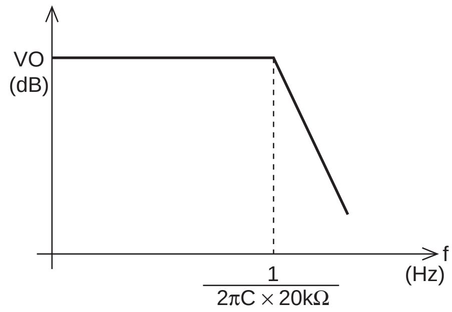


Fig. 46

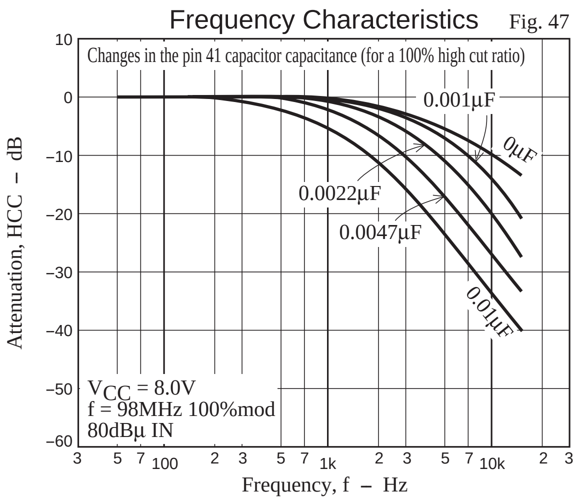


Fig. 47

• Pilot canceler adjustment (pins 17 and 18)

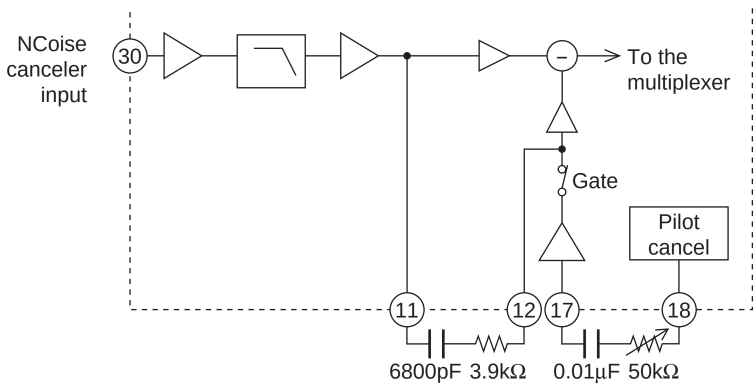


Fig. 48

The pilot canceler signal waveform (pin 19) is a 19kHz signal that contains no third harmonic as shown in figure 48. Since this signal has the same phase as the pilot signal, no capacitor is required between pin 18 and ground. Since it has no third harmonic component, excellent pilot cancellation can be acquired in both the left and right channels by adjusting with a variable resistor.

• Separation adjustment (pin 19)

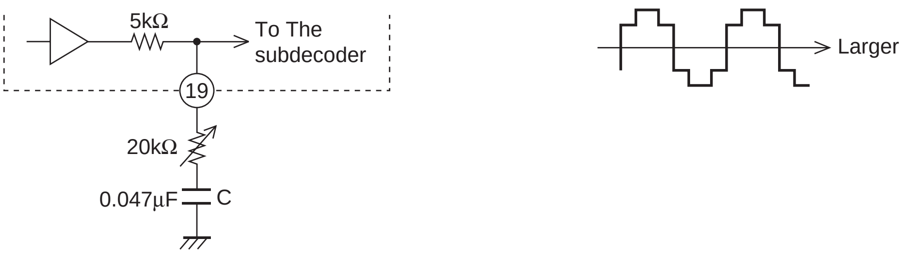


Fig. 49

The separation is adjusted by modifying the input level to the subdecoder with the variable resistor connected to pin 19. Since only the sub-modulation level is changed by changing the variable resistor setting, the monaural (main) output level is not changed. Furthermore, degradation of high band separation in the decoder can be avoided if the impedance of the external capacitor (C) in the subchannel frequency band (23 to 53kHz) is made sufficiently smaller than the variable resistor.

9. MRC Circuit

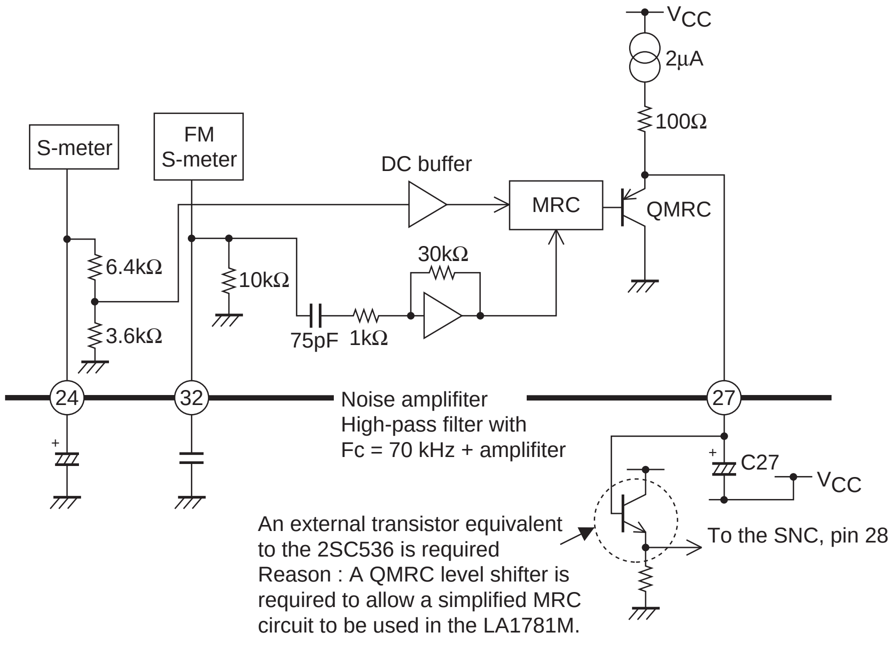


Fig. 50

(1) When there is no AC noise on pin 32

$V_{24} = V_{27} - V_{BE}$
↑
QMRC

V27 is about 2.5V when the antenna input is 60dB or higher.

(2) Since the MRC noise amplifier gain is fixed, the MRC circuit is adjusted by reducing the AC input level.

Fig. 51

(3) The MRC attack and release are determined by C27 on pin 27.

Attack : 7µA × C27 → 2µA × C27

Release : 500Ω × C27 → 100Ω

Notes on the Noise Canceler

The noise canceler characteristics have been improved by implementing the circuit that determines the gate time in logic. Since the time constant in earlier noise cancelers was determined by an RC circuit such as that shown in figure 52, the rise time shown in figure 53 was influenced by the values of the resistor and capacitor used. As a result the noise exclusion efficiency was reduced by this delay in the rise time. In the LA1781M, this rise time was shortened by implementing the circuit that determines the gate time in logic, allowing it to reliably exclude noise.

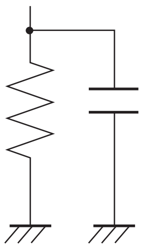


Fig. 52

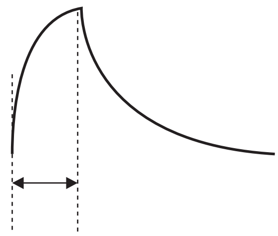


Fig. 53

Gain Distribution (FM)
This section investigates the gain in each block in the LA1781M when the Sanyo recommended circuits are used.

(Test conditions)
Ambient temperature : 26°C
Antenna and mixer input frequency : 98.1MHz
First and second IF input frequency : 10.7MHz
The input levels when V_{SM} = 2V will be as follows.
ANT IN : 19dBμ
MIX IN : 30dBμ
1st IF IN : 42dBμ
2nd IF IN : 60dBμ
When the gains for each block are determined according to the above, the results are as follows.
RF GAIN : 11dB
MIX GAIN : 12dB
1st IF GAIN : 18dB

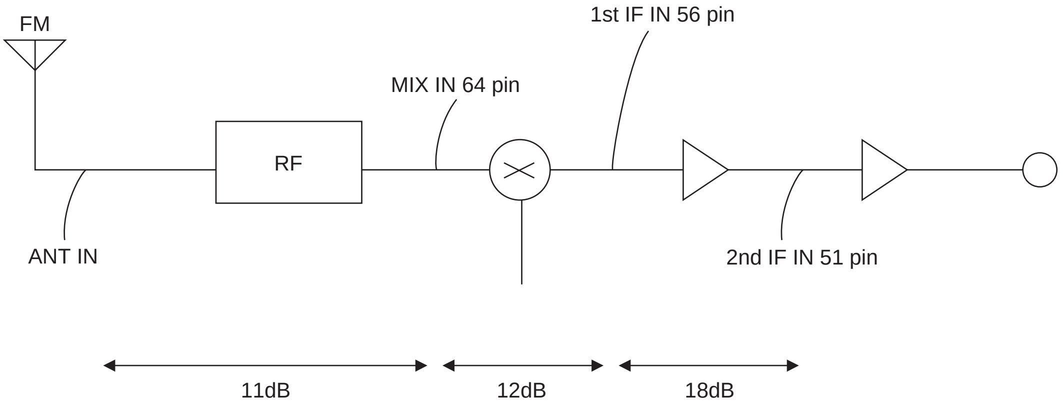


Fig. 54

(AM)
This section investigates the gain in each block in the LA1781M when the Sanyo recommended circuits are used.

(Test conditions)
Ambient temperature : 26°C
Antenna and mixer input frequency : 1MHz
First and second mixer input frequency : 10.7MHz
Second IF input frequency : 450kHz

The gains at each stage will be as follows.
RF Gain (ANT IN-pin62) : 17dB
1st MIX Gain (pin62-pin56) : 8dB
1st IF Gain (pin55-pin53) : 15dB

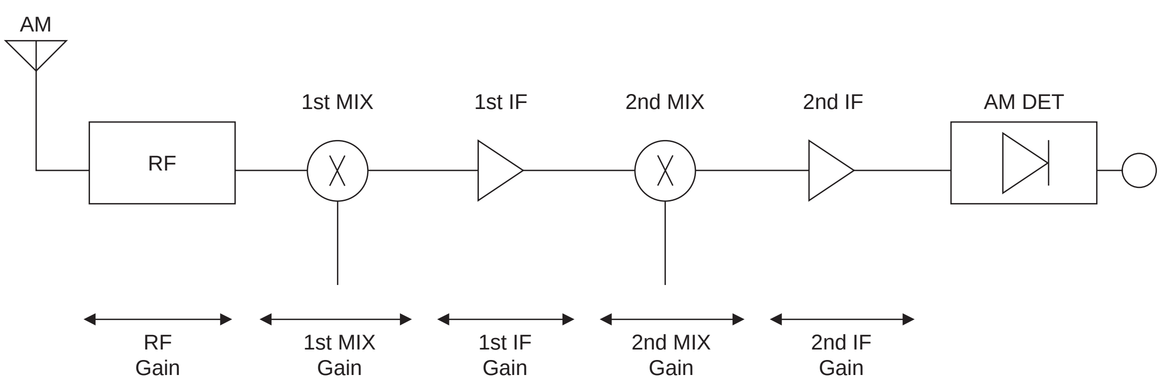
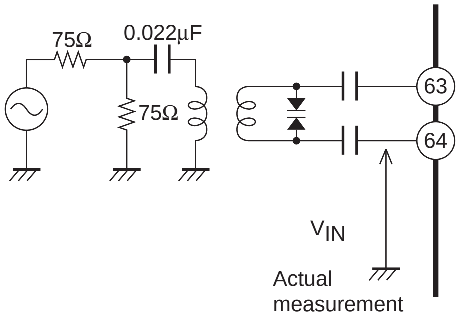


Fig. 55

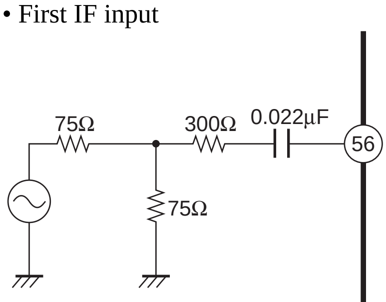
Input Circuits for Each Stage

[FM]

• Mixer input

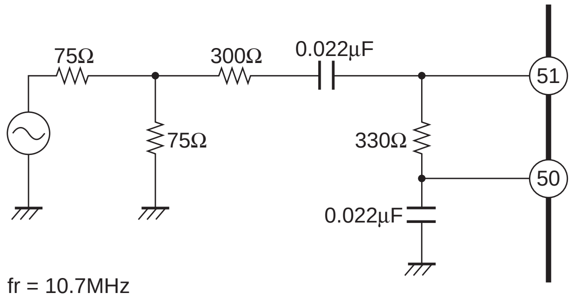


• First IF input



fr = 10.7MHz

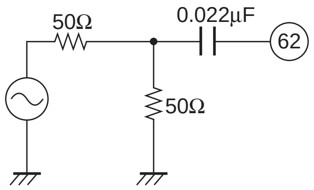
• IF input



fr = 10.7MHz

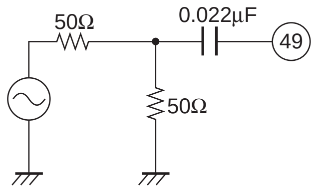
[AM]

• First mixer input



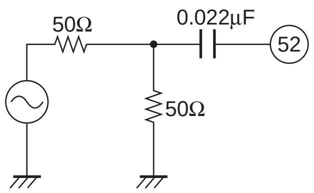
fr = RF

• Second mixer input



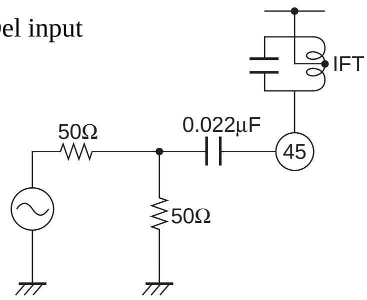
fr = 10.71MHz (f2nd osc + 0.45MHz)

• IF input



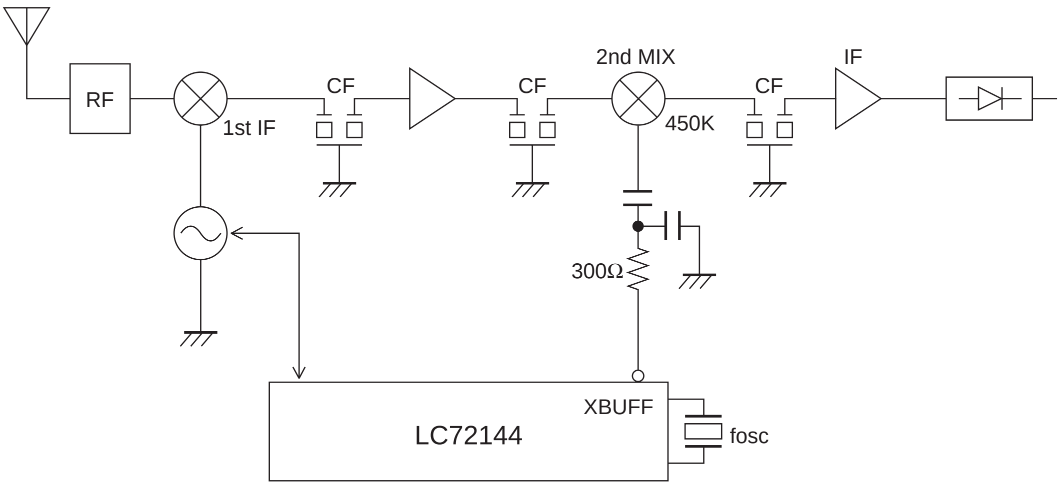
fr = 450kHz

• Del input

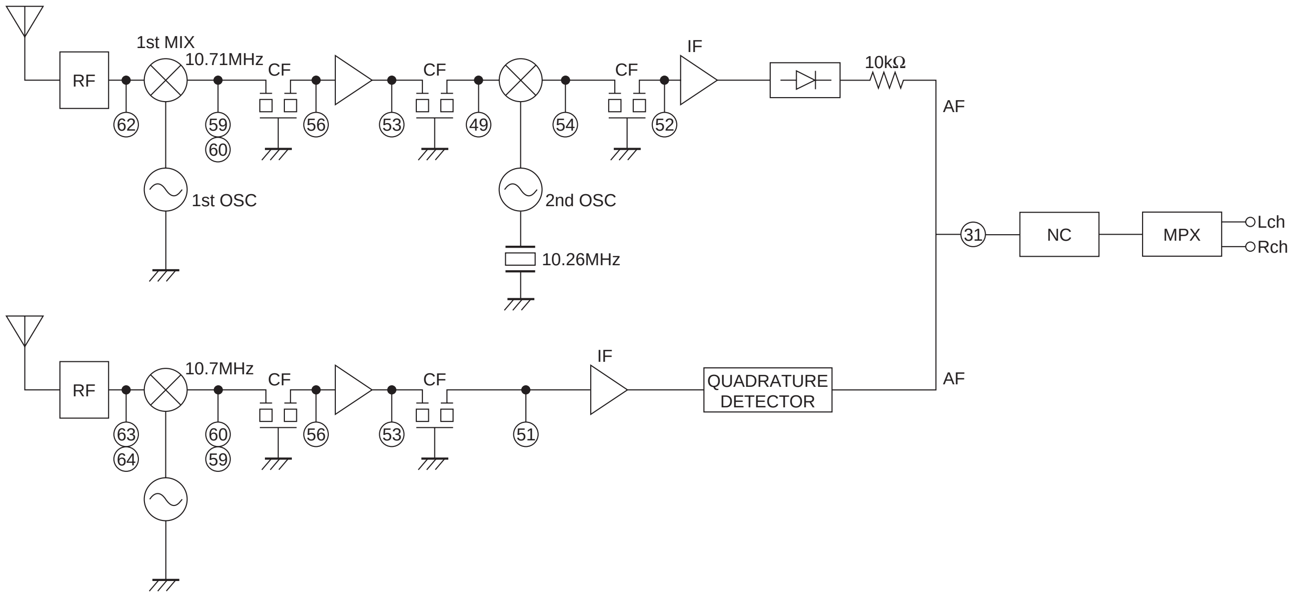


fr = 450kHz

Sample AM tuner Circuit with the LC72144 Used Together



		AM 1st IF	Step	FM IF
1	f_{OSC} 10.25NHz	10.7MHz	10kHz, 11kHz	10.7MHz
2	f_{OSC} 10.35NHz	10.8MHz	9kHz, 10kHz	10.8MHz



Crystal Oscillator Element

Kinseki, Ltd.

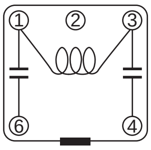
Frequency : 10.26MHz
CL : 20pF
Model No. : HC-49/U-S

Coil Specifications

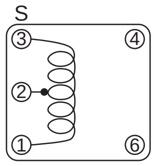
Sumida Electronics, Ltd.

[AM Block]

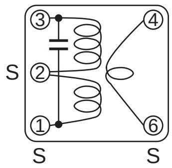
AM FILTEER (SA-1051)



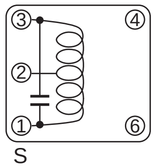
AM OSC (SA-359)



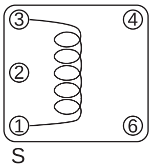
AM IF1 (SA-264)



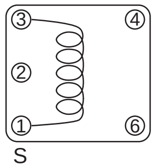
AM IF2 (SA-1063)



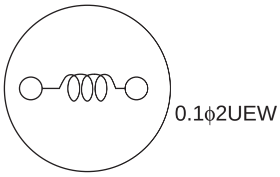
AM loading (SA-1062)



AM ANT IN (SA-1048)

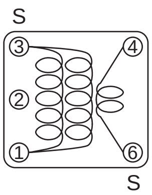


AM RF amplifier (RC875-222J)

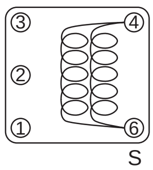


[FM Block]

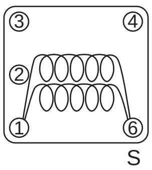
FM RF (SA-1060)



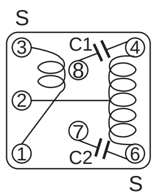
FM ANT (SA-1061)



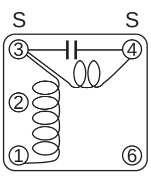
FM OSC (SA-1052)



FM MIX (SA-266)



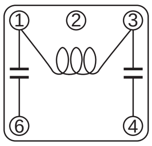
FM DET (SA-208)



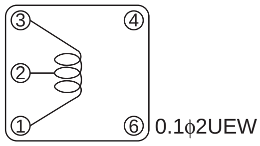
The Toko Electric Corporation

[AM Block]

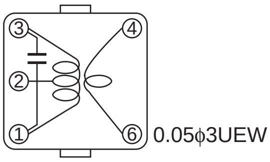
AM FILTEER (A286LBIS-15327)



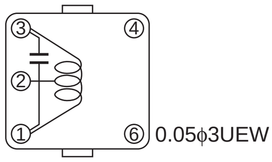
AM OSC (V666SNS-213BY)



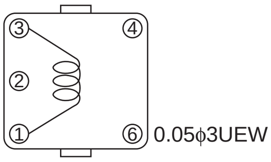
AM IF1 (7PSGTC-5001A = S)



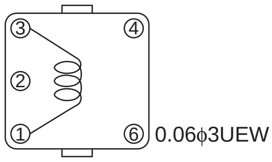
AM IF2 (7PSGTC-5002Y = S)



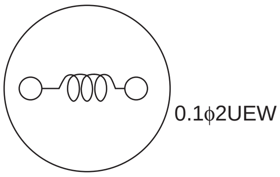
AM loading (269ANS-0720Z)



AM ANT IN (385BNS-027Z)

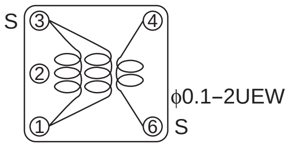


AM RF amplifier (187LY-222)

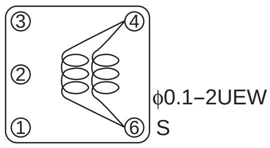


[FM Block]

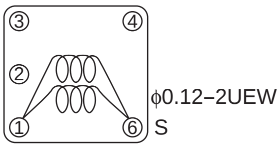
FM RF (V666SNS-208AQ)



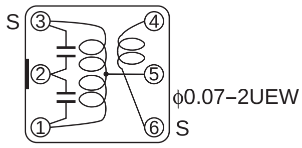
FM ANT (V666SNS-209BS)



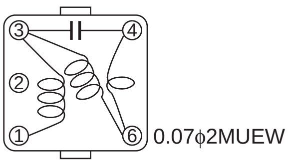
FM OSC (V666SNS-205APZ)



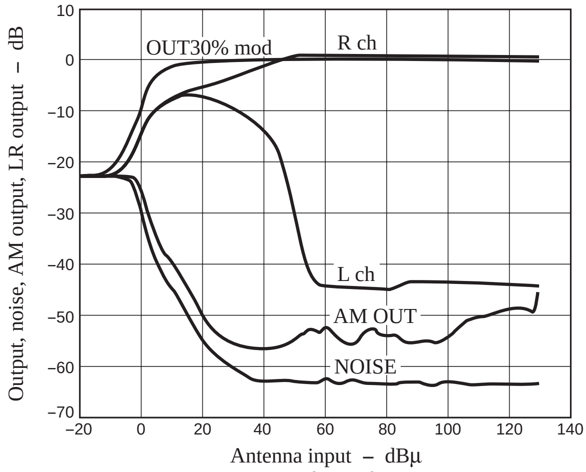
FM MIX (371DH-1108FYH)



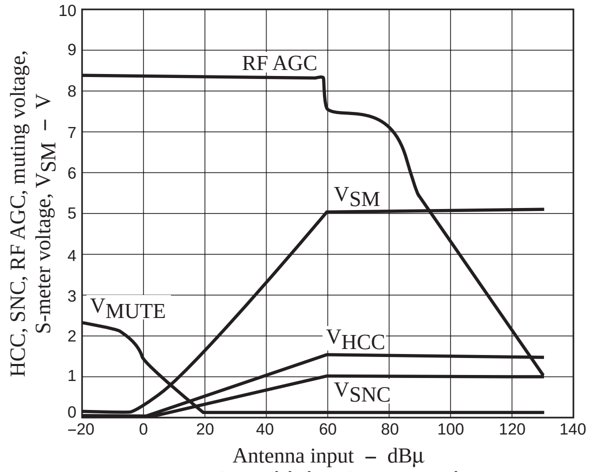
FM DET (DM6000DEAS-8407GLF)



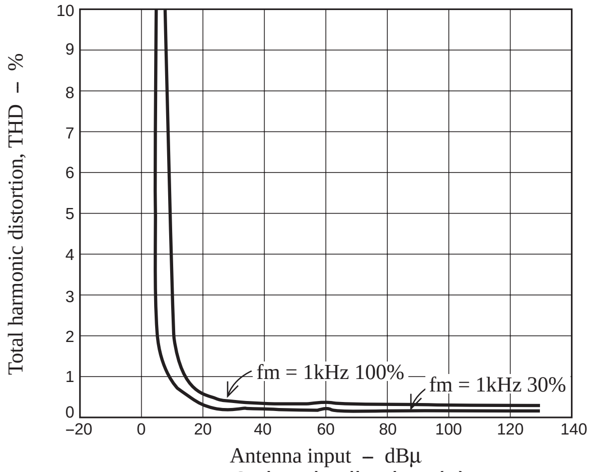
FM I/O Characteristics



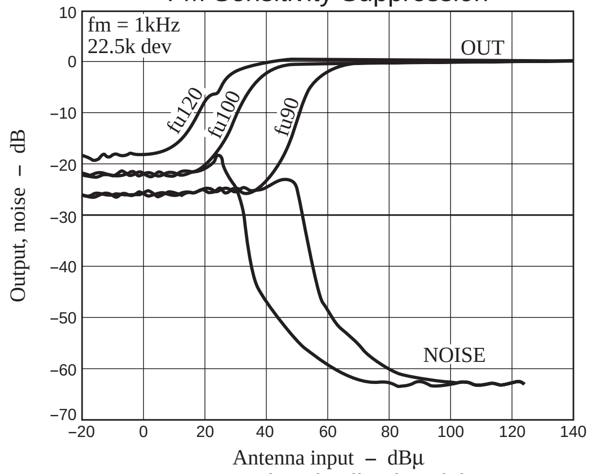
FM I/O Characteristics



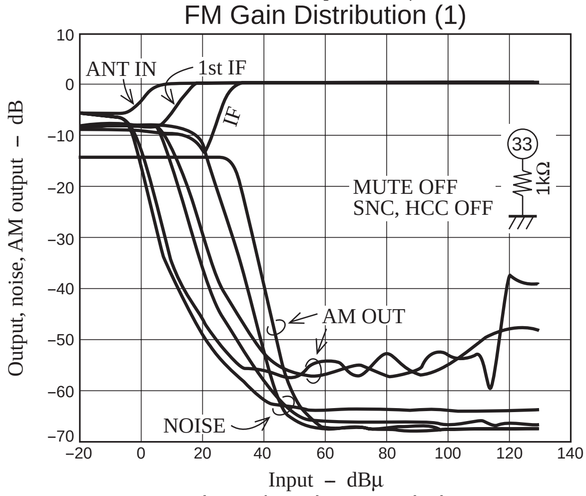
FM Distortion



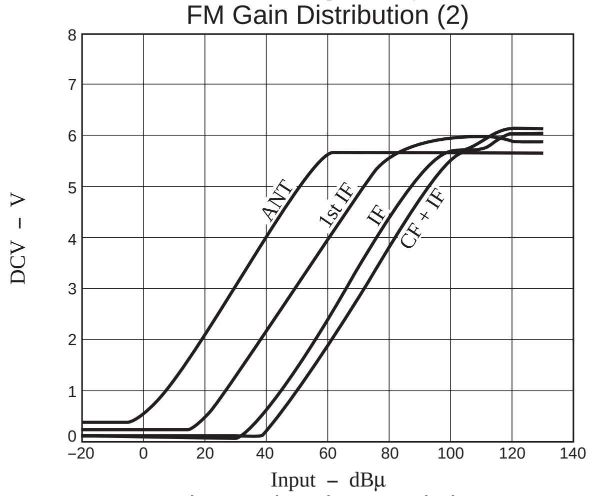
FM Sensitivity Suppression



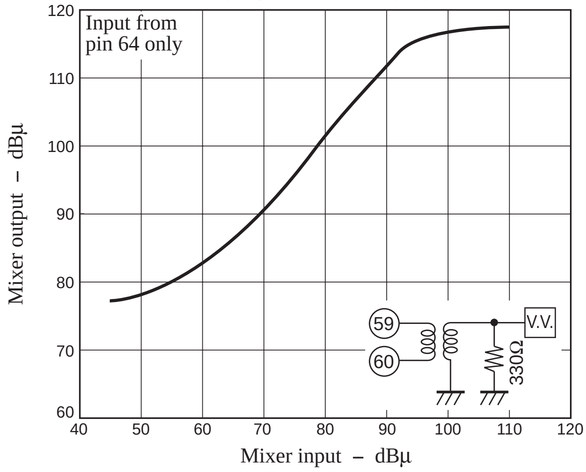
FM Gain Distribution (1)



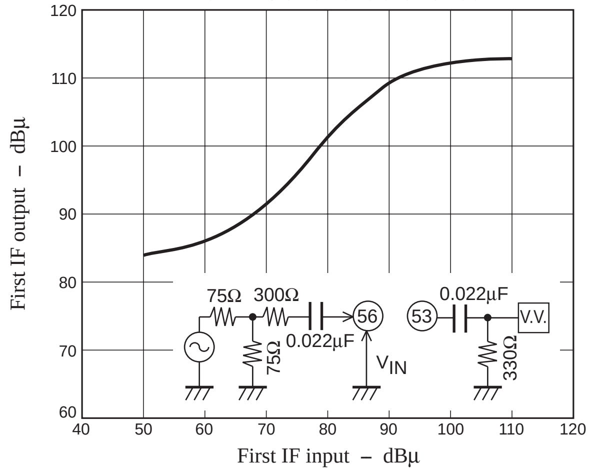
FM Gain Distribution (2)



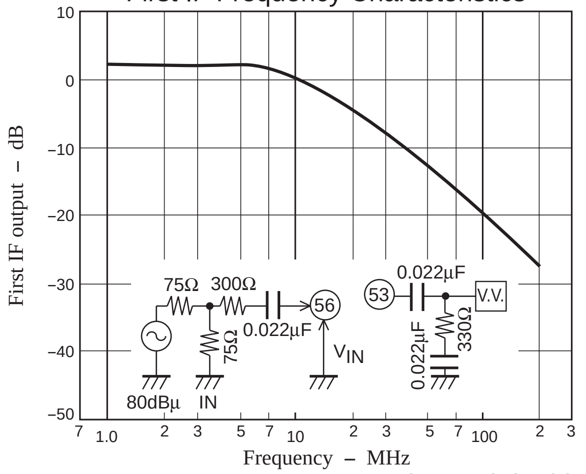
Mixer I/O Characteristics



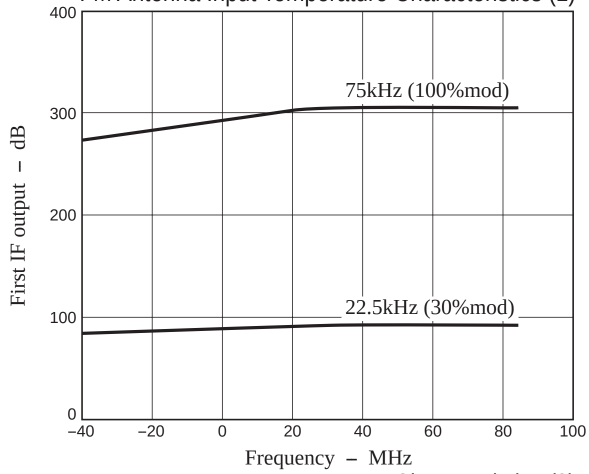
First IF I/O Characteristics



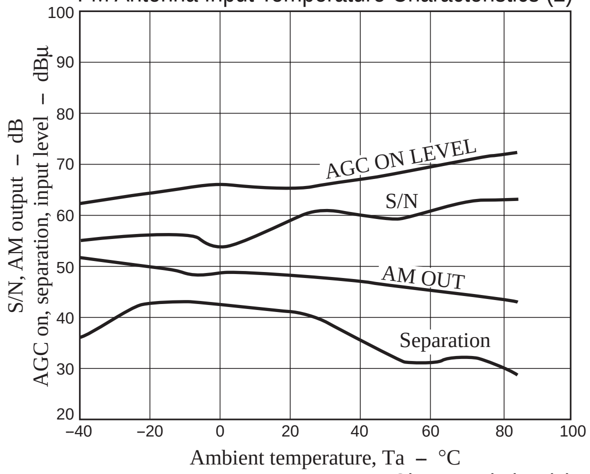
First IF Frequency Characteristics



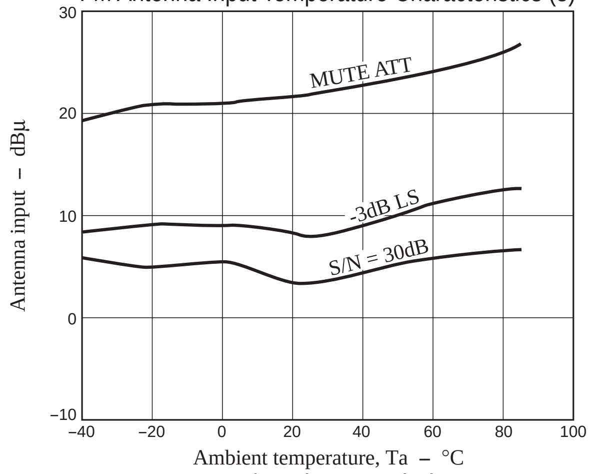
FM Antenna Input Temperature Characteristics (1)



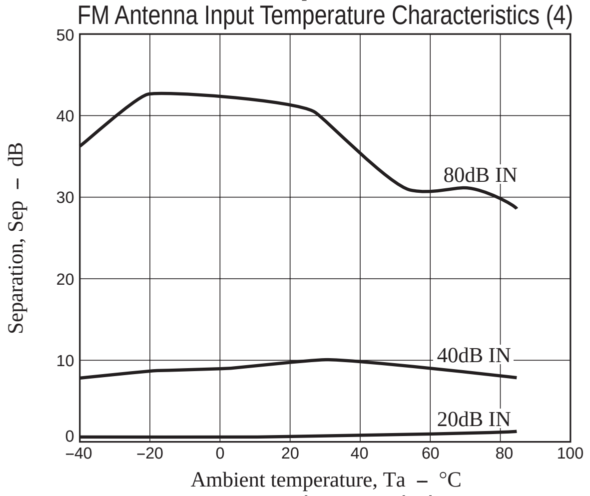
FM Antenna Input Temperature Characteristics (2)



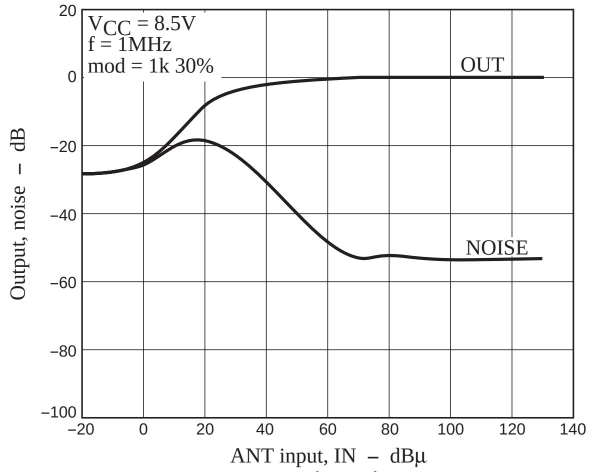
FM Antenna Input Temperature Characteristics (3)



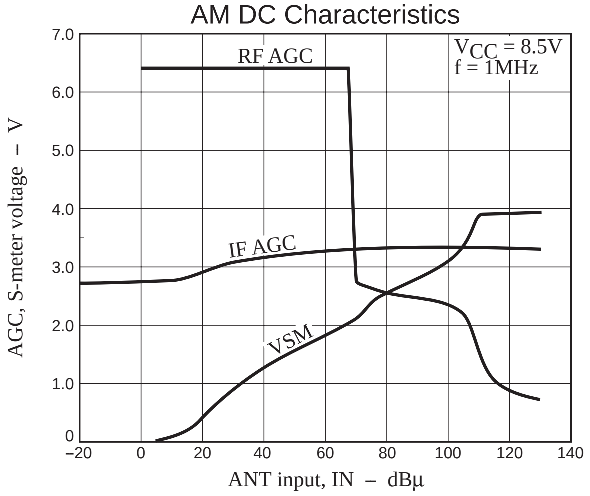
FM Antenna Input Temperature Characteristics (4)



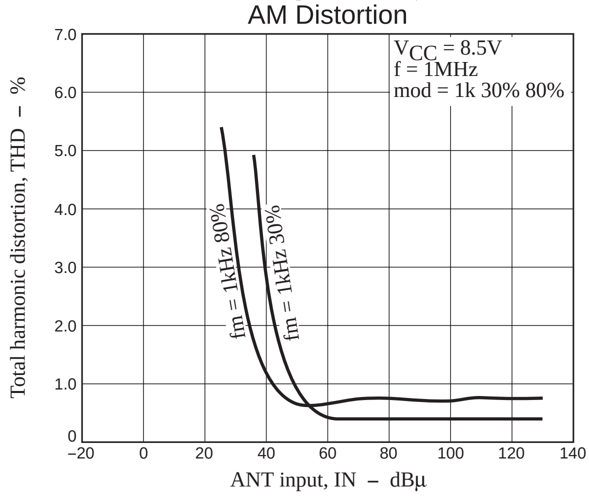
AM I/O Characteristics



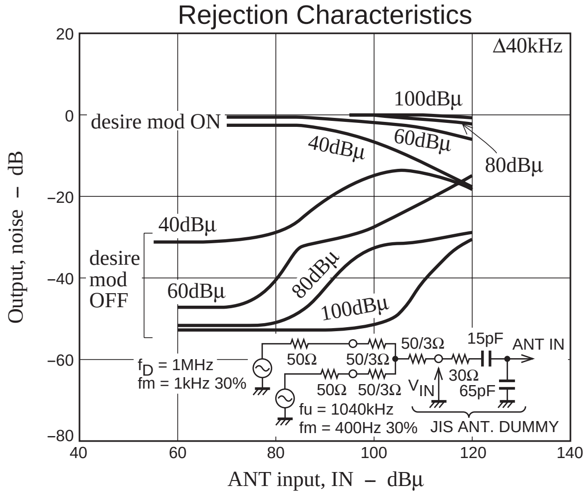
AM DC Characteristics



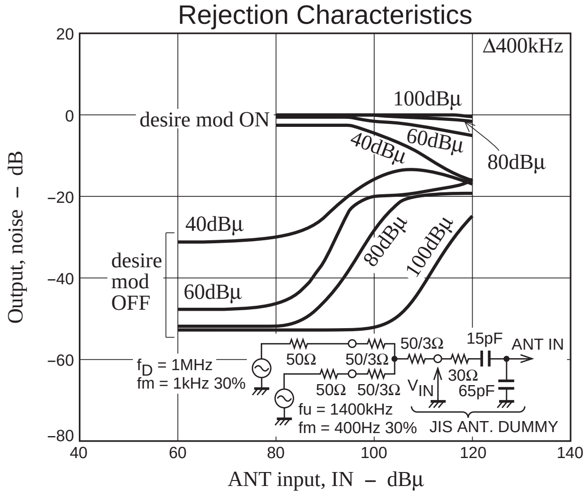
AM Distortion



AM Second-Channel Interference Rejection Characteristics



AM Second-Channel Interference Rejection Characteristics



This catalog provides information as of July, 2009. Specifications and information herein are subject to change without notice.

Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits,General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.

Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)
Факс: 8 (812) 320-02-42
Электронная почта: org@eplast1.ru
Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.