TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC14AP,TC74HC14AF

#### Hex Schmitt Inverter

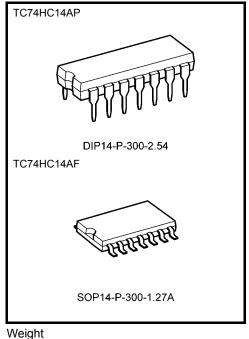
The TC74HC14A is a high speed CMOS SCHMITT INVERTER fabricated with silicon gate  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the TC74HC04A but the inputs have 25% VCC hysteresis and with its schmitt trigger function, the TC74HC14A can be used as a line receivers which will receive slow input signals.

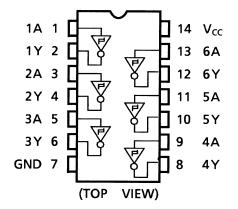
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

### Features

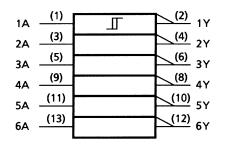
- High speed:  $t_{pd} = 11 \text{ ns}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 1 \ \mu A \ (max)$  at  $Ta = 25^{\circ}C$
- High noise immunity:  $V_H = 1.1 V$  at  $V_{CC} = 5 V$
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2~6 V
- Pin and function compatible with 74LS14



**Pin Assignment** 



### **IEC Logic Symbol**



Weight DIP14-P-300-2.54 SOP14-P-300-1.27A

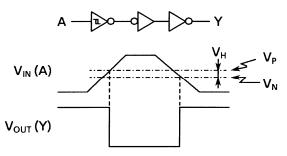
: 0.96 g (typ.) : 0.18 g (typ.)

## **TOSHIBA**

#### **Truth Table**

А	Y	
L	Н	
Н	L	

System Diagram, Waveform



## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~7	V
DC input voltage	V <sub>IN</sub>	$-0.5 \sim V_{CC} + 0.5$	V
DC output voltage	V <sub>OUT</sub>	$-0.5 \sim V_{CC} + 0.5$	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current I <sub>OK</sub>		±20	mA
DC output current	IOUT	±25	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of  $Ta = -40^{\circ}C \sim 65^{\circ}C$ . From  $Ta = 65^{\circ}C$  to  $85^{\circ}C$  a derating factor of  $-10 \text{ mW/}^{\circ}C$  shall be applied until 300 mW.

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2~6	V
Input voltage	V <sub>IN</sub>	0~V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

## **Electrical Characteristics**

#### **DC Characteristics**

		Test Condition		-	Ta = 25°0	)	Ta = −40~85°C			
Characteristics	Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
				2.0	1.0	1.25	1.50	1.0	1.50	
Positive threshold voltage	VP		_	4.5	2.3	2.70	3.15	2.3	3.15	V
, , , , , , , , , , , , , , , , , , ,				6.0	3.0	3.50	4.20	3.0	4.20	
				2.0	0.30	0.65	0.9	0.30	0.9	
Negative threshold voltage	V <sub>N</sub>		_	4.5	1.13	1.60	2.0	0.13	2.0	V
5				6.0	1.50	2.30	2.6	1.50	2.6	
Hysteresis voltage				2.0	0.3	0.6	1.0	0.3	1.0	
	V <sub>H</sub>		—		0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	V <sub>OH</sub>	V <sub>IN</sub> = VIL		2.0	1.9	2.0	_	1.9	—	
			$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5		4.4	—	
High-level output voltage				6.0	5.9	6.0	_	5.9	—	V
			I <sub>OH</sub> = -4 mA	4.5	4.18	4.31		4.13	—	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	—	
		V <sub>IN</sub> = VIH		2.0	—	0.0	0.1		0.1	
			$I_{OL} = 20 \ \mu A$	4.5	—	0.0	0.1		0.1	
Low-level output voltage	V <sub>OL</sub>			6.0	_	0.0	0.1	—	0.1	V
			I <sub>OL</sub> = 4 mA	4.5		0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0		0.18	0.26		0.33	
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$ or GND		6.0	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{C}$	$V_{IN} = V_{CC}$ or GND		_	_	1.0	_	10.0	μΑ

### AC Characteristics ( $C_L = 15 \text{ pF}$ , $V_{CC} = 5 \text{ V}$ , $Ta = 25^{\circ}C$ , input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	_	_	4	8	ns
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>	_		11	21	ns

## AC Characteristics ( $C_L = 50 \text{ pF}$ , input: $t_r = t_f = 6 \text{ ns}$ )

	Test Condition		Ta = 25°0		)	Ta = -4	Ta = −40~85°C		
Characteristics Symbol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
	<b>t</b>		2.0	_	30	75	_	95	
Output transition time	t <sub>TLH</sub>	—	4.5	—	8	15		19	ns
t <sub>THL</sub>	ιτης		6.0	—	7	13		16	
	<b>4</b>		2.0	_	42	125		155	
Propagation delay time	t <sub>pLH</sub>	_	4.5	_	14	25		31	ns
	t <sub>pHL</sub>		6.0	—	12	21	—	26	
Input capacitance	C <sub>IN</sub>			_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)	_			28		_		pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

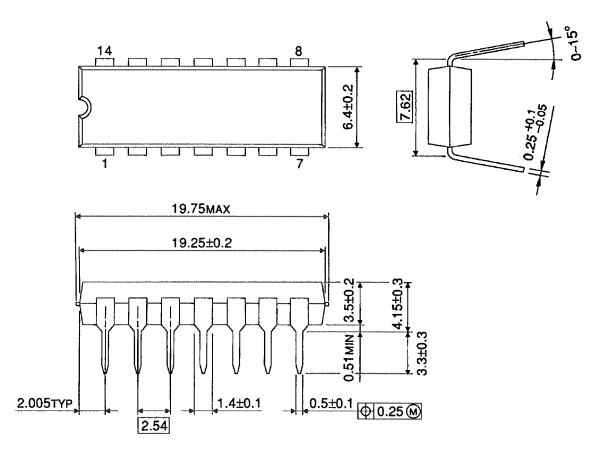
Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$  (per gate)

### **Package Dimensions**

DIP14-P-300-2.54

Unit : mm



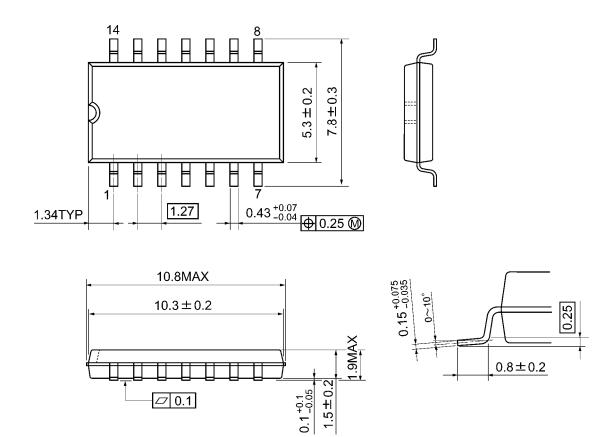
Weight: 0.96 g (typ.)



## **Package Dimensions**

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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