74LV244A

Octal buffer/line driver; 3-state Rev. 1 — 23 November 2016

Product data sheet

General description 1.

The 74LV244A is an 8-bit buffer/line driver with 3-state outputs. The device features two output enables (10E and 20E). A HIGH on nOE causes the associated outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. **Features and benefits**

- Wide supply voltage range from 2.0 V to 5.5 V
- Maximum t_{pd} of 6.5 ns at 5 V
- Typical $V_{OL(p)}$ < 0.8 V at V_{CC} = 3.3 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3 \text{ V}$ at $V_{CC} = 3.3 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - ♦ HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 150 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C



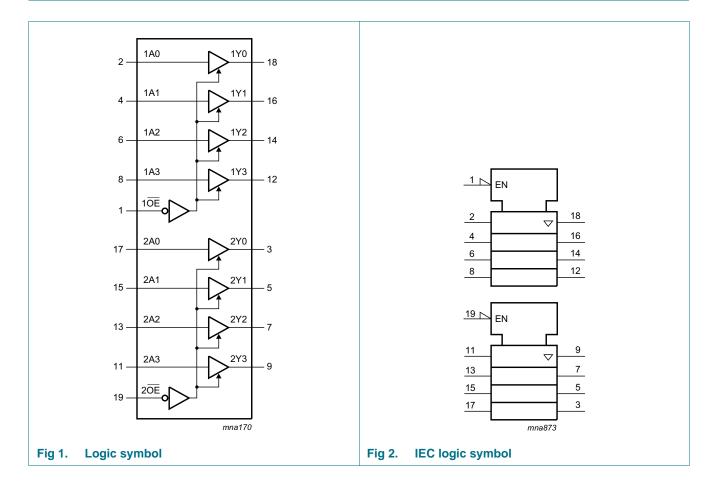
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3. Ordering information

Table 1. Ordering information

Type number	Package	'ackage								
	Temperature range	Name	Description	Version						
74LV244APW	−40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1						

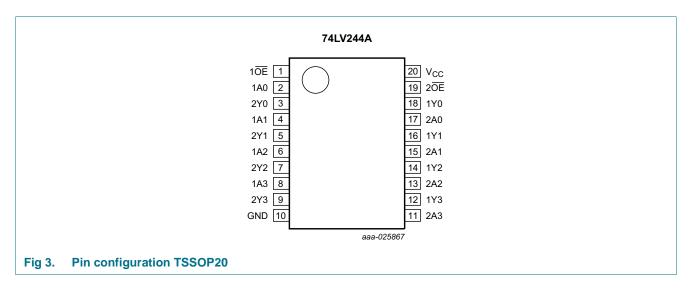
4. Functional diagram



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5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1 OE , 2 OE	1, 19	output enable input (active LOW)
1A0, 1A1, 1A2, 1A3	2, 4, 6, 8	data input
2Y0, 2Y1, 2Y2, 2Y3	3, 5, 7, 9	data output
GND	10	ground (0 V)
2A0, 2A1, 2A2, 2A3	17, 15, 13, 11	data input
1Y0, 1Y1, 1Y2, 1Y3,	18, 16, 14, 12	data output

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6. Functional description

Table 3. Function table [1]

Control	Input	Output
nOE	nAn	nYn
L	L	L
L	Н	Н
Н	X	Z

^[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage	[1]	-0.5	+7.0	V
Vo	output voltage	active mode [2][3]	-0.5	V _{CC} + 0.5	V
		power-down or 3-state mode [2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V	-20	-	mA
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
I _O	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±35	mA
I _{CC}	supply current		-	70	mA
I _{GND}	ground current		-70	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$ [4]	-	500	mW

^[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

^[2] The output voltage ratings may be exceeded if the output current ratings are observed.

^[3] This value is limited to 7.0 V maximum.

^[4] For TSSOP20 package: above 100 $^{\circ}$ C the value of P_{tot} derates linearly with 10 mW/K.

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8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		2.0	5.5	V
VI	input voltage		0	5.5	V
Vo	output voltage	active mode	0	V _{CC}	V
		power-down or 3-state mode	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.3 V to 2.7 V	-	200	ns/V
		V _{CC} = 3.0 V to 3.6 V	-	100	ns/V
		V _{CC} = 4.5 V to 5.5 V	-	20	ns/V

9. Static characteristics

Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		–40 °C to	+85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level	V _{CC} = 2 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 2.3 V to 2.7 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
		V _{CC} = 3.0 V to 3.6 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
V _{IL}	LOW-level	V _{CC} = 2 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 2.3 V to 2.7 V	-	-	$0.3V_{CC}$	-	0.3V _{CC}	-	0.3V _{CC}	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.3V _{CC}	-	0.3V _{CC}	-	0.3V _{CC}	V
		V _{CC} = 4.5 V to 5.5 V	-	-	$0.3V_{CC}$	-	0.3V _{CC}	-	0.3V _{CC}	V
V _{OH} HIGH-levoutput voltage	HIGH-level	$V_I = V_{IH}$ or V_{IL}								V
	•	V_{CC} = 2.0 V to 5.5 V; I_{O} = -50 μ A	V _{CC} -0.1	-	-	V _{CC} -0.1	-	V _{CC} -0.1	-	V
		$V_{CC} = 2.3 \text{ V}; I_{O} = -2 \text{ mA}$	2	-	-	2	-	2	-	V
		$V_{CC} = 3.0 \text{ V}; I_{O} = -8 \text{ mA}$	2.58	-	-	2.48	-	2.48	-	V
		$V_{CC} = 4.5 \text{ V};$ $I_{O} = -16 \text{ mA}$	3.94	-	-	3.8	-	3.8	-	V
V_{OL}	LOW-level	$V_I = V_{IH}$ or V_{IL}								
	output voltage	$V_{CC} = 2.0 \text{ V to } 5.5 \text{ V};$ $I_{O} = 50 \mu\text{A}$	-	-	0.1	-	0.1	-	0.1	V
		$V_{CC} = 2.3 \text{ V}; I_{O} = 2 \text{ mA}$	-	-	0.4	-	0.4	-	0.4	V
		$V_{CC} = 3.0 \text{ V}; I_{O} = 8 \text{ mA}$	-	-	0.36	-	0.44	-	0.44	V
		$V_{CC} = 4.5 \text{ V}; I_{O} = 16 \text{ mA}$	-	-	0.44	-	0.55	-	0.55	V
l _{OZ}	OFF-state output current	$\begin{aligned} &V_{CC} = 5.5 \text{ V;} \\ &V_{I} = V_{IH} \text{ or } V_{IL}; \\ &V_{O} = \text{GND to } 5.5 \text{ V} \end{aligned}$	-	-	±0.25	-	±2.5	-	±2.5	μΑ

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Table 6. Static characteristics ...continued Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C		-40 °C to +85 °C		–40 °C to	Unit		
			Min	Тур	Max	Min	Max	Min	Max	
l _{OFF}	power-off leakage current	V_1 or $V_O = GND$ to 5.5 V; $V_{CC} = 0$ V	-	-	0.5	-	5	-	5	μА
I ₁	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0$ V to 5.5 V	-	-	±0.1	-	±1	-	±1	μА
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μΑ

10. Dynamic characteristics

Table 7. Dynamic characteristics GND = 0 V. For test circuit see <u>Figure 6</u>.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
t _{pd}	propagation	nAn to nYn; see Figure 4 [2]								
	delay	V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	4.9	12.5	1	15	1	15	ns
		C _L = 50 pF	-	6.8	15.3	1	18	1	18	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	3.7	8.4	1	10	1	10	ns
		C _L = 50 pF	-	5.2	11.9	1	13.5	1	13.5	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	2.9	5.5	1	6.5	1	6.5	ns
		C _L = 50 pF	-	4.1	7.5	1	8.5	1	8.5	ns
t _{en}	enable time	nOE to nYn; see Figure 5 [2]								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	6.1	14.6	1	17	1	17	ns
		C _L = 50 pF	-	8.2	17.8	1	21	1	21	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.6	10.6	1	12.5	1	12.5	ns
		C _L = 50 pF	-	6.3	14.1	1	16	1	16	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.2	7.3	1	8.5	1	8.5	ns
		C _L = 50 pF	-	4.4	9.3	1	10.5	1	10.5	ns

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 Table 7.
 Dynamic characteristics ...continued

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
t _{dis}	disable time	nOE to nYn; see Figure 5								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	6.4	14.1	1	16	1	16	ns
		C _L = 50 pF	-	11.0	19.2	1	21	1	21	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.1	11.7	1	13	1	13	ns
		C _L = 50 pF	-	8.5	16	1	18	1	18	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.0	12.2	1	13.5	1	13.5	ns
		C _L = 50 pF	-	6.2	14.2	1	15.5	1	15.5	ns
t _{sk(o)}	skew	C _L = 50 pF								
		V _{CC} = 2.3 V to 2.7 V	-	-	2	-	2	-	2	ns
		V _{CC} = 3.0 V to 3.6 V	-	-	1.5	-	1.5	-	1.5	ns
		V _{CC} = 4.5 V to 5.5 V	-	-	1	-	1	-	1	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	2	6	-	6	-	6	pF
Co	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	5	-	-	-	-	-	pF
C_{PD}	power dissipation capacitance	per buffer; [3] $C_L = 50 \text{ pF}$; $f = 10 \text{ MHz}$; $V_I = \text{GND to } V_{CC}$								
		V _{CC} = 3.3 V	-	9	-	-	-	-	-	pF
		V _{CC} = 5.0 V	-	11	-	-	-	-	-	pF

^[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 2.5 V, 3.3 V, and 5 V respectively, unless otherwise specified.

 t_{en} is the same as t_{PZL} and $t_{\text{PZH}}.$

 t_{dis} is the same as t_{PLZ} and $t_{\text{PHZ}}.$

[3] C_{PD} is used to determine the dynamic power dissipation P_D (μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in Volts.

^[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

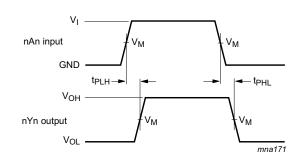
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Table 8. Noise characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	Conditions	Т	_{amb} = 25 °C	Unit	
			Min	Тур	Max	
$V_{CC} = 3.3$	V; C _L = 50 pF				•	·
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.3	0.8	V
$V_{OL(v)}$	LOW-level output voltage (valley)		-0.8	-0.2	-	V
V _{OH(v)}	HIGH-level output voltage (valley)		-	2.9	-	V
V _{IH(AC)}	AC HIGH-level input voltage (dynamic)		2.31	-	-	V
V _{IL(AC)}	AC LOW-level input voltage (dynamic)		-	-	0.99	V

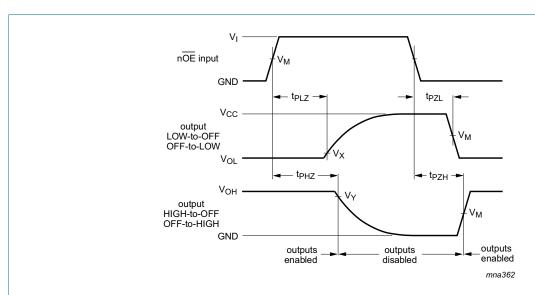
11. Waveforms



Measurement points are given in Table 9.

 V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Fig 4. Propagation delay input (nAn) to output (nYn)



Measurement points are given in Table 9.

 $V_{\mbox{\scriptsize OL}}$ and $V_{\mbox{\scriptsize OH}}$ are typical voltage output levels that occur with the output load.

Fig 5. Enable and disable times

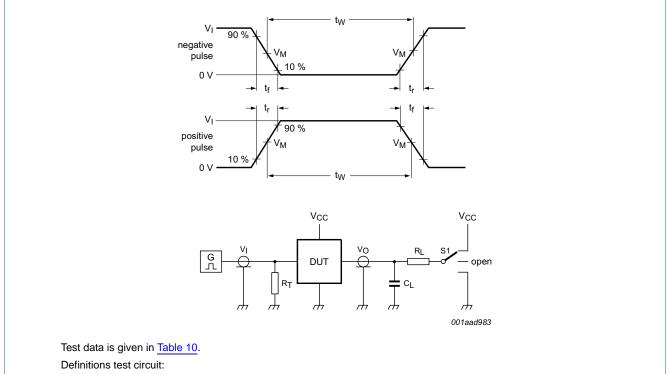
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Table 9. Measurement points

Input	Output		
V _M	V _M	V _X	V _Y
0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} – 0.3 V



 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator

 C_L = Load capacitance including jig and probe capacitance

R_L = Load resistor

S1 = Test selection switch

Fig 6. Test circuit for measuring switching times

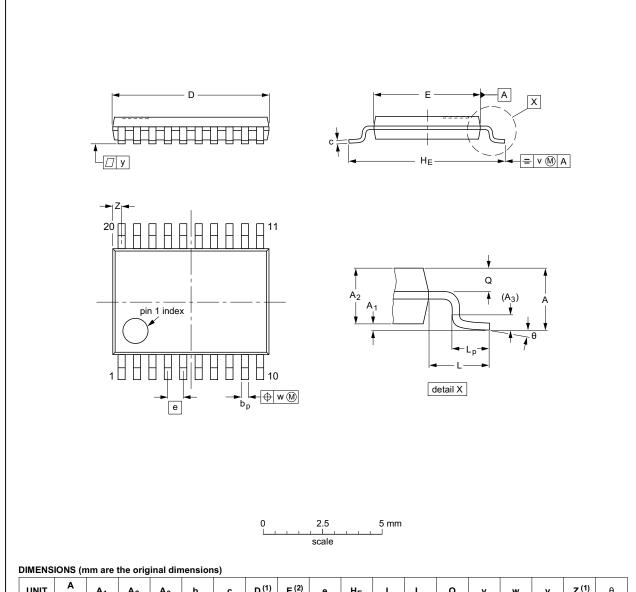
Table 10. Test data

Input		Load		S1 position			
V_{I}	t _r , t _f	CL	R_L	t _{PHL} , t _{PLH} t _{PZH} , t _{PHZ} t _{PZL} ,		t _{PZL} , t _{PLZ}	
GND to V_{CC}	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	

12. Package outline

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	C	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	Q	>	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.5 0.2	8° 0°

Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT360-1		MO-153				-99-12-27 03-02-19

Fig 7. Package outline SOT360-1 (TSSOP20)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LV244A v.1	20161123	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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