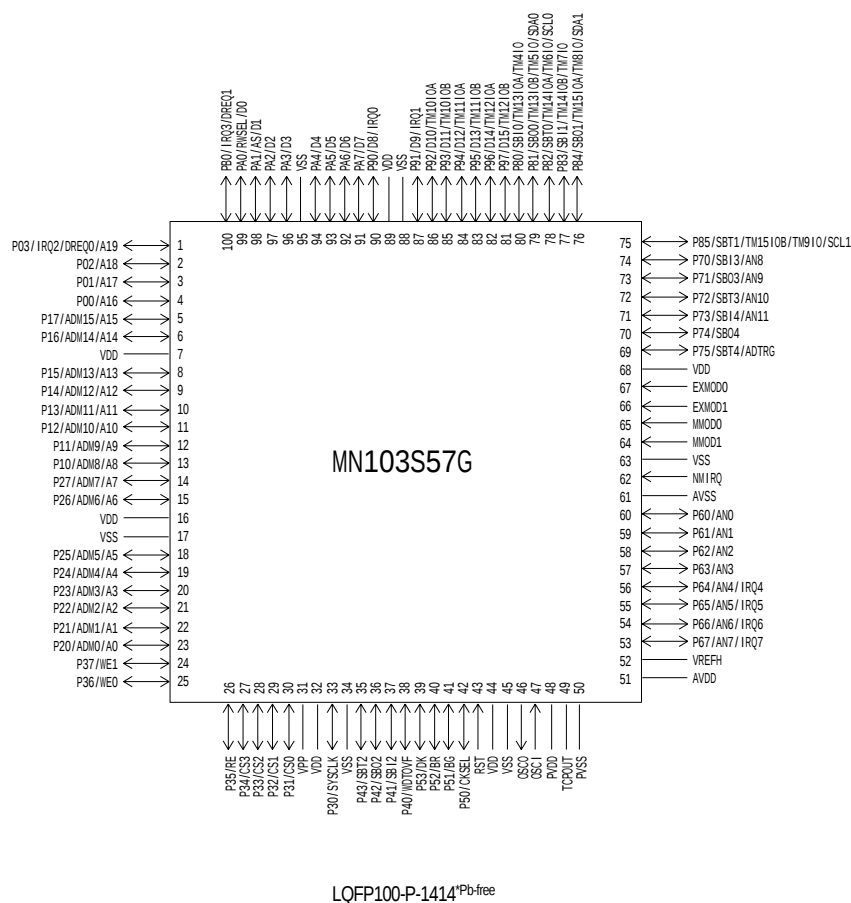


MN103S57G

■ Type		MN103S57G (under development)	
■ Command ROM (×64-bit)		128 K-byte	
■ Data RAM (×32-bit)		16 K-byte	
■ Package		LQFP100-P-1414 *Pb-free	
■ Minimum Instruction Execution Time		25.0 ns (at 3.0 V to 3.6 V, 40 MHz)	
■ Interrupts		• RESET • IRQ × 8 • NMI • Timer × 28 • SIF × 10 • I ² C × 2 • DMA × 12 • WDT • A/D • System error	
■ Timer Counter		8-bit timer × 10 Reload-down count Cascade connection possible (usable as a 16-bit to 32-bit timer) 16-bit timer × 6 Up-down count Input capture function PWM generating function Compare/capture register 2-ch. Watchdog timer × 1	
■ DMA Controller		Number of channels: 4 Unit of transfer: 8/16/32 bits Max. Transfer cycles: 65535 Starting factor: external interrupt, timer factor, serial transmission/reception factor, I ² C transmission/reception factor, external transmission request factor, A/D conversion finish, software factor Transfer method: 2-bus cycle transfer Addressing modes: fixed, increment, decrement Transfer modes: word transfer, burst transfer, intermittent transfer	
■ Serial Interface		UART/synchronous/multi-master I ² C interface selective: 2 UART/synchronous interface selective: 3	
■ I/O Pins	I/O	73	• Common use
■ A/D Inputs		10-bit × 12-ch.	
■ Electrical Characteristics		T.B.D.	

Pin Assignment



SupportTool

In-circuit Emulator	PX-ICE103S57	
On-boardDevelopmentTools	PX-ODB103S-O	
Flash Memory Built-in Type	Type	MN103SF57G (under development)
	Command ROM (× 64-bit)	128 K-byte
	Data RAM (× 32-bit)	16 K-byte
	Minimum instruction execution time	25.0 ns (at 3.0 V to 3.6 V, 40 MHz)
	Package	LQFP100-P-1414A *Pb-free

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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.