Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PHOTOCOUPLER PS2581L1,PS2581L2

LONG CREEPAGE TYPE HIGH ISOLATION VOLTAGE 4-PIN PHOTOCOUPLER -NEPOC Series-

DESCRIPTION

The PS2581L1, PS2581L2 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon phototransistor in a plastic DIP (Dual In-line Package).

Creepage distance and clearance of leads are over 8 millimeters.

The PS2581L2 is lead bending type (Gull-wing) for surface mounting.

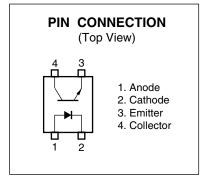
DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40008862

FEATURES

- Long creepage and clearance distance (8 mm)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- High collector to emitter voltage (VCEO = 80 V)
- High-speed switching ($t_r = 3 \mu s$ TYP., $t_f = 5 \mu s$ TYP.)
- High current transfer ratio (CTR = 200% TYP.)
- Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA101391
 - BSI approved: No. 8243/8244
 - NEMKO approved: No. P97103006
 - DEMKO approved: No. 314523
 - SEMKO approved: No. 714542
 - FIMKO approved: No. FI 23944
- <R>

APPLICATIONS

- Power supply
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controller

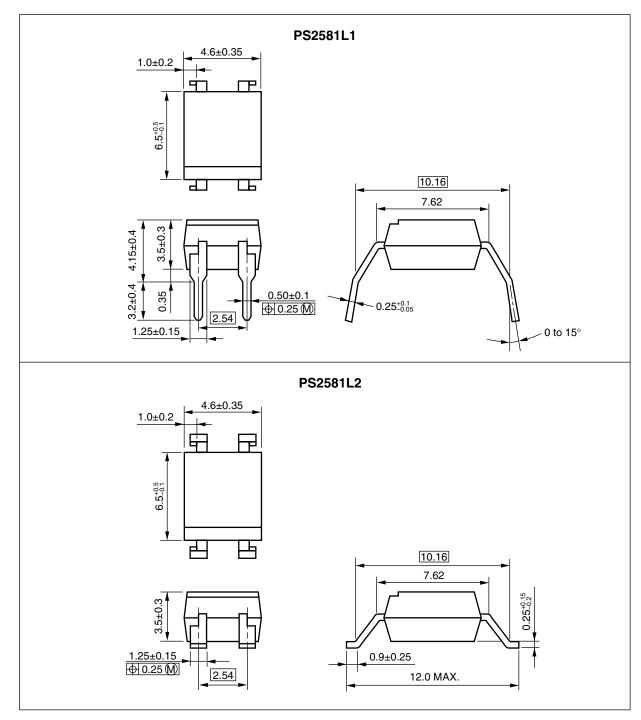


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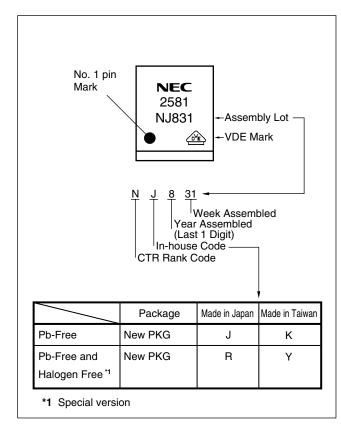


PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	8 mm
Outer Creepage Distance	8 mm
Inner Creepage Distance	4 mm
Isolation Distance	0.4 mm

NEC

<R> MARKING EXAMPLE



<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS2581L1	PS2581L1-A	Pb-Free	Magazine case 100 pcs	Standard products	PS2581L1
PS2581L2	PS2581L2-A			(UL, CSA, BSI, NEMKO,	PS2581L2
PS2581L2-E3	PS2581L2-E3-A		Embossed Tape 1 000 pcs/reel	SEMKO, DEMKO, FIMKO,	
PS2581L2-E4	PS2581L2-E4-A			DIN EN60747-5-2	
PS2581L1	PS2581L1-Y-A	Special version	Magazine case 100 pcs	(VDE0884 Part2)	PS2581L1
PS2581L2	PS2581L2-Y-A	(Pb-Free and		Approved products)	PS2581L2
PS2581L2-E3	PS2581L2-Y-E3-A	Halogen Free)	Embossed Tape 1 000 pcs/reel		

*1 For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current (DC)	lf	80	mA
	Reverse Voltage	VR	6	V
	Power Dissipation Derating	⊿P₀/°C	1.5	mW/°C
	Power Dissipation	P⊳	150	mW
	Peak Forward Current	IFP	1	А
Transistor	Collector to Emitter Voltage	VCEO	80	V
	Emitter to Collector Voltage	VECO	7	V
	Collector Current	lc	50	mA
	Power Dissipation Derating	⊿Pc/°C	1.5	mW/°C
	Power Dissipation	Pc	150	mW
Isolation Vo	bltage ^{*2}	BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	–55 to +100	°C
Storage Ten	nperature	Tstg	–55 to +150	°C

*1 PW = 100 *µ*s, Duty Cycle = 1%

*2 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output Pins 1-2 shorted together, 3-4 shorted together.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

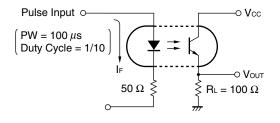
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	V _F I _F = 10 mA		1.17	1.4	V
	Reverse Current	IR	V _R = 5 V			5	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		50		pF
Transistor	Collector to Emitter Dark Current	Iceo	$V_{CE} = 80 \text{ V}, \text{ IF} = 0 \text{ mA}$			100	nA
Coupled	Current Transfer Ratio (Ic/IF)*1	CTR	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	80	200	400	%
	Collector Saturation Voltage	VCE (sat)	IF = 10 mA, Ic = 2 mA			0.3	V
	Isolation Resistance	R ⊦o	VI-0 = 1.0 kVDC	10 ¹¹			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1.0 MHz		0.5		pF
	Rise Time [⁺] 2	tr	Vcc = 10 V, Ic = 2 mA,		3		μs
	Fall Time [*]	tr	R∟ = 100 Ω		5		

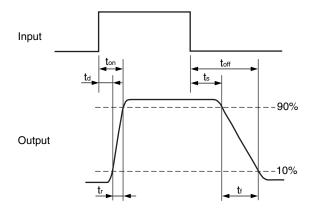
*1 CTR rank

- L : 200 to 400 (%)
- M: 80 to 240 (%)
- D : 100 to 300 (%)

H : 80 to 160 (%)

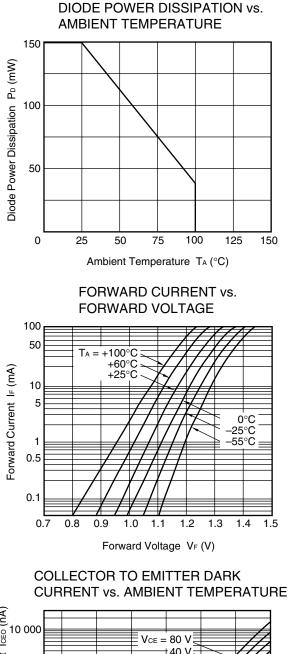
- W : 130 to 260 (%)
- N : 80 to 400 (%)
- *2 Test circuit for switching time

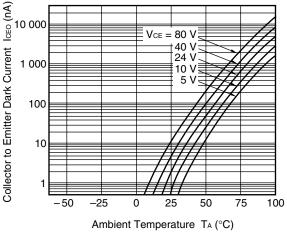


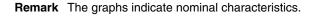




TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)





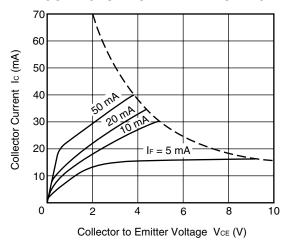


Data Sheet PN10239EJ04V0DS

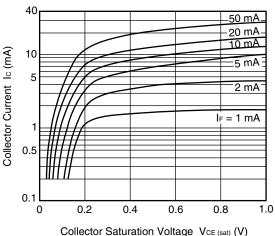
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE

Ambient Temperature T_A (°C)

COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE

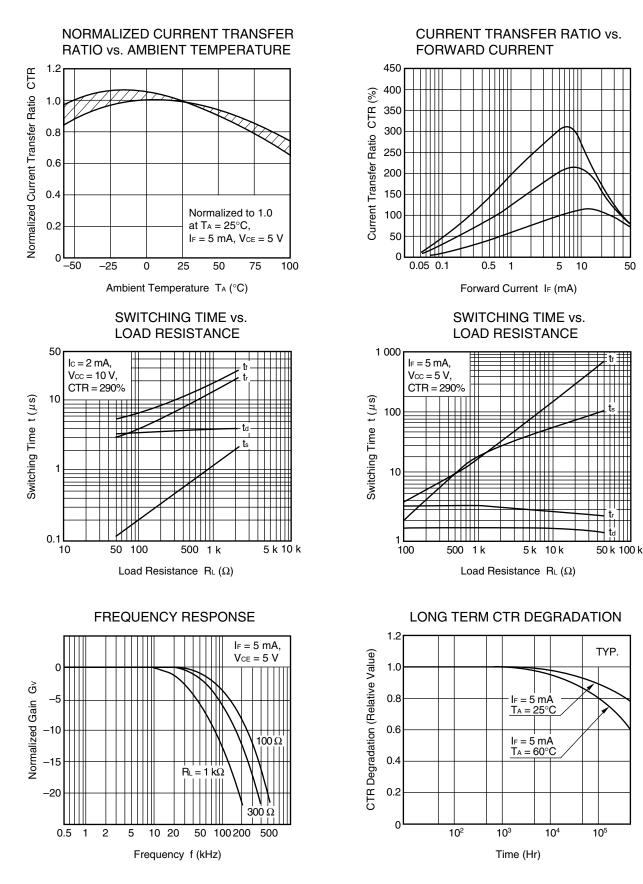


50

ts

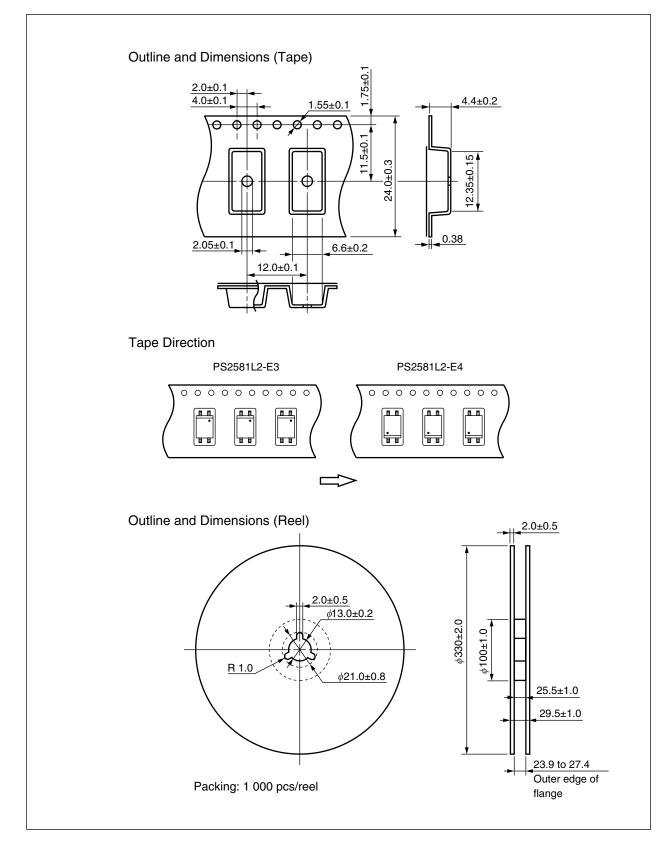
tr

td



Remark The graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (in millimeters)



Data Sheet PN10239EJ04V0DS

NOTES ON HANDLING

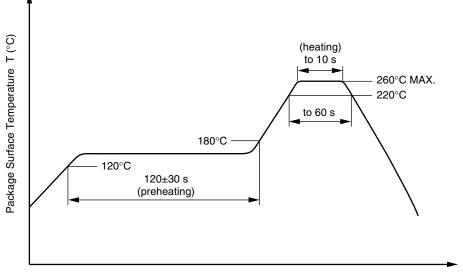
1. Recommended soldering conditions

(1) Infrared reflow soldering

- · Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

 Peak temperature (lead part temperature) 	350°C or below
 Time (each pins) 	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.
- (b) Please be sure that the temperature of the package would not be heated over 100°C.



(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. This tendency may sometimes be obvious, especially below $I_F = 1$ mA.

Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 \text{ pC}$	Uiorm Upr	890 1 335	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) U_{pr} = 1.875 \times U_{IORM}, P_{d} < 5 pC	Upr	1 669	V _{peak}
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>8.0	mm
Creepage distance		>8.0	mm
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A MAX. at least 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)	T-:	475	
Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 [°]	Ω

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	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	Do not lick the product or in any way allow it to enter the mouth.

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<u>PS2581L1-H-A</u> <u>PS2581L1-A</u> <u>PS2581L2-A</u> <u>PS2581L2-1-E3-H-A</u> <u>PS2581L1-L-A</u> <u>PS2581L2-D-A</u> <u>PS2581L2-D-A</u> <u>PS2581L2-H-A</u> PS2581L2-W-A PS2581L2-F3-A PS2581L2-F3-L-A



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