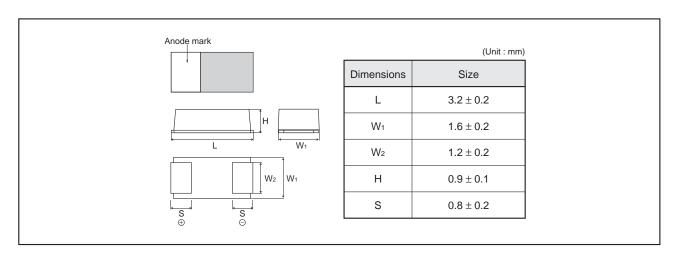
# Chip tantalum capacitors (Bottom surface electrode type : Large capacitance)

TCT Series AS Case Datasheet

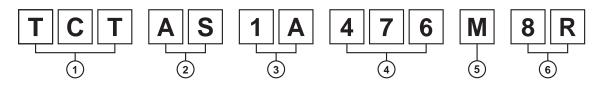
#### Features

- 1) Bottom electrode configuration results in significantly greater compactness.
- 2) Filet formation enables easy visibility after mounting.
- 3) Ideal for noise removal on power supply lines with limited space.
- 4) Eco-friendly halogen-free products.

#### Dimensions



## ●Part No. Explanation



1 Series name

2 Case style

AS: 3216-10 (1206) size

3 Rated voltage

Rated voltage (V)	2.5	4	6.3	10	16	20	25	35
CODE	0E	0G	0J	1A	1C	1D	1E	1V

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

 $M:\pm 20\%$ 

(6) Taping

8 : Reel width : 8mm

R : Positive electrode on the side opposite to sprocket hole

<sup>\*</sup>This specification has possibility of charge, due to underdevelopment product. Please ask for latest specification to our sales.

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#### Rated table

Capacitance		Rated voltage (V.DC)								
(μF)	2.5	4	6.3	10	16	20	25	35		
1.0 (105)								AS		
1.5 (155)								☆AS		
2.2 (225)								☆AS		
3.3 (335)							☆AS			
4.7 (475)							AS			
10 (106)						AS				
15 (156)					☆AS					
22 (226)					AS					
33 (336)				AS						
47 (476)			AS	AS						
100 (107)		AS	AS							
150 (157)		☆AS								
220 (227)	☆AS	AS								

Remark) Case size codes (AS) in the above show products line-up.

☆ Under development

## Marking

The indications listed below should be given on the surface of a capacitor.

(1) Polarity : The polarity should be shown by  $\square$  bar. (on the anode side)

(2) Rated DC voltage : A voltage code is shown as below table.

(3) Capacitance : A capacitance code is shown as below table.

Voltage Code	Rated DC Voltage (V)		
е	2.5		
g	4		
j	6.3		
А	10		
С	16		
D	20		
E	25		
V	35		

Capacitance Code	Nominal Capacitance (μF)			
А	1.0			
Е	1.5			
J	2.2			
N	3.3			
S	4.7			
а	10			
е	15			
j	22			
n	33			
S	47			
ā	100			
ē	150			
j	220			

Visual typical example

voltage code and capacitance code are variable with parts number.

[AS case]

EX.) 
$$\frac{A}{(1)} \frac{s}{(2)}$$

(1) voltage code (2) capacitance code



TCT Series AS Case Datasheet

# ● Characteristics

Item		Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)				
Operating Temp		-55°C to +125°C	Voltage reduction when temperature exceeds +85°C				
Maximum opera temperature wit derating	ating h no voltage	+85°C					
Rated voltage (V.DC)		2.5 4 6.3 10 16 20 25 35	at 85°C				
Category voltag	je (V.DC)	1.6 2.5 4 6.3 10 13 16 22	at 125°C				
Surge voltage (	V.DC)	3.2 5.0 8 13 20 26 32 44	at 85°C				
DC Leakage cu	rrent	Shall be satisfied the value on " Standard list "	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage: Rated voltage for 5min				
Capacitance tolerance Shall be satisfied allowance range ±20%			As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC Equivalent series circuit				
Tangent of loss (Df, tan $\delta$ )	angle	Shall be satisfied the value on "Standard list"	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC Equivalent series circuit				
Impedance		Shall be satisfied the value on " Standard list "	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit				
Resistance to Soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3				
	L.C.	Less than 200% of initial limit	Dip in the solder bath Solder temp : 260±10°C				
	⊿C / C	AS0E227 : Within +20/–30% of initial value AS0G227 : Within +20/–30% of initial value Others:Within ±20% of initial value	Duration : 5±0.5s Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample.				
	Df (tan δ)	Less than 200% of initial limit					
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3				
	L.C.	Less than 200% of initial limit	Repetition: 5 cycles (1 cycle: steps 1 to 4) without discontinuation.				
	⊿c/c	AS0E227 : Within ±30% of initial value	Temp. Time				
		AS0G227 : Within ±30% of initial value Others:Within ±20% of initial value	1 –55±3°C 30±3min.				
	D( () 2)		2 Room temp. 3min. or less				
	Df (tan δ)	Less than 200% of initial limit	3 125±2°C 30±3min.				
			4 Room temp. 3min. or less				
			After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3				
	L.C.	Less than 200% of initial limit	After leaving the sample under such atmospheric condition that the temperature and humidity are				
	⊿C/C	Within ±20% of initial value	60±2°C and 90 to 95% RH, respectively, for 500±12h				
	Df (tan δ)	AS0E227 : Less than 300% of initial limit AS0G227 : Less than 300% of initial limit Others:Less than 200% of initial limit	leave it at room temperature for over 24h and then measure the sample.				



Iten	n	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)				
Temperature	Temp.	–55°C	As per 4.29 JIS C 5101-1 - As per 4.13 JIS C 5101-3				
Stability	⊿C / C	AS0E227: Within 0/–20% of initial value AS0G227: Within 0/–20% of initial value Others:Within 0/–15% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	-					
	Temp.	+85°C					
	⊿C/C	Within +15/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	Less than 1000% of initial limit					
	Temp.	+125°C					
	⊿c/c	Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	Less than 1250% of initial limit					
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1				
	L.C.	Less than 200% of initial value	As per 4.14JIS C 5101-3 Apply the specified surge voltage via the serial resistance of				
	⊿c/c	Within ±20% of initial value	1kΩ every 5±0.5 min.				
	Df (tan δ)	Less than 200% of initial limit	for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times.				
			After the specimens, leave it at room temperature for over 24h and then measure the sample.				
oading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3				
nign temperature	L.C.	Less than 200% of initial limit	After applying the rated voltage for 1000+36/0 h without discontinuation via the serial resistance of 3Ω or less				
	⊿C/C	AS0E227 : Within +20/ $-30\%$ of initial value AS0G227 : Within +20/ $-30\%$ of initial value Others:Within $\pm 20\%$ of initial value	at a temperature of 85±2°C, leave the sample at room temperature / humidity for over 24h and measure the value.				
	Df (tan δ)	AS0E227 : Less than 300% of initial limit AS0G227 : Less than 300% of initial limit Others:Less than 200% of initial limit					
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
Appearance		There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s.  (See the figure below)  (Unit: mm)  F (Apply force)  thickness=1.6mm				
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.				

Item		Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)			
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.			
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.			
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25% IPA 75%			
Vibration Capacitance		Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm			
	Appearance There should be no significant abnorm		Time: 2h each in X and Y directions  Mounting: The terminal is soldered on a print circuit board.			

# ●Standard products list

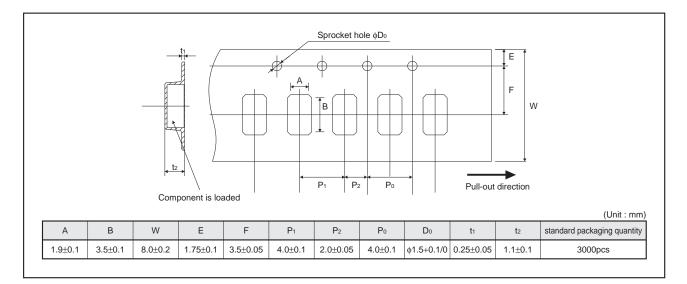
Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		Impedance 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.5min (μA)	–55°C	25°C 85°C	125°C	(Ω)
* TCT AS 0E 227 M8R	2.5	1.6	3.2	220	± 20	27.5	35	20	25	2.5
TCT AS 0G 107 M8R	4	2.5	5	100	± 20	20	35	20	25	3
* TCT AS 0G 157 M8R	4	2.5	5	150	± 20	30	35	20	25	2.7
TCT AS 0G 227 M8R	4	2.5	5	220	± 20	88	80	30	40	2.5
TCT AS 0J 476 M8R	6.3	4	8	47	± 20	6	35	20	25	4
TCT AS 0J 107 M8R	6.3	4	8	100	± 20	31.5	34	18	24	3
TCT AS 1A 336 M8R	10	6.3	13	33	± 20	6.6	30	15	20	4
TCT AS 1A 476 M8R	10	6.3	13	47	± 20	9.4	35	20	25	4
* TCT AS 1C 156 M8R	16	10	20	15	± 20	4.8	30	15	20	4
TCT AS 1C 226 M8R	16	10	20	22	± 20	7.1	35	20	25	4
TCT AS 1D 106 M8R	20	13	26	10	± 20	4	30	15	20	4
* TCT AS 1E 335 M8R	25	16	32	3.3	± 20	1.7	30	15	20	8
TCT AS 1E 475 M8R	25	16	32	4.7	± 20	2.4	30	15	20	8
TCT AS 1V 105 M8R	35	22	44	1	± 20	0.7	30	15	20	8
* TCT AS 1V 155 M8R	35	22	44	1.5	± 20	1.1	30	15	20	8
* TCT AS 1V 225 M8R	35	22	44	2.2	± 20	1.6	30	15	20	8

<sup>\*</sup> = Under development

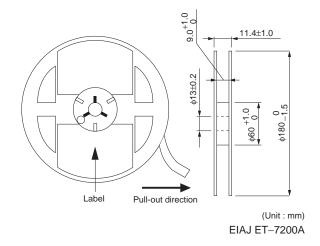


TCT Series AS Case Datasheet

# Packaging specifications



## ●Reel dimensions



# **Notice**

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA EU		CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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# **ROHM Semiconductor:**

TCTAS1V105M8R TCTAS1A476M8R TCTAS0J476M8R TCTAS0J107M8R



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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



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