DTA113Z series

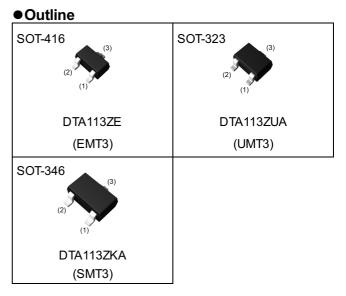
PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

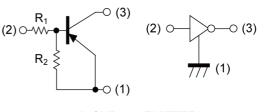
Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-100mA
R ₁	1.0kΩ
R ₂	10kΩ

Features

- 1) Built-In Biasing Resistors,
 - $R_1 = 1.0 k\Omega$, $R_2 = 2.2 k\Omega$
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC113Z series
- 6) Lead Free/RoHS Compliant.



Inner circuit



(1) GND (+) (EMITTER)
(2) IN (BASE)
(3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA113ZE	SOT-416 (EMT3)	1616	TL	180	8	3000	E11
DTA113ZUA	SOT-323 (UMT3)	2021	T106	180	8	3000	111
DTA113ZKA	SOT-346 (SMT3)	2928	T146	180	8	3000	E11

• Absolute maximum ratings ($T_a = 25^{\circ}C$)

F	Symbol	Values	Unit	
Supply voltage		V _{cc}	-50	V
Input voltage	V _{IN}	-10 to 5	V	
Output current		Ι _ο	-100	mA
Collector current		I _{C(MAX)} *1	-100	mA
	DTA113ZE		150	
Power dissipation	DTA113ZUA	P _D *2	200	mW
	DTA113ZKA		200	
Junction temperature		Tj	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

• Electrical characteristics (T_a = 25°C)

Devenuetor	Current el	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
	V _{I(off)}	V _{CC} = -5V, I _O = -100µA		-	-0.3		
Input voltage	V _{I(on)}	V _O = -0.3V, I _O = -20mA	-3.0	-	-	V	
Output voltage	V _{O(on)}	I _O = -10mA, I _I = -0.5mA	-	-100	-300	mV	
Input current	I _I	V _I = -5V	-	-	-7.2	mA	
Output current	I _{O(off)}	V _{CC} = -50V, V _I = 0V	-	-	-500	nA	
DC current gain	G _I	V _O = -5V, I _O = -5mA	33	-	-	-	
Input resistance	R ₁	-	0.7	1.0	1.3	kΩ	
Resistance ratio	R_2/R_1	-	8	10	12	-	
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz	

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land.



•Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

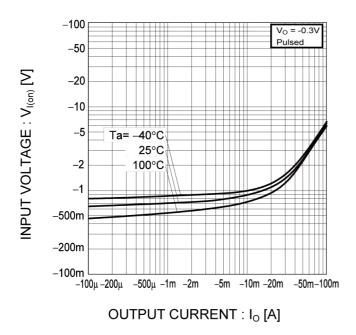


Fig.2 Output current vs. input voltage (OFF characteristics)

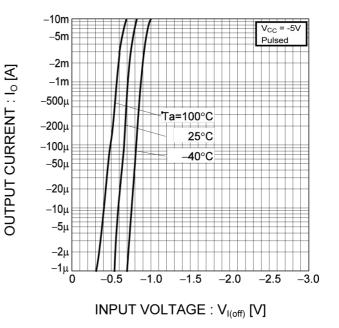
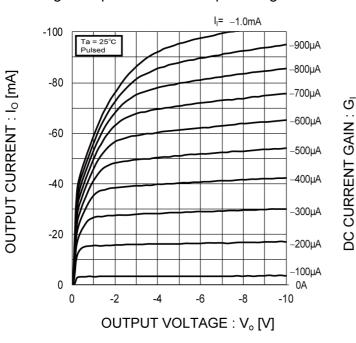
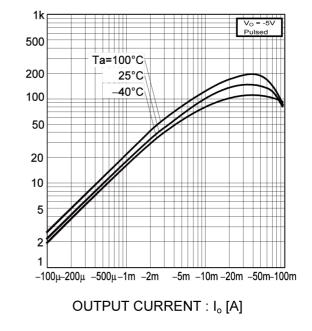


Fig.3 Output current vs. output voltage Fig.4 DC current gain vs. output current







•Electrical characteristic curves (T_a =25°C)

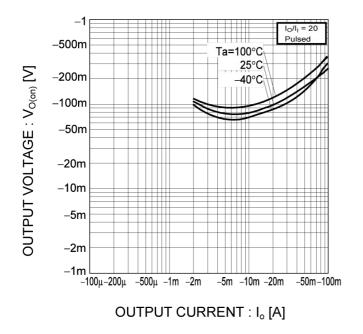


Fig.5 Output voltage vs. output current

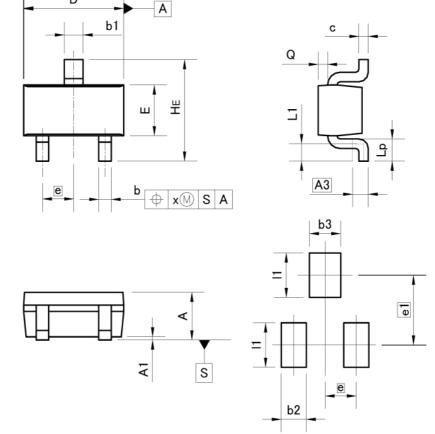


Dimensions



D

(EMT3)



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES		
DIN	MIN	MAX	MIN	MAX	
А	0.60	0.80	0.024	0.031	
A1	0.00	0.10	0.000	0.004	
A3	0.	25	0.0	10	
b	0.15	0.30	0.006	0.012	
b1	0.25	0.40	0.010	0.016	
с	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
е	0.	50	0.020		
HE	1.40	1.80	0.055	0.071	
L1	0.10		0.004	-	
Lp	0.15	-	0.006	-	
Q	0.05	0.25	0.002	0.010	
x	-	0.10	17	0.004	

DIM	MILIMETERS		INCHES		
	MIN	MAX	MIN	MAX	
b2	-	0.40	-	0.016	
b3	-	0.50	-	0.020	
e1	1.10		0.0	43	
1		0.70		0.028	

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
A	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.026		
HE	2.00	2.20	0.079	0.087	
L1	0.10	0.40	0.004	0.016	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
x	-	0.10	-	0.004	
DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.50	-	0.020	
e1	1.	55	0.0	61	
1	-	0.65	-	0.026	

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
А	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.1	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.95		0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
x	—	0.10	-	0.004	
У	-	0.10	-	0.004	
DIM	MILIM	ETERS	INC	HES	
DIM	MIN	ΜΔΧ	MIN	MAX	

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.60	-	0.024	
e1	2.10		0.0	83	
1	-	0.90	-	0.035	

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSIII	CLASSⅢ	CLASSI

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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