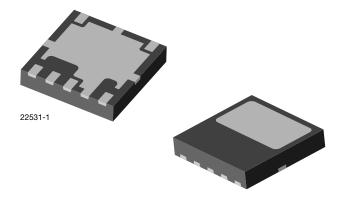
IR Receiver Modules for Remote Control Systems



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SHA

FEATURES

- Height of 0.8 mm
- ± 75° half angle sensitivity
- · Low supply current
- Photo detector and preamplifier in one package
- Suitable for all common data formats including those for short bursts



RoHS

COMPLIANT

GREEN

<u>(5-2008)</u>

- Supply voltage: 2.5 V to 5.5 V, typically even 2.0 V to 5.5 V is possible
- Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

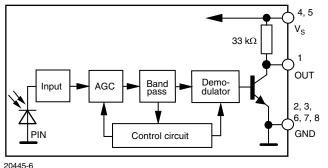
The TSOP573.., TSOP575.. series are miniaturized receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a lead frame, the epoxy lens cap is designed as an IR filter.

The demodulated output signal can be directly decoded by a microprocessor. The TSOP573.. is compatible with all common IR remote control data formats. It is optimized to suppress allmost all spurious pulses from energy saving fluorescent lamps. The TSOP575.. has an excellent noise suppression. It is immune to dimmed LCD backlighting and any fluorescent lamps. IR receivers with AGC3 and AGC5 setting may also suppress some data signals in case of continuous transmission.

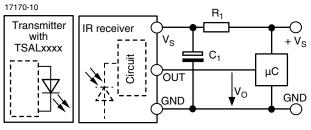
This component has not been qualified according to automotive specifications.

PARTS TABLE		
CARRIER FREQUENCY	STANDARD APPLICATIONS (AGC3)	VERY NOISY ENVIRONMENTS (AGC5)
36 kHz	TSOP57336	TSOP57536
38 kHz	TSOP57338	TSOP57538
40 kHz	TSOP57340	TSOP57540
56 kHz	TSOP57356	TSOP57556

BLOCK DIAGRAM



APPLICATION CIRCUIT



The external components R₁ and C₁ are optional to improve the robustness against electrical overstress (typical values are R₁ = 100 Ω , C₁ = 0.1 μ F).

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ABSOLUTE MAXIMUM RATINGS

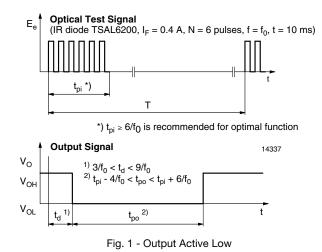
ABSOLUTE MAXIMUM H	ATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		Vs	- 0.3 to + 6	V
Supply current		I _S	5	mA
Output voltage		Vo	- 0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Тj	100	°C
Storage temperature range		T _{stg}	- 25 to + 85	°C
Operating temperature range		T _{amb}	- 25 to + 85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTI	CAL CHARACTERISTICS	(T _{amb} = 25 °	°C, unless c	otherwise s	pecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5		5.5	V
Supply ourrent	$V_{\rm S} = 5 \ V, \ E_{\rm v} = 0$	I _{SD}	0.55	0.7	0.9	mA
Supply current	$E_v = 40$ klx, sunlight	I _{SH}		0.8		mA
Transmission distance	$E_v = 0,$ IR diode TSAL6200, I _F = 250 mA, test signal see fig. 1	d		40		m
Output voltage low	I _{OSL} = 0.5 mA, E _e = 0.7 mW/m ² , test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _{o,} test signal see fig. 1	E _{e min.}		0.2	0.4	mW/m ²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e max.}	50			W/m ²
Directivity	Angle of half transmission distance	φ1/2		± 75		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



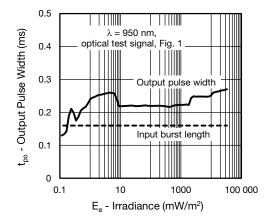
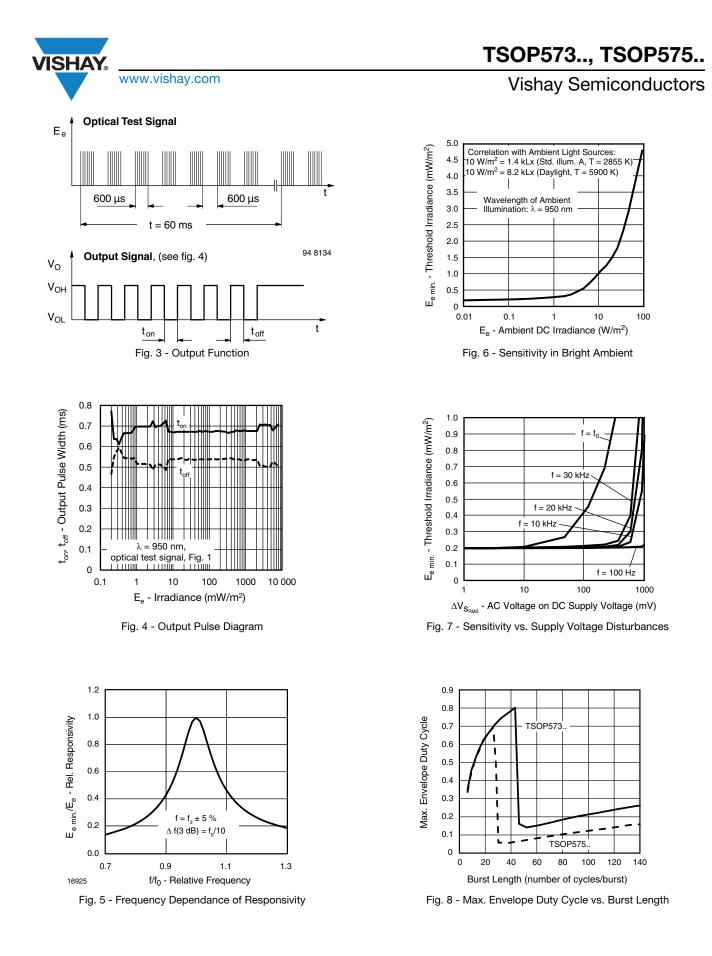


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

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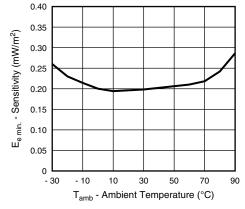
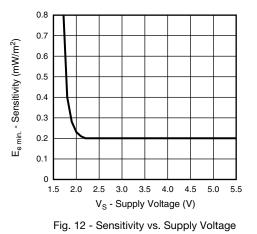


Fig. 9 - Sensitivity vs. Ambient Temperature



1.0 S(\lambda) relative Spectral Sensitivity 0.9 0.8 0.7 0.6 0.5 0.4 0.3 0.2 0.1 0 750 800 850 900 950 1000 1050 1100 1150 λ - Wavelength (nm)

Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

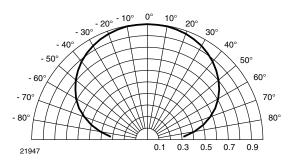


Fig. 11 - Horizontal Directivity

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SUITABLE DATA FORMAT

The TSOP573.., TSOP575.. series is designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP573.., TSOP575.. in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see figure 12 or figure 13)

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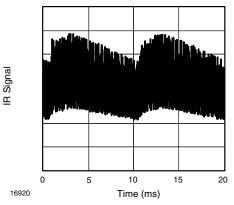


Fig. 13 - IR Signal from Fluorescent Lamp with Low Modulation

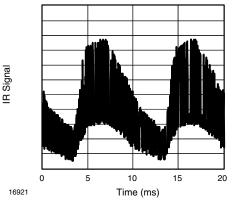


Fig. 14 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP573	TSOP575
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	10 to 42 cycles ≥ 10 cycles	10 to 28 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	42 cycles > 7 x burst length	28 cycles > 12 x burst length
Maximum number of continuous short bursts/second	2000	2000
Recommended for NEC code	Yes	Yes
Recommended for RC5/RC6 code	Yes	Yes
Recommended for r-step code	Yes	Yes
Recommended for Thomson 56 kHz code	Yes	Yes
Recommended for XMP code	Yes	No
Recommended for RCMM code	Yes	Yes
Suppression of interference from fluorescent lamps	Most common disturbance signals are suppressed	Even extreme disturbance signals are suppressed

Note

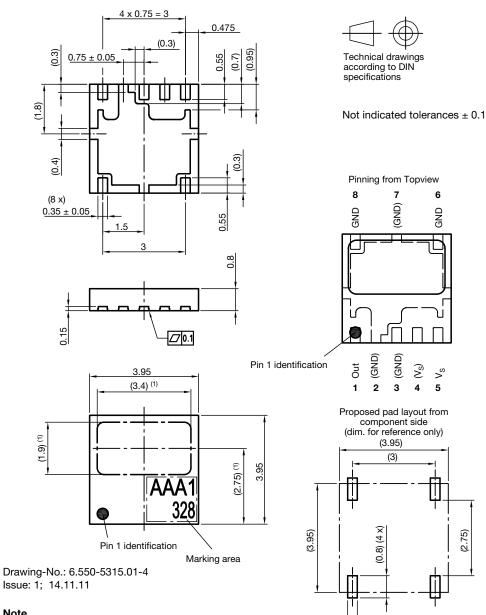
For data formats with bursts longer than 12 cycles/burst please see the datasheet for TSOP574..



PACKAGE DIMENSIONS in millimeters

TSOP573.., TSOP575..

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Note ⁽¹⁾ Optically effective area

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6

(0.35) (4 x)





ASSEMBLY INSTRUCTIONS

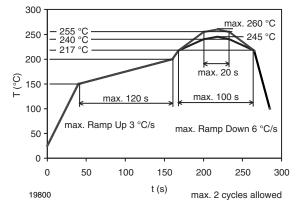
Reflow Soldering

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE

Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off



ORDERING INFORMATION			
ORDERING CODE	PACKAGING	VOLUME ⁽¹⁾	REMARKS
TSOP57TT1	Tape and reel	MOQ: 1800 pcs	3.95 mm x 3.95 mm x 0.75 mm
TSOP57TT2	rape and reer	MOQ: 7000 pcs	5.55 mm x 5.55 mm x 0.75 mm

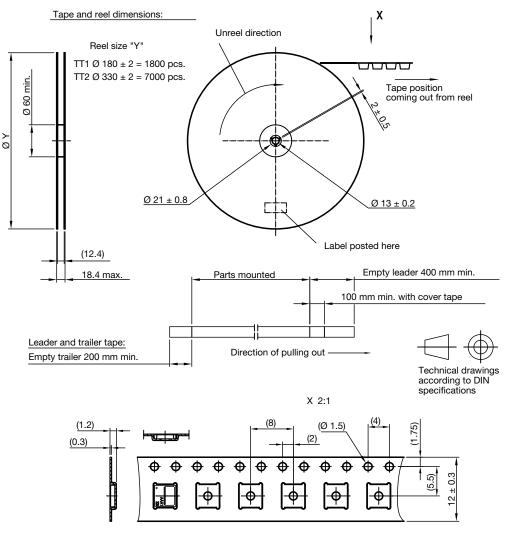
Note

(1) MOQ: minimum order quantity

TAPING VERSION TSOP57... DIMENSIONS in millimeters

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Drawing-No.: 9.700-5347.01-4 Issue: 1; 14.11.11 Not indicated tolerances ± 0.1

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TSOP573.., TSOP575..





LABEL

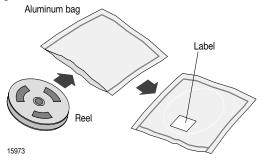
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxx+	Company logo
Long bar code top	Туре	Length
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
Short bar code bottom	Туре	Length
Selection-code	Х	3
Data-code	N	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 $^\circ\text{C}$ + 5 $^\circ\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes. An EIA JEDEC standard JSTD-020 level 3 label is included on all dry bags.

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This bag contains MOISTURE-SENSITIVE DEVICES

3. After bag is opened, devices that will be subjected to reflow solder or other high

a) Humidity Indicator Card reads >10% for level 2a - 5a devices or $>\!\!60\%$ for level 2 devices when read at $23{\pm}5^{\circ}C$

a) Mounted within: <u>168</u> hours of factory conditions

5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure

260

1. Calculated shelf life in sealed bag: 12 months at <40°C and <90%

2. Peak package body temperature: $260 \, {}_{\rm If \ blank, \ see \ adjacent \ bar \ code \ label} \, {}^{\circ}{\rm C}$

relative humidity (RH)

temperature process must be

4. Devices require bake, before mounting, if:

≤30°C/60% RH, or b) Stored per J-STD-033

b) 3a or 3b are not met

Bag Seal Date:

3

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VISHAY SEMICONDUCTORS STANDARD **BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



22178

22650

EIA JEDEC standard JSTD-020 level 3 label is included on all dry bags

If blank, see adjacent bar code label Note: Level and body temperature defined by IPC/JEDEC J-STD-020

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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