



# ANALOG DEVICES

## Wideband 4 GHz, 36 dB Isolation at 1 GHz, CMOS, 1.65 V to 2.75 V, Dual SPDT

### ADG936/ADG936-R

#### FEATURES

Wideband switch:  $-3$  dB @ 4 GHz  
 ADG936 absorptive dual SPDT  
 ADG936-R reflective dual SPDT  
 High off isolation (36 dB @ 1 GHz)  
 Low insertion loss (0.9 dB dc to 1 GHz)  
 Single 1.65 V to 2.75 V power supply  
 CMOS/LVTTL control logic  
 20-lead TSSOP and 4 mm  $\times$  4 mm LFCSP packages  
 Low power consumption (1  $\mu$ A maximum)

#### APPLICATIONS

Wireless communications  
 General-purpose RF switching  
 Dual-band applications  
 High speed filter selection  
 Digital transceiver front end switch  
 IF switching  
 Tuner modules  
 Antenna diversity switching

#### GENERAL DESCRIPTION

The ADG936/ADG936-R are wideband analog switches that comprise two independently selectable SPDT switches using a CMOS process to provide high isolation and low insertion loss to 1 GHz. The ADG936 is an absorptive/matched dual SPDT with 50  $\Omega$  terminated shunt legs; the ADG936-R is a reflective dual SPDT. These devices are designed such that the isolation is high over the dc to 1 GHz frequency range. They have on-board CMOS control logic, eliminating the need for external controlling circuitry. The control inputs are both CMOS and LVTTL

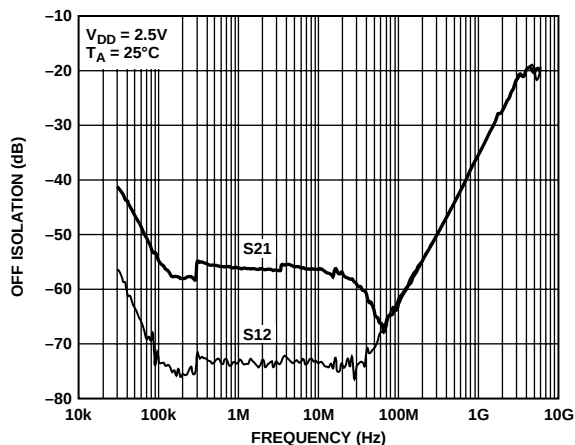


Figure 3. Off Isolation vs. Frequency

#### FUNCTIONAL BLOCK DIAGRAMS

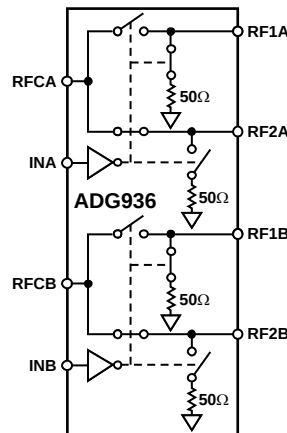


Figure 1.

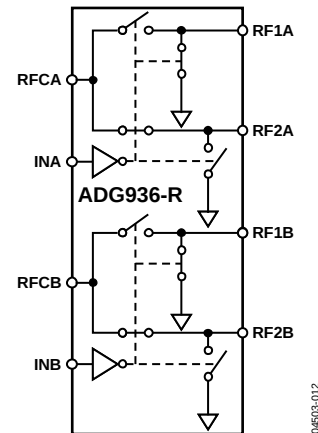


Figure 2.

compatible. The low power consumption of these CMOS devices makes them ideally suited for wireless applications and general-purpose high frequency switching.

#### PRODUCT HIGHLIGHTS

1.  $-36$  dB off isolation @ 1 GHz.
2. 0.9 dB insertion loss @ 1 GHz.
3. 20-lead TSSOP and 4 mm  $\times$  4 mm LFCSP packages.

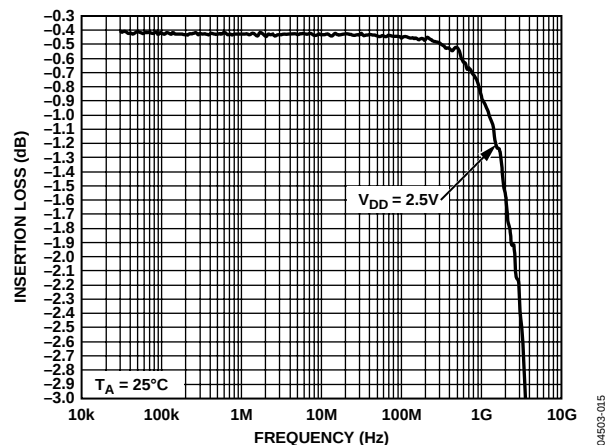


Figure 4. Insertion Loss vs. Frequency

#### Rev. A

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TABLE OF CONTENTS

|                                                    |   |                                           |    |
|----------------------------------------------------|---|-------------------------------------------|----|
| Features .....                                     | 1 | Typical Performance Characteristics ..... | 7  |
| Applications.....                                  | 1 | Test Circuits.....                        | 9  |
| Functional Block Diagrams.....                     | 1 | Applications.....                         | 11 |
| General Description .....                          | 1 | Absorptive vs. Reflective .....           | 11 |
| Product Highlights .....                           | 1 | Filter Selection .....                    | 11 |
| Revision History .....                             | 2 | Tx/Rx Switching .....                     | 11 |
| Specifications.....                                | 3 | Antenna Diversity Switch.....             | 11 |
| Absolute Maximum Ratings.....                      | 4 | Evaluation Board .....                    | 12 |
| ESD Caution.....                                   | 4 | Outline Dimensions .....                  | 13 |
| Pin Configurations and Function Descriptions ..... | 5 | Ordering Guide .....                      | 14 |
| Terminology .....                                  | 6 |                                           |    |

REVISION HISTORY

|                                         |    |
|-----------------------------------------|----|
| <b>8/05—Rev. 0 to Rev. A</b>            |    |
| Changes to Table 1 .....                | 3  |
| Changes to Figure 6.....                | 5  |
| Changes to Ordering Guide .....         | 14 |
| <b>7/04—Revision 0: Initial Version</b> |    |

## SPECIFICATIONS

$V_{DD} = 1.65\text{ V to }2.75\text{ V}$ ,  $GND = 0\text{ V}$ , input power = 0 dBm, all specifications  $T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted.<sup>1</sup>

Table 1.

| Parameter                              | Symbol             | Conditions                                      | Min           | B Version<br>Typ <sup>2</sup> | Max           | Unit          |
|----------------------------------------|--------------------|-------------------------------------------------|---------------|-------------------------------|---------------|---------------|
| <b>AC ELECTRICAL CHARACTERISTICS</b>   |                    |                                                 |               |                               |               |               |
| Operating Frequency <sup>3</sup>       |                    |                                                 | DC            |                               | 2             | GHz           |
| 3 dB Frequency <sup>4</sup>            |                    |                                                 |               |                               | 4             | GHz           |
| Input Power <sup>4</sup>               |                    | 0 V dc bias                                     |               |                               | 7             | dBm           |
|                                        |                    | 0.5 V dc bias                                   |               |                               | 16            | dBm           |
| Insertion Loss                         | $S_{21}, S_{12}$   | DC to 100 MHz; $V_{DD} = 2.5\text{ V} \pm 10\%$ |               | 0.4                           | 0.5           | dB            |
|                                        |                    | 500 MHz; $V_{DD} = 2.5\text{ V} \pm 10\%$       |               | 0.6                           | 0.8           | dB            |
|                                        |                    | 1000 MHz; $V_{DD} = 2.5\text{ V} \pm 10\%$      |               | 0.9                           | 1.25          | dB            |
| Isolation—RFCx to RF1x/RF2x            | $S_{21}, S_{12}$   | 100 MHz                                         | 52            | 60                            |               | dB            |
|                                        |                    | 500 MHz                                         | 40            | 47                            |               | dB            |
|                                        |                    | 1000 MHz                                        | 31            | 36                            |               | dB            |
| Crosstalk—RF1x to RF2x                 | $S_{21}, S_{12}$   | 100 MHz                                         | 53            | 69                            |               | dB            |
|                                        |                    | 500 MHz                                         | 42            | 45                            |               | dB            |
|                                        |                    | 1000 MHz                                        | 34            | 37                            |               | dB            |
| Return Loss (On Channel) <sup>4</sup>  | $S_{11}, S_{22}$   | DC to 100 MHz                                   | 20            | 25                            |               | dB            |
|                                        |                    | 500 MHz                                         | 19            | 23                            |               | dB            |
|                                        |                    | 1000 MHz                                        | 16            | 24                            |               | dB            |
| Return Loss (Off Channel) <sup>4</sup> | $S_{11}, S_{22}$   | DC to 100 MHz                                   | 18            | 24                            |               | dB            |
|                                        |                    | 500 MHz                                         | 17            | 23                            |               | dB            |
|                                        |                    | 1000 MHz                                        | 16            | 21                            |               | dB            |
| On Switching Time <sup>4</sup>         | $t_{ON}$           | 50% CTRL to 90% RF                              |               | 11                            | 14            | ns            |
| Off Switching Time <sup>4</sup>        | $t_{OFF}$          | 50% CTRL to 10% RF                              |               | 10                            | 13            | ns            |
| Rise Time <sup>4</sup>                 | $t_{RISE}$         | 10% to 90% RF                                   |               | 6.1                           | 8             | ns            |
| Fall Time <sup>4</sup>                 | $t_{FALL}$         | 90% to 10% RF                                   |               | 6                             | 8             | ns            |
| 1 dB Compression <sup>4</sup>          | $P_{-1\text{ dB}}$ | 1000 MHz                                        |               | 16                            |               | dBm           |
| Third-Order Intermodulation Intercept  | $IP_3$             | 900 MHz/901 MHz, 4 dBm                          | 27.5          | 32                            |               | dBm           |
| Video Feedthrough <sup>5</sup>         |                    |                                                 |               | 3                             |               | mV p-p        |
| <b>DC ELECTRICAL CHARACTERISTICS</b>   |                    |                                                 |               |                               |               |               |
| Input High Voltage                     | $V_{INH}$          | $V_{DD} = 2.25\text{ V to }2.75\text{ V}$       | 1.7           |                               |               | V             |
|                                        | $V_{INH}$          | $V_{DD} = 1.65\text{ V to }1.95\text{ V}$       | $0.65 V_{CC}$ |                               |               | V             |
| Input Low Voltage                      | $V_{INL}$          | $V_{DD} = 2.25\text{ V to }2.75\text{ V}$       |               |                               | 0.7           | V             |
|                                        | $V_{INL}$          | $V_{DD} = 1.65\text{ V to }1.95\text{ V}$       |               |                               | $0.35 V_{CC}$ | V             |
| Input Leakage Current                  | $I_I$              | $0 \leq V_{IN} \leq 2.75\text{ V}$              |               | $\pm 0.1$                     | $\pm 1$       | $\mu\text{A}$ |
| <b>CAPACITANCE<sup>4</sup></b>         |                    |                                                 |               |                               |               |               |
| RF Port On Capacitance                 | $C_{RF\text{ ON}}$ | $f = 1\text{ MHz}$                              |               | 2.5                           |               | pF            |
| Digital Input Capacitance              | $C_{DIG}$          | $f = 1\text{ MHz}$                              |               | 2                             |               | pF            |
| <b>POWER REQUIREMENTS</b>              |                    |                                                 |               |                               |               |               |
| $V_{DD}$                               |                    |                                                 | 1.65          |                               | 2.75          | V             |
| Quiescent Power Supply Current         | $I_{DD}$           | Digital inputs = 0 V or $V_{DD}$                |               | 0.1                           | 1             | $\mu\text{A}$ |

<sup>1</sup> Temperature range of B Version:  $-40^\circ\text{C}$  to  $+85^\circ\text{C}$ .

<sup>2</sup> Typical values are at  $V_{DD} = 2.5\text{ V}$  and  $25^\circ\text{C}$ , unless otherwise noted.

<sup>3</sup> Operating frequency is the point at which insertion loss degrades by 1 dB.

<sup>4</sup> Guaranteed by design, not subject to production test.

<sup>5</sup> Video feedthrough is the dc transience at the output of any port of the switch when the control voltage is switched from high to low or from low to high in a  $50\ \Omega$  test setup, measured with 1 ns rise time pulses and a 500 MHz bandwidth.

## ABSOLUTE MAXIMUM RATINGS

T<sub>A</sub> = 25°C, unless otherwise noted.

Table 2.

| Parameter                                         | Rating                                         |
|---------------------------------------------------|------------------------------------------------|
| V <sub>DD</sub> to GND                            | –0.5 V to +4 V                                 |
| Inputs to GND                                     | –0.5 V to V <sub>DD</sub> + 0.3 V <sup>1</sup> |
| Continuous Current                                | 30 mA                                          |
| Input Power                                       | 18 dBm                                         |
| Operating Temperature Range                       |                                                |
| Industrial (B Version)                            | –40°C to +85°C                                 |
| Storage Temperature Range                         | –65°C to +150°C                                |
| Junction Temperature                              | 150°C                                          |
| TSSOP Package                                     |                                                |
| θ <sub>JA</sub> Thermal Impedance                 | 143°C/W                                        |
| LFCSP Package                                     |                                                |
| θ <sub>JA</sub> Thermal Impedance (4-Layer Board) | 30.4°C/W                                       |
| Lead Temperature, Soldering (10 sec)              | 300°C                                          |
| IR Reflow, Peak Temperature (<20 sec)             | 235°C                                          |
| ESD                                               | 1 kV                                           |

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Only one absolute maximum rating may be applied at any one time.

Table 3. Truth Table

| INx | RF1x | RF2x |
|-----|------|------|
| 0   | Off  | On   |
| 1   | On   | Off  |

<sup>1</sup> RF1x/RF2x off port inputs to ground = –0.5 V to V<sub>DD</sub> – 0.5 V.

## ESD CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



PIN CONFIGURATIONS AND FUNCTION DESCRIPTIONS

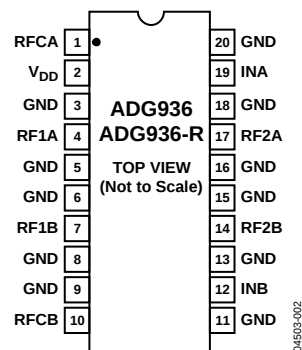
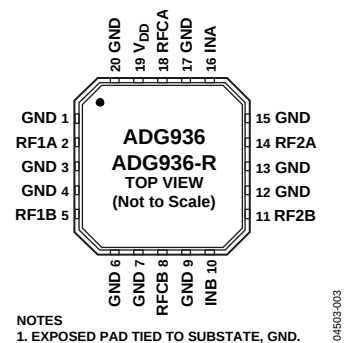


Figure 5. 20-Lead TSSOP (RU-20)



NOTES  
1. EXPOSED PAD TIED TO SUBSTATE, GND.

Figure 6. 20-Lead 4 mm x 4 mm LFCSP (CP-20-1)

Table 4. Pin Function Descriptions

| Pin No.                               |                                      | Mnemonic        | Description                                                                                                    |
|---------------------------------------|--------------------------------------|-----------------|----------------------------------------------------------------------------------------------------------------|
| 20-Lead TSSOP                         | 20-Lead LFCSP                        |                 |                                                                                                                |
| 1                                     | 18                                   | RFCA            | Common RF Port for Switch A.                                                                                   |
| 2                                     | 19                                   | V <sub>DD</sub> | Power Supply Input. These parts can operate from 1.65 V to 2.75 V. V <sub>DD</sub> should be decoupled to GND. |
| 3, 5, 6, 8, 9, 11, 13, 15, 16, 18, 20 | 1, 3, 4, 6, 7, 9, 12, 13, 15, 17, 20 | GND             | Ground Reference Point for All Circuitry on the Part.                                                          |
| 4                                     | 2                                    | RF1A            | RF1A Port.                                                                                                     |
| 7                                     | 5                                    | RF1B            | RF1B Port.                                                                                                     |
| 10                                    | 8                                    | RFCB            | Common RF Port for Switch B.                                                                                   |
| 12                                    | 10                                   | INB             | Logic Control Input.                                                                                           |
| 14                                    | 11                                   | RF2B            | RF2B Port.                                                                                                     |
| 17                                    | 14                                   | RF2A            | RF2A Port.                                                                                                     |
| 19                                    | 16                                   | INA             | Logic Control Input.                                                                                           |

## TERMINOLOGY

Table 5.

| Parameter               | Description                                                                                                                                                                                                                                                                                      |
|-------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| $V_{DD}$                | Most Positive Power Supply Potential.                                                                                                                                                                                                                                                            |
| $I_{DD}$                | Positive Supply Current.                                                                                                                                                                                                                                                                         |
| GND                     | Ground (0 V) Reference.                                                                                                                                                                                                                                                                          |
| INx                     | Logic Control Input.                                                                                                                                                                                                                                                                             |
| $V_{INL}$               | Maximum Input Voltage for Logic 0.                                                                                                                                                                                                                                                               |
| $V_{INH}$               | Minimum Input Voltage for Logic 1.                                                                                                                                                                                                                                                               |
| $I_{INL}$ ( $I_{INH}$ ) | Input Current of the Digital Input.                                                                                                                                                                                                                                                              |
| $C_{IN}$                | Digital Input Capacitance.                                                                                                                                                                                                                                                                       |
| $t_{ON}$                | Delay Between Applying the Digital Control Input and the Output Switching On.                                                                                                                                                                                                                    |
| $t_{OFF}$               | Delay Between Applying the Digital Control Input and the Output Switching Off.                                                                                                                                                                                                                   |
| $t_{RISE}$              | Rise Time. Time for the RF signal to rise from 10% of the on level to 90% of the on level.                                                                                                                                                                                                       |
| $t_{FALL}$              | Fall Time. Time for the RF signal to fall from 90% of the on level to 10% of the on level.                                                                                                                                                                                                       |
| Off Isolation           | The Attenuation Between Input and Output Ports of the Switch When the Switch Control Voltage Is in the Off Condition.                                                                                                                                                                            |
| Insertion Loss          | The Attenuation Between Input and Output Ports of the Switch When the Switch Control Voltage Is in the On Condition.                                                                                                                                                                             |
| Crosstalk               | Measure of Unwanted Signal Coupled Through from One Channel to Another as a Result of Parasitic Capacitance.                                                                                                                                                                                     |
| $P_{-1\text{ dB}}$      | 1 dB Compression Point. The RF input power level at which the switch insertion loss increases by 1 dB over its low level value. $P_{-1\text{ dB}}$ is a measure of how much power the on switch can handle before the insertion loss increases by 1 dB.                                          |
| $IP_3$                  | Third-Order Intermodulation Intercept. This is a measure of the power in false tones that occurs when closely spaced tones are passed through a switch, whereby the nonlinearity of the switch causes these false tones to be generated.                                                         |
| Return Loss             | The Amount of Reflected Power Relative to the Incident Power at a Port. Large return loss indicates good matching. By measuring return loss, the voltage standing wave ratio (VSWR) can be calculated from conversion charts. VSWR indicates the degree of matching present at a switch RF port. |
| Video Feedthrough       | Spurious Signals Present at the RF Ports of the Switch When the Control Voltage Is Switched from High to Low or from Low to High Without an RF Signal Present.                                                                                                                                   |

# TYPICAL PERFORMANCE CHARACTERISTICS

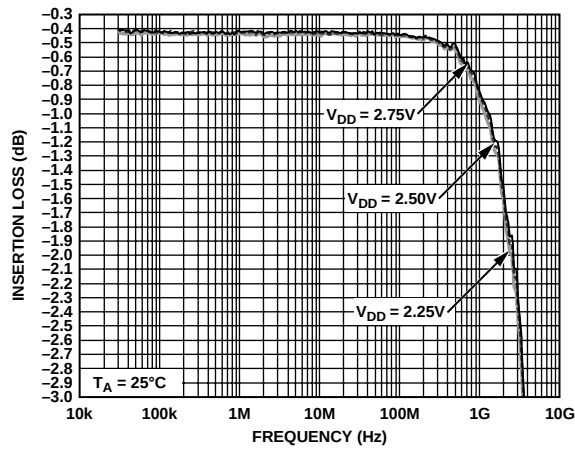


Figure 7. Insertion Loss vs. Frequency over Supplies (S12 and S21)

04503-015

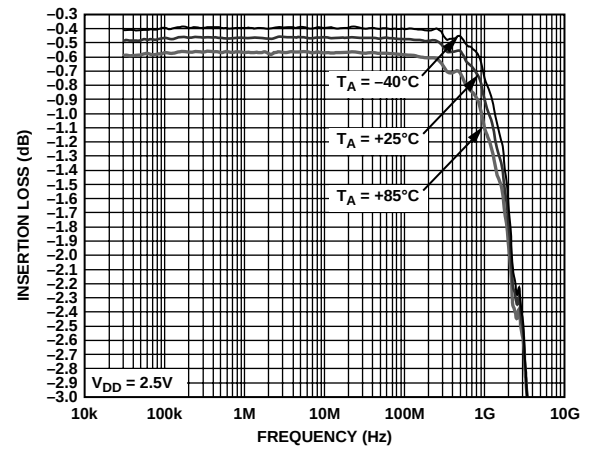


Figure 10. Insertion Loss vs. Frequency over Temperature (S12 and S21)

04503-018

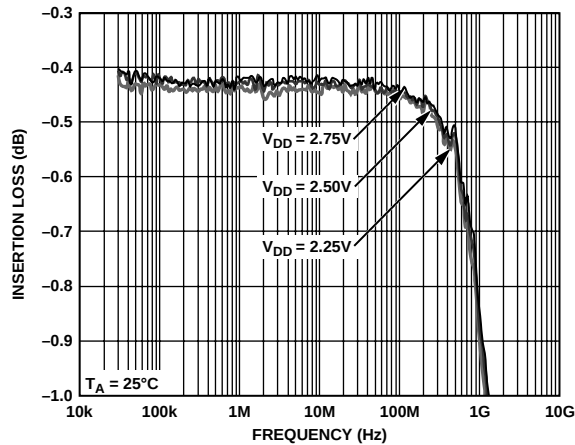


Figure 8. Insertion Loss vs. Frequency over Supplies (S12 and S21) (Zoomed Figure 7)

04503-016

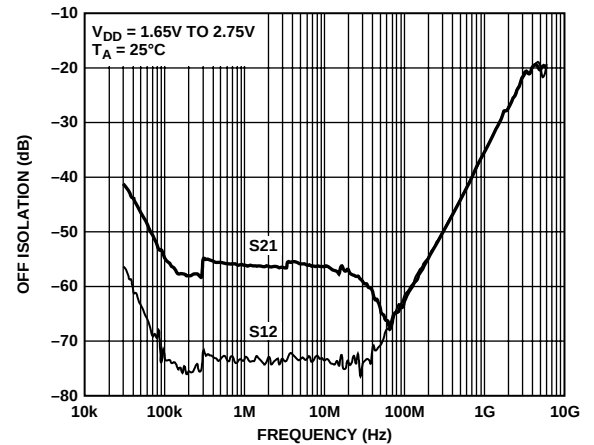


Figure 11. Isolation vs. Frequency over Supplies

04503-019

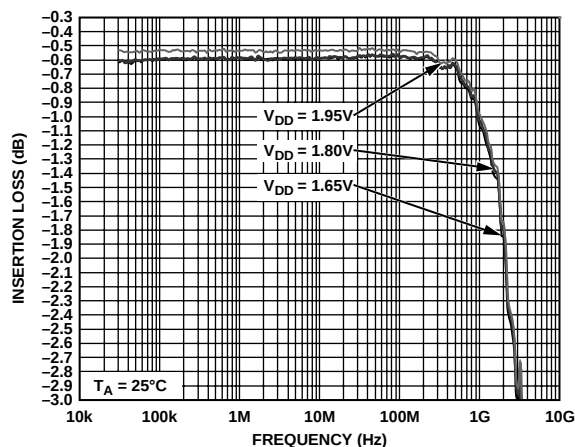


Figure 9. Insertion Loss vs. Frequency over Supplies (S12 and S21)

04503-017

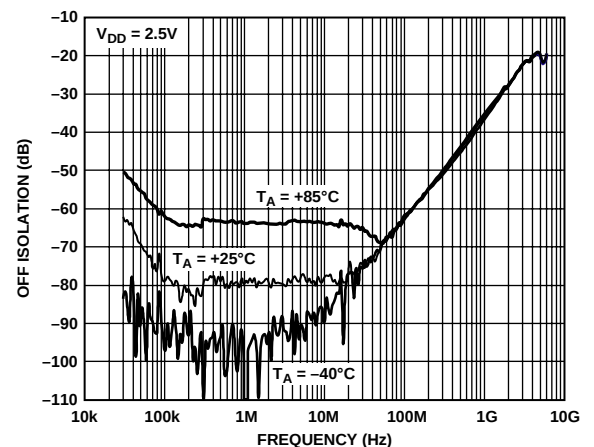


Figure 12. Isolation vs. Frequency over Temperature

04503-020

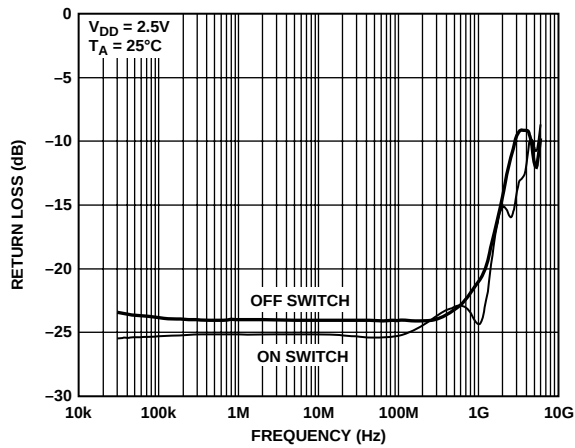


Figure 13. Return Loss vs. Frequency (S11)

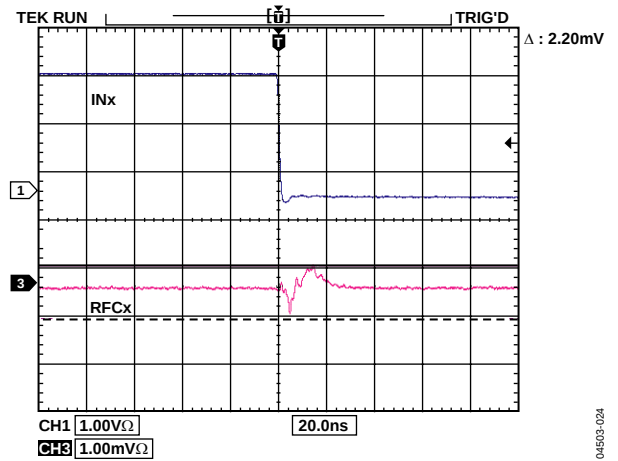


Figure 16. Video Feedthrough

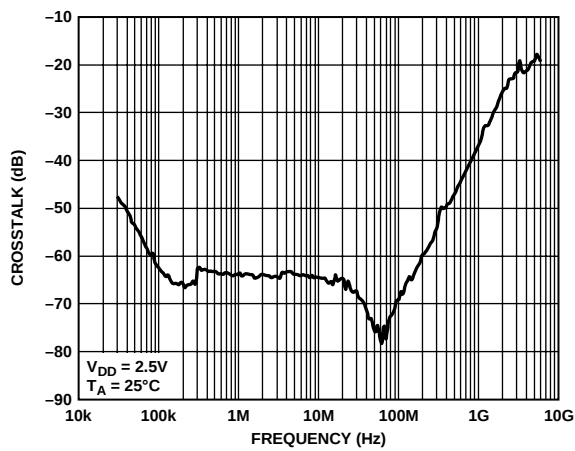


Figure 14. Crosstalk vs. Frequency (S12 and S21)

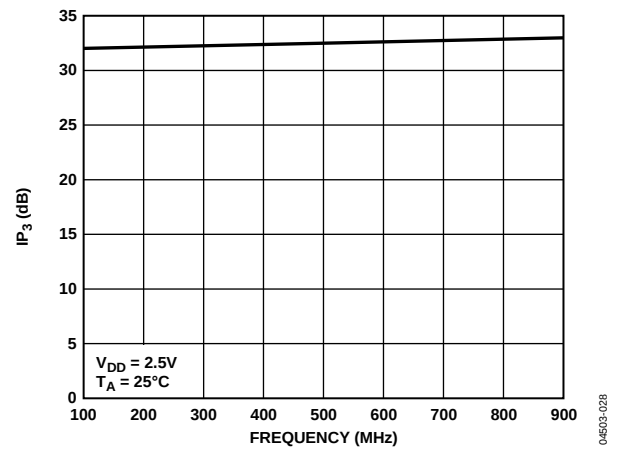


Figure 17.  $IP_3$  vs. Frequency

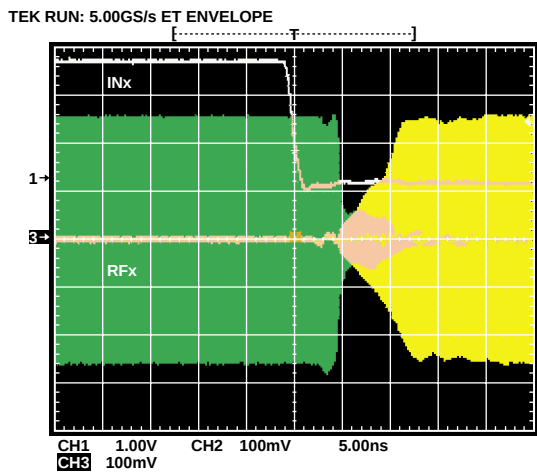


Figure 15. Switch Timing

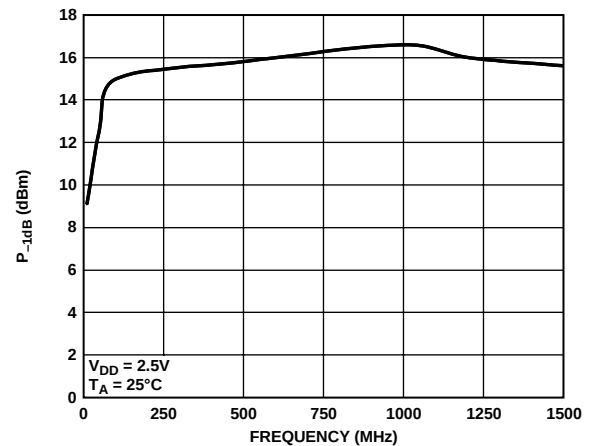


Figure 18.  $P_{-1dB}$  vs. Frequency

## TEST CIRCUITS

Similar setup for the ADG936. Additional pins omitted for clarity.

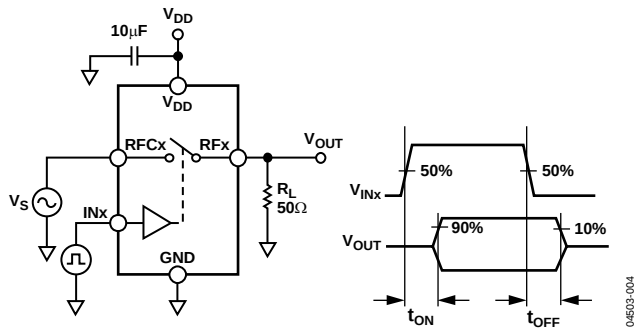


Figure 19. Switch Timing:  $t_{ON}$ ,  $t_{OFF}$

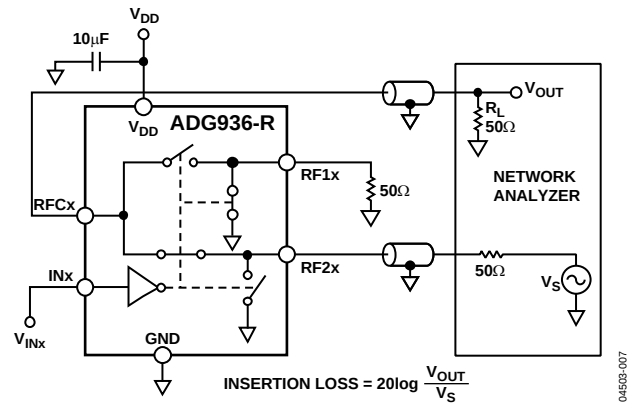


Figure 22. Insertion Loss

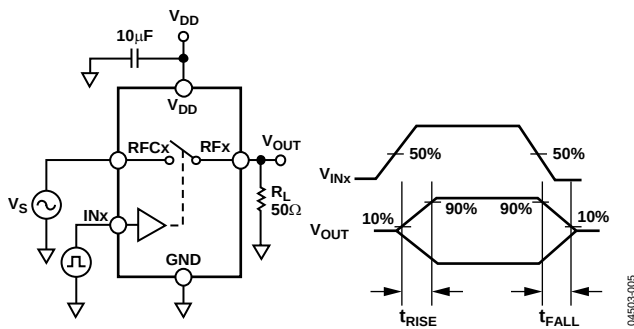


Figure 20. Switch Timing:  $t_{RISE}$ ,  $t_{FALL}$

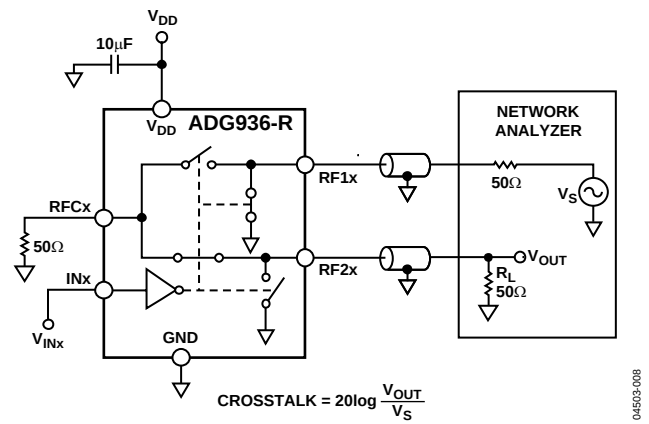


Figure 23. Crosstalk

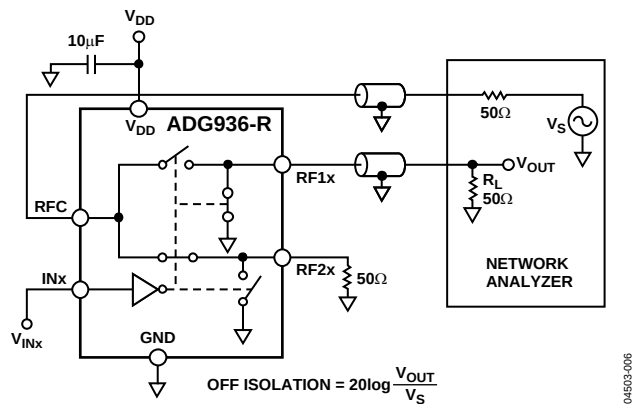


Figure 21. Off Isolation

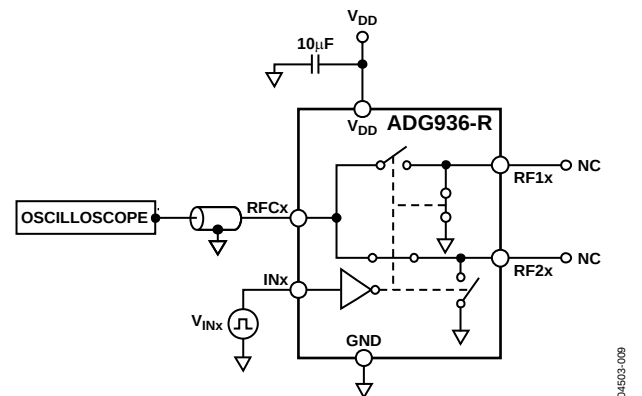


Figure 24. Video Feedthrough

**ADG936/ADG936-R**



Figure 25.  $IP_3$



Figure 26.  $P_{-1}$  dB

## APPLICATIONS

The ADG936/ADG936-R are ideal solutions for low power, high frequency applications. The low insertion loss, high isolation between ports, low distortion, and low current consumption of these parts make them excellent solutions for many high frequency switching applications. They can be used in applications such as switchable filters, transmitters and receivers for radar systems, and communication systems from base stations to cell phones.

The ADG9xx family of wideband switches is designed to meet the demands of devices transmitting at ISM band frequencies to 1 GHz and higher. The low insertion loss, high isolation between ports, single pin control interface, no requirement for dc blocking capacitors, and TTL interface compatibility make them cost-effective and easy-to-integrate switching solutions for many high frequency switching and low power applications, because the parts can handle up to 16 dBm of power.

### ABSORPTIVE VS. REFLECTIVE

The ADG936 is an absorptive (matched) switch with 50  $\Omega$  terminated shunt legs; the ADG936-R is a reflective switch with 0  $\Omega$  terminated shunts to ground. The ADG936 absorptive switch has a good VSWR on each port, regardless of the switch mode. An absorptive switch should be used when there is a need for a good VSWR that is looking into the port but not passing the through-signal to the common port. The ADG936 is, therefore, ideal for applications that require minimum reflections back to the RF source. It also ensures that the maximum power is transferred to the load.

The ADG936-R reflective switch is suitable for applications in which high off-port VSWR does not matter. The switch also has some other desired performance features. It can be used in many applications, including high speed filter selection. In most cases, an absorptive switch can be used instead of a reflective switch, but not vice versa.

### FILTER SELECTION

The ADG936 and ADG936-R can be used to switch high frequency signals between different filters and to multiplex the signal to the output. These dual SPDT switches are also ideal for high speed signal routing and for switching high speed differential signals.

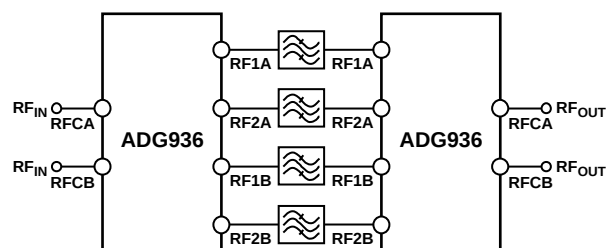


Figure 27. Filter Selection

### Tx/Rx SWITCHING

The low insertion loss and high isolation between ports ensure that the ADG936/ADG936-R are suitable transmit/receive switches for all ISM band and wireless LAN applications, providing the required isolation between the transmit and receive signals.

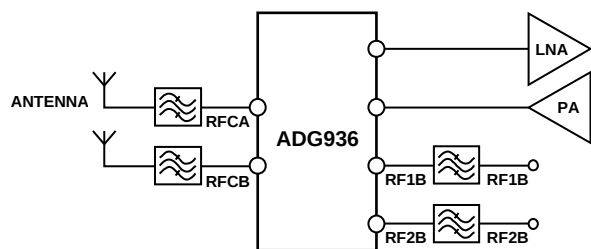


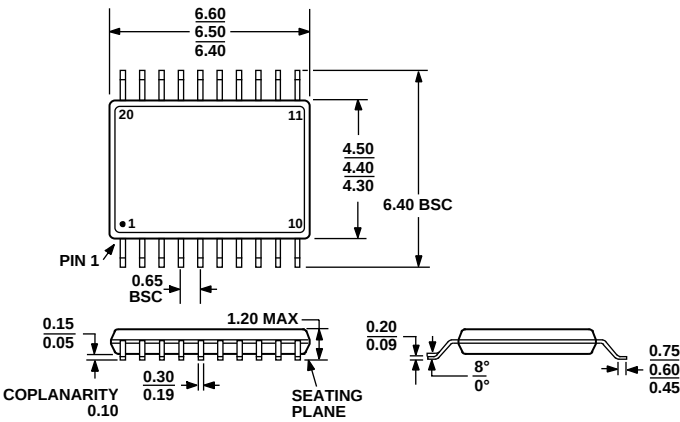
Figure 28. Tx/Rx Switching

### ANTENNA DIVERSITY SWITCH

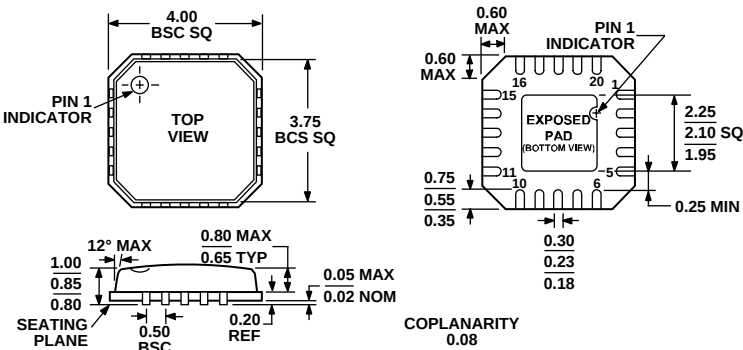
The ADG936/ADG936-R are ideal for use as antenna diversity switches, switching in different antennas to the tuner. The low insertion loss, which ensures minimum signal loss and high isolation between channels, makes these dual SPDT switches suitable for switching applications in tuner modules and set-top boxes.



OUTLINE DIMENSIONS



COMPLIANT TO JEDEC STANDARDS MO-153-AC  
Figure 30. 20-Lead Thin Shrink Small Outline Package [TSSOP]  
(RU-20)  
Dimensions shown in millimeters



COMPLIANT TO JEDEC STANDARDS MO-220-VGGD-1  
Figure 31. 20-Lead Lead Frame Chip Scale Package [LFCSP\_VQ]  
4 mm × 4 mm Body, Very Thin Quad  
(CP-20-1)  
Dimensions shown in millimeters

# ADG936/ADG936-R

## ORDERING GUIDE

| Model                         | Temperature Range | Package Description                               | Package Option |
|-------------------------------|-------------------|---------------------------------------------------|----------------|
| ADG936BRU                     | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-500RL7              | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-REEL                | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-REEL7               | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRUZ <sup>1</sup>       | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRUZ-REEL <sup>1</sup>  | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRUZ-REEL7 <sup>1</sup> | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BCP                     | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-500RL7              | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-REEL                | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-REEL7               | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCPZ <sup>1</sup>       | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCPZ-REEL <sup>1</sup>  | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCPZ-REEL7 <sup>1</sup> | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| EVAL-ADG936EB                 |                   | Evaluation Board                                  | RU-20          |
| ADG936BRU-R                   | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-R-500RL7            | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-R-REEL              | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BRU-R-REEL7             | –40°C to +85°C    | 20-Lead Thin Shrink Small Outline Package (TSSOP) | RU-20          |
| ADG936BCP-R                   | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-R-500RL7            | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-R-REEL              | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| ADG936BCP-R-REEL7             | –40°C to +85°C    | 20-Lead Lead Frame Chip Scale Package (LFCSP_VQ)  | CP-20-1        |
| EVAL-ADG936REB                |                   | Evaluation Board                                  | RU-20          |

<sup>1</sup> Z = PB-free part.

**NOTES**

## **NOTES**



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
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- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
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Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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