74AUP1G126 Low-power buffer/line driver; 3-state Rev. 7 — 16 May 2018

Product data sheet

1 General description

The 74AUP1G126 provides a single non-inverting buffer/line driver with 3-state output. The 3-state output is controlled by the output enable input (OE). A LOW level at pin OE causes the output to assume a high-impedance OFF-state. This device has the input-disable feature, which allows floating input signals. The inputs are disabled when the output enable input OE is LOW.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

2 Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \ \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- · Input-disable feature allows floating input conditions
- I_{OFF} circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

ne<mark>x</mark>peria

Low-power buffer/line driver; 3-state

3 Ordering information

Table 1. Ordering i	nformation							
Type number	Package	Package						
	Temperature range	Name	Description	Version				
74AUP1G126GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
74AUP1G126GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886				
74AUP1G126GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm	SOT891				
74AUP1G126GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm	SOT1115				
74AUP1G126GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202				
74AUP1G126GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm	SOT1226				

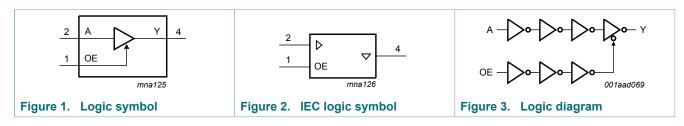
4 Marking

Table 2. Marking

Type number	Marking code ^[1]
74AUP1G126GW	pN
74AUP1G126GM	pN
74AUP1G126GF	pN
74AUP1G126GN	pN
74AUP1G126GS	pN
74AUP1G126GX	pN

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

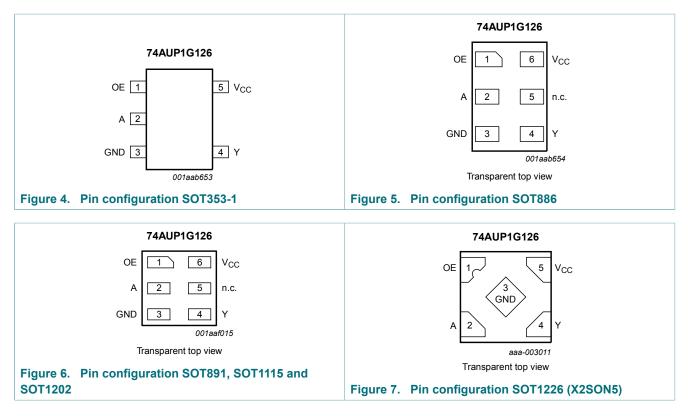
5 Functional diagram



Low-power buffer/line driver; 3-state

6 Pinning information

6.1 Pinning



6.2 Pin description

Table 3. Pin description						
Symbol	Pin		Description			
	TSSOP5 and X2SON5	XSON6				
OE	1	1	output enable input			
A	2	2	data input			
GND	3	3	ground (0 V)			
Y	4	4	data output			
n.c.	-	5	not connected			
V _{CC}	5	6	supply voltage			

Functional description 7

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = Don't care; Z = high-impedance OFF-state.

Input		Output
OE	A	Y
н	L	L
Н	Н	Н
L	X	Z

Limiting values 8

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V ₀ < 0 V	-50	-	mA
Vo	output voltage	Active mode ^[1]	-0.5	V _{CC} + 0.5	V
		Power-down mode ^[1]	-0.5	+4.6	V
I _O	output current	V_{O} = 0 V to V_{CC}	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C ^[2]	-	250	mW

For XSON6 and X2SON5 packages: above 118 °C the value of Ptot derates linearly with 7.8 mW/K.

Recommended operating conditions 9

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V_{CC} = 0.8 V to 3.6 V	0	200	ns/V

© Nexperia B.V. 2018. All rights reserved.

Low-power buffer/line driver; 3-state

10 Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
T _{amb} = 2	5 °C					<u> </u>
VIH	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.72	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.31	V
		$I_{\rm O}$ = 3.1 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.44	V
		$I_{\rm O}$ = 2.7 mA; $V_{\rm CC}$ = 3.0 V	-	-	0.31	V
		$I_{\rm O}$ = 4.0 mA; $V_{\rm CC}$ = 3.0 V	-	-	0.44	V
I	input leakage current	$V_{\rm I}$ = GND to 3.6 V; $V_{\rm CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
l _{oz}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.1	μA
IOFF	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μA
ΔI _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μA

© Nexperia B.V. 2018. All rights reserved.

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$		-	-	0.5	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1]	-	-	40	μA
		OE input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1]	-	-	110	μA
		all inputs; V _I = GND to 3.6 V; OE = GND; V _{CC} = 0.8 V to 3.6 V	[2]	-	-	1	μA
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V_{I} = GND or V_{CC}		-	0.9	-	pF
Co	output capacitance	output enabled; V_0 = GND; V_{CC} = 0 V		-	1.7	-	pF
		output disabled; V _{CC} = 0 V to 3.6 V; V _O = GND or V _{CC}		-	1.5	-	pF
T _{amb} = -4	40 °C to +85 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.	70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.	65 × V _{CC}	-	-	V
		V_{CC} = 2.3 V to 2.7 V		1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V		2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V		-	-	$0.30 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V		-	-	$0.35 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V		-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V		-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$					
		$I_{\rm O}$ = -20 µA; V _{CC} = 0.8 V to 3.6 V	٧	/ _{CC} - 0.1	-	-	V
		I_{O} = -1.1 mA; V_{CC} = 1.1 V	0	.7 × V _{CC}	-	-	V
		$I_{\rm O}$ = -1.7 mA; $V_{\rm CC}$ = 1.4 V		1.03	-	-	V
		$I_{\rm O}$ = -1.9 mA; $V_{\rm CC}$ = 1.65 V		1.30	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V		1.97	-	-	V
		$I_{\rm O}$ = -3.1 mA; $V_{\rm CC}$ = 2.3 V		1.85	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V		2.67	-	-	V
		$I_{\rm O}$ = -4.0 mA; $V_{\rm CC}$ = 3.0 V		2.55	-	-	V

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$					
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V		_	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V		-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V		-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V		-	-	0.35	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V		-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V		-	-	0.45	V
		$I_{\rm O}$ = 2.7 mA; $V_{\rm CC}$ = 3.0 V		-	-	0.33	V
		$I_{\rm O}$ = 4.0 mA; $V_{\rm CC}$ = 3.0 V		-	-	0.45	V
l _l	input leakage current	$V_{\rm I}$ = GND to 3.6 V; $V_{\rm CC}$ = 0 V to 3.6 V		-	-	±0.5	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$		-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V		-	-	±0.5	μA
ΔI _{OFF}	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$		-	-	±0.6	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$		-	-	0.9	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1]	-	-	50	μA
		OE input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1]	-	-	120	μA
		all inputs; V _I = GND to 3.6 V; OE = GND; V _{CC} = 0.8 V to 3.6 V	[2]	-	-	1	μA
T _{amb} = -4	10 °C to +125 °C			· · · · · ·			
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V		0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V		0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V		1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V		2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V		-	-	$0.25 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V		-	-	$0.30 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V		-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V		-	-	0.9	V

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	$0.6 \times V_{CC}$	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		$I_{\rm O}$ = -4.0 mA; $V_{\rm CC}$ = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		$I_{\rm O}$ = 2.7 mA; $V_{\rm CC}$ = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
l _l	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1] _	-	75	μA
		OE input; $V_I = V_{CC} - 0.6 \text{ V}$; $I_O = 0 \text{ A}$; $V_{CC} = 3.3 \text{ V}$	[1] _	-	180	μA
		all inputs; V _I = GND to 3.6 V; OE = GND; V _{CC} = 0.8 V to 3.6 V	[2] _	-	1	μA

Low-power buffer/line driver; 3-state

11 Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 10

Symbol	Parameter	Conditions	Min	Typ ^[1]	Max	Unit
T _{amb} = 25	°C; C _L = 5 pF					
t _{pd}	propagation delay	A to Y; see Figure 8 [2]				
		V _{CC} = 0.8 V	-	20.6	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	5.5	10.5	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	3.9	6.1	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.2	4.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.6	2.6	3.6	ns
		V _{CC} = 3.0 V to 3.6 V	1.4	2.4	3.1	ns
en	enable time	OE to Y; see Figure 9 ^[3]				
		V _{CC} = 0.8 V	-	71.6	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	6.2	12.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.2	6.9	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.3	5.3	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	2.4	3.6	ns
		V _{CC} = 3.0 V to 3.6 V	1.3	2.0	2.9	ns
dis	disable time	OE to Y; see Figure 9 [4]				
		V _{CC} = 0.8 V	-	10.3	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	4.2	6.2	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	3.2	4.4	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.1	4.4	ns
		V_{CC} = 2.3 V to 2.7 V	1.7	2.4	3.2	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	2.8	3.6	ns
T _{amb} = 25	°C; C _L = 10 pF					
pd	propagation delay	see <u>Figure 8</u> ^[2]				
		V _{CC} = 0.8 V	-	24.0	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	6.4	12.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	4.5	7.3	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.8	5.5	ns
		V_{CC} = 2.3 V to 2.7 V	2.1	3.2	4.2	ns
		V _{CC} = 3.0 V to 3.6 V	1.8	3.0	3.8	ns

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Typ ^[1]	Max	Unit
t _{en}	enable time	see <u>Figure 9</u> ^[3]				
		V _{CC} = 0.8 V	-	75.3	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	7.1	14.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.8	8.0	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	3.9	5.9	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	2.9	4.2	ns
		V_{CC} = 3.0 V to 3.6 V	1.4	2.6	3.6	ns
t _{dis}	disable time	see <u>Figure 9</u> ^[4]				
		$V_{CC} = 0.8 V$	-	12.2	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.5	5.3	7.6	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.1	5.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.4	4.2	5.7	ns
		V_{CC} = 2.3 V to 2.7 V	1.9	3.2	4.1	ns
		V_{CC} = 3.0 V to 3.6 V	2.4	4.1	5.0	ns
T _{amb} = 25	°C; C _L = 15 pF			-		
t _{pd}	propagation delay	see <u>Figure 8</u> ^[2]				
		V _{CC} = 0.8 V	-	27.4	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	7.2	14.1	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.1	8.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.3	6.3	ns
		V _{CC} = 2.3 V to 2.7 V	2.0	3.7	4.9	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.5	4.4	ns
t _{en}	enable time	see <u>Figure 9</u> ^[3]				
		V _{CC} = 0.8 V	-	79.2	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	7.8	15.8	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.4	8.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	4.3	6.7	ns
		V_{CC} = 2.3 V to 2.7 V	1.8	3.4	4.8	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	3.1	4.3	ns
				1		

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Typ ^[1]	Max	Unit
t _{dis}	disable time	see <u>Figure 9</u> ^[4]				
		V _{CC} = 0.8 V	-	14.9	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.3	6.4	8.5	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.0	6.6	ns
		V _{CC} = 1.65 V to 1.95 V	3.1	5.4	6.6	ns
		V_{CC} = 2.3 V to 2.7 V	2.4	4.0	5.0	ns
		V _{CC} = 3.0 V to 3.6 V	3.2	5.3	6.2	ns
T _{amb} = 25	°C; C _L = 30 pF					
t _{pd}	propagation delay	see <u>Figure 8</u> ^[2]				
		V _{CC} = 0.8 V	-	37.4	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.8	9.5	18.7	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	6.7	10.8	ns
		$V_{\rm CC}$ = 1.65 V to 1.95 V	2.9	5.6	8.4	ns
		V_{CC} = 2.3 V to 2.7 V	2.7	4.8	6.3	ns
		V_{CC} = 3.0 V to 3.6 V	2.7	4.6	5.8	ns
t _{en}	enable time	see <u>Figure 9</u> ^[3]				
		V _{CC} = 0.8 V	-	90.6	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.7	10.0	20.4	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	6.9	11.3	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	5.6	8.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	4.5	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.2	4.2	5.8	ns
t _{dis}	disable time	see <u>Figure 9</u> ^[4]				
		V _{CC} = 0.8 V	-	51.6	-	ns
		V _{CC} = 1.1 V to 1.3 V	6.0	9.8	13.6	ns
		V _{CC} = 1.4 V to 1.6 V	4.5	7.7	10.5	ns
		V _{CC} = 1.65 V to 1.95 V	5.2	8.8	11.4	ns
		V_{CC} = 2.3 V to 2.7 V	3.9	6.4	7.4	ns
		V _{CC} = 3.0 V to 3.6 V	5.5	9.0	10.7	ns

74AUP1G126

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Typ ^[1]	Мах	Unit
T _{amb} = 25	°C	·			1	
C _{PD} p	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$	5]			
		output enabled				
		V _{CC} = 0.8 V	-	2.7	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.8	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.6	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.2	-	pF

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} . [3] t_{en} is the same as t_{PZH} and t_{PZL} .

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 10

Symbol	Parameter	Conditions	-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Max	Min	Max	
C _L = 5 pl	F				1	1	
t _{pd}	propagation delay	A to Y; see Figure 8 [1]					
		V _{CC} = 1.1 V to 1.3 V	2.5	11.7	2.5	12.9	ns
		V _{CC} = 1.4 V to 1.6 V	2.0	7.3	2.0	8.1	ns
	V _{CC} = 1.65 V to 1.95 V	1.7	6.1	1.7	6.7	ns	
		V_{CC} = 2.3 V to 2.7 V	1.4	4.3	1.4	4.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	3.9	1.2	4.4	ns
t _{en}	enable time	OE to Y; see Figure 9 ^[2]					
		V _{CC} = 1.1 V to 1.3 V	2.6	13.6	2.6	13.6	ns
		V_{CC} = 1.4 V to 1.6 V	2.2	7.4	2.2	7.7	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	5.9	1.7	6.2	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	3.8	1.4	4.1	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	3.2	1.2	3.4	ns

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	-40 °C t	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Мах	Min	Мах	
t _{dis}	disable time	OE to Y; see Figure 9 [3]					
		V _{CC} = 1.1 V to 1.3 V	2.9	6.4	2.9	6.5	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.6	2.2	4.7	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	4.6	1.7	4.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.4	3.4	1.4	3.6	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	3.7	1.2	3.8	ns
C _L = 10 p	р F			1	1	1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[1]					
		V _{CC} = 1.1 V to 1.3 V	3.0	13.8	3.0	15.2	ns
		V _{CC} = 1.4 V to 1.6 V	1.9	8.5	1.9	9.4	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	6.8	1.7	7.6	ns
		V_{CC} = 2.3 V to 2.7 V	1.6	5.3	1.6	5.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	4.6	1.6	5.2	ns
t _{en}	enable time	OE to Y; see Figure 9 ^[2]					
		V _{CC} = 1.1 V to 1.3 V	3.0	15.4	3.0	15.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	8.3	2.1	8.6	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	6.5	1.7	6.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.4	4.5	1.4	4.8	ns
		V_{CC} = 3.0 V to 3.6 V	1.3	3.8	1.3	4.0	ns
t _{dis}	disable time	OE to Y; see Figure 9 ^[3]					
		V _{CC} = 1.1 V to 1.3 V	3.3	7.9	3.3	7.9	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	5.7	2.1	5.9	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	5.8	1.7	6.0	ns
		V_{CC} = 2.3 V to 2.7 V	1.4	4.3	1.4	4.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.3	5.2	1.3	5.3	ns

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	-40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
			Min	Мах	Min	Max	
C _L = 15 p	ρF					1	
t _{pd}	propagation delay	A to Y; see Figure 8	[1]				
		V _{CC} = 1.1 V to 1.3 V	3.3	15.8	3.3	17.5	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	9.8	2.5	10.9	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	7.9	2.0	8.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.8	6.0	1.8	6.7	ns
		V_{CC} = 3.0 V to 3.6 V	1.8	5.4	1.8	6.1	ns
en	enable time	OE to Y; see Figure 9	[2]				
		V _{CC} = 1.1 V to 1.3 V	3.3	17.1	3.3	17.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.9	9.4	2.9	9.7	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	7.3	2.0	7.7	ns
		V_{CC} = 2.3 V to 2.7 V	1.7	5.2	1.7	5.6	ns
		V_{CC} = 3.0 V to 3.6 V	1.5	4.5	1.5	4.7	ns
t _{dis}	disable time	OE to Y; see Figure 9	[3]				
		V _{CC} = 1.1 V to 1.3 V	3.7	9.3	3.7	9.4	ns
		V_{CC} = 1.4 V to 1.6 V	2.5	6.9	2.5	7.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	7.4	2.0	7.5	ns
		V_{CC} = 2.3 V to 2.7 V	1.7	5.1	1.7	5.5	ns
		V_{CC} = 3.0 V to 3.6 V	1.5	6.7	1.5	6.9	ns

Nexperia

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	-40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
			Min	Мах	Min	Max	
C _L = 30 p	oF					1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[1]					
		V _{CC} = 1.1 V to 1.3 V	4.4	21.4	4.4	24.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	13.0	3.0	14.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	10.3	2.6	11.5	ns
		V_{CC} = 2.3 V to 2.7 V	2.5	7.8	2.5	8.7	ns
		V _{CC} = 3.0 V to 3.6 V	2.5	7.0	2.5	8.3	ns
en	enable time	OE to Y; see Figure 9 [2]					
		V _{CC} = 1.1 V to 1.3 V	4.3	22.0	4.3	22.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.7	12.0	3.7	12.5	ns
		V _{CC} = 1.65 V to 1.95 V	3.2	9.5	3.2	10.1	ns
		V_{CC} = 2.3 V to 2.7 V	2.9	6.8	2.9	7.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.7	6.4	2.7	6.7	ns
t _{dis}	disable time	OE to Y; see <u>Figure 9</u> ^[3]					
		V _{CC} = 1.1 V to 1.3 V	4.7	14.3	4.7	14.4	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	10.7	3.0	11.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	11.5	2.6	11.6	ns
		V_{CC} = 2.3 V to 2.7 V	2.3	9.0	2.3	10.2	ns
		V _{CC} = 3.0 V to 3.6 V	2.2	10.8	2.2	12.0	ns

Nexperia

 $\begin{array}{ll} [1] & t_{pd} \text{ is the same as } t_{PLH} \text{ and } t_{PHL}. \\ [2] & t_{en} \text{ is the same as } t_{PZH} \text{ and } t_{PZL}. \\ [3] & t_{dis} \text{ is the same as } t_{PHZ} \text{ and } t_{PLZ}. \end{array}$

Low-power buffer/line driver; 3-state

11.1 Waveforms and test circuit

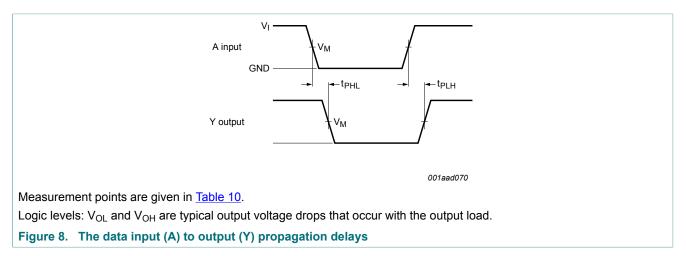
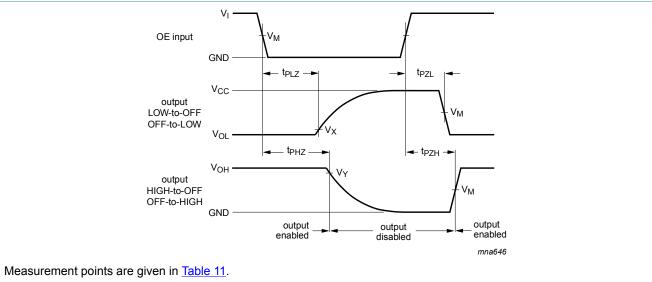


Table 10. Measurement points

Supply voltage	Output	Input					
V _{cc}	V _M	V _M	VI	t _r = t _f			
0.8 V to 3.6 V	0.5 x V _{CC}	0.5 x V _{CC}	V _{CC}	≤ 3.0 ns			



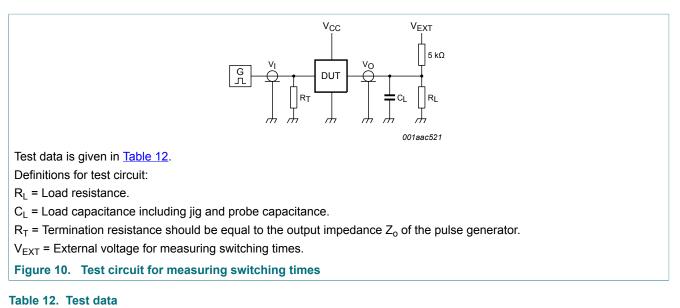
Logic levels: V_{OL} and V_{OH} are typical output voltage drops that occur with the output load.

Figure 9. Enable and disable times

Supply voltage	Input	Output		
V _{cc}	V _M	V _M	V _X	V _Y
0.8 V to 1.6 V	0.5 x V _{CC}	0.5 x V _{CC}	V _{OL} + 0.1 V	V _{OH} - 0.1 V
1.65 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{OL} + 0.15 V	V _{OH} - 0.15 V
3.0 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{OL} + 0.3 V	V _{OH} - 0.3 V

All information provided in this document is subject to legal disclaimers.

Low-power buffer/line driver; 3-state



Supply voltage	Load	V _{EXT}			
V _{cc}	CL	R _L ^[1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2 \times V_{CC}$

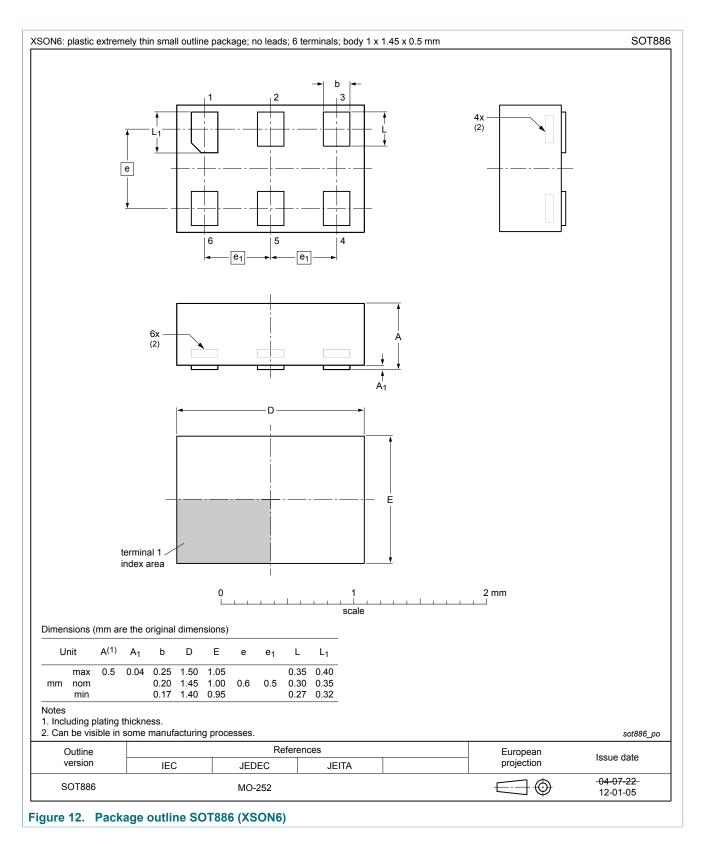
[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

Low-power buffer/line driver; 3-state

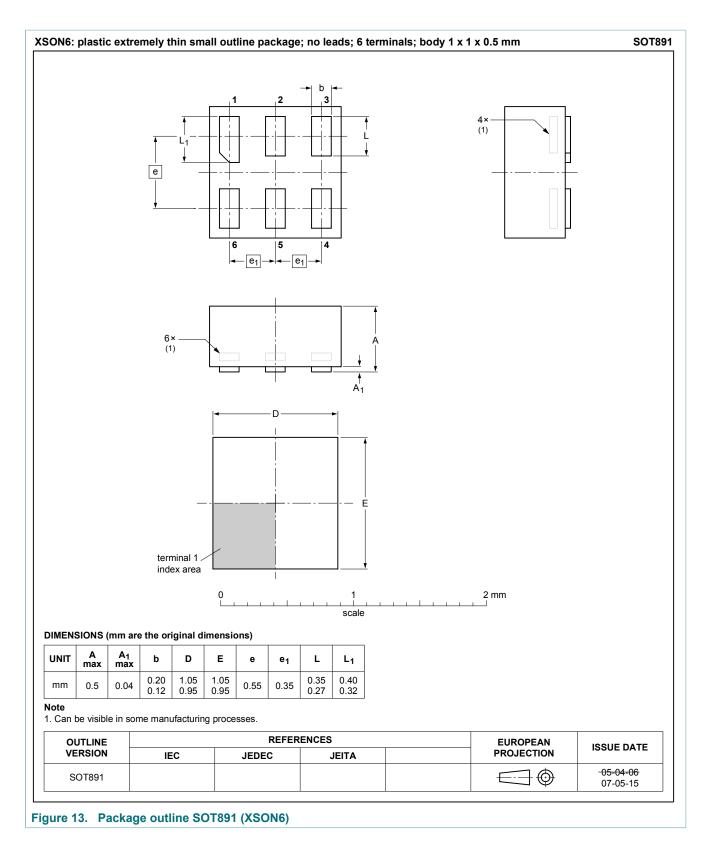
12 Package outline

	: plas	tic th	in shr	ink sr	nall o	utline	packa	age; 5	i leads	s; boc	ly wid	th 1.2	5 mm	l			SC	DT353
		Ĩ	y						с	↓ ↓				X	(M) A			
		-		- Z	[] 	4 3 ← ⊕ w v	(A ₁ ↓	detail	L _p L _p	(A ₃) ↓ ↓	Α 			
		m ara	the crite	inal dia			1.5 sca		1	3 mm _⊣								
DIMENS	IONS (n A max	ım are A ₁	the orig	inal din	L	s)			e	3 mm	HE	L	Lp	v	w	У	Z ⁽¹⁾	θ
					nension bp 0.30	c 0.25	sca D(1) 2.25	E(1)	e 0.65		2.25	L 0.425	0.46	v 0.3	w 0.1	y 0.1	0.60	θ 7° 0°
UNIT mm lote	A max. 1.1	A₁ 0.1 0	A₂ 1.0 0.8	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08	sca D(1) 2.25 1.85	E(1) 1.35 1.15	0.65	e1								7°
UNIT mm lote . Plastic	A max. 1.1 c or meta	A₁ 0.1 0	A₂ 1.0 0.8	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08 num per s	sca D(1) 2.25 1.85 side are	E(1) 1.35 1.15	0.65 Iuded.	e1	2.25		0.46 0.21	0.3 EUROI	0.1 PEAN	0.1	0.60 0.15	7° 0°
UNIT mm lote . Plastic VE	A max. 1.1	A₁ 0.1 0	A 2 1.0 0.8 sions of	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08 num per s	sca D(1) 2.25 1.85 side are REFEF	E(1) 1.35 1.15 e not inc RENCES	0.65 Iuded.	e 1 1.3	2.25		0.46 0.21	0.3	0.1 PEAN	0.1	0.60	7° 0°

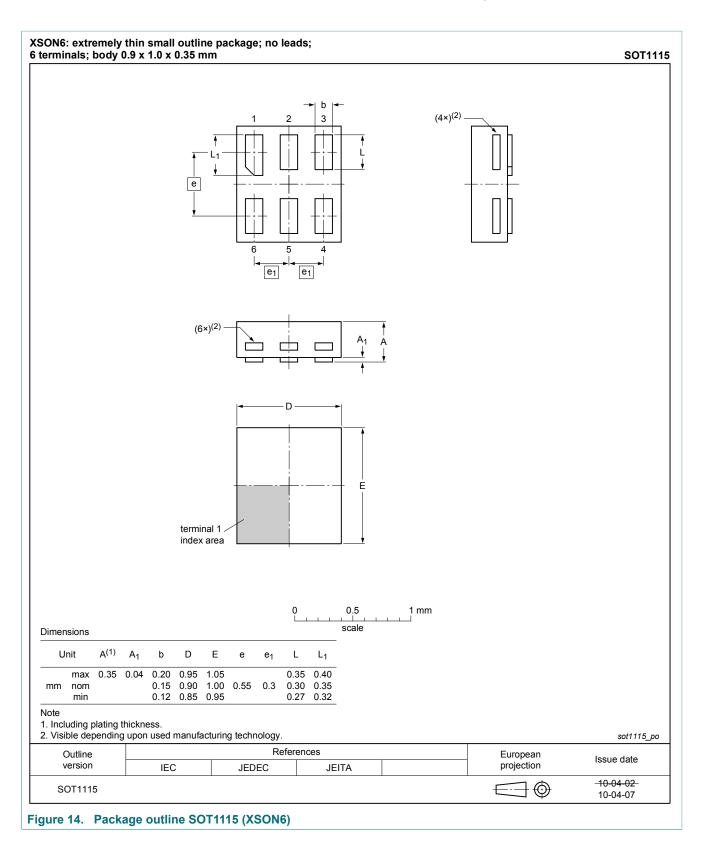
Low-power buffer/line driver; 3-state



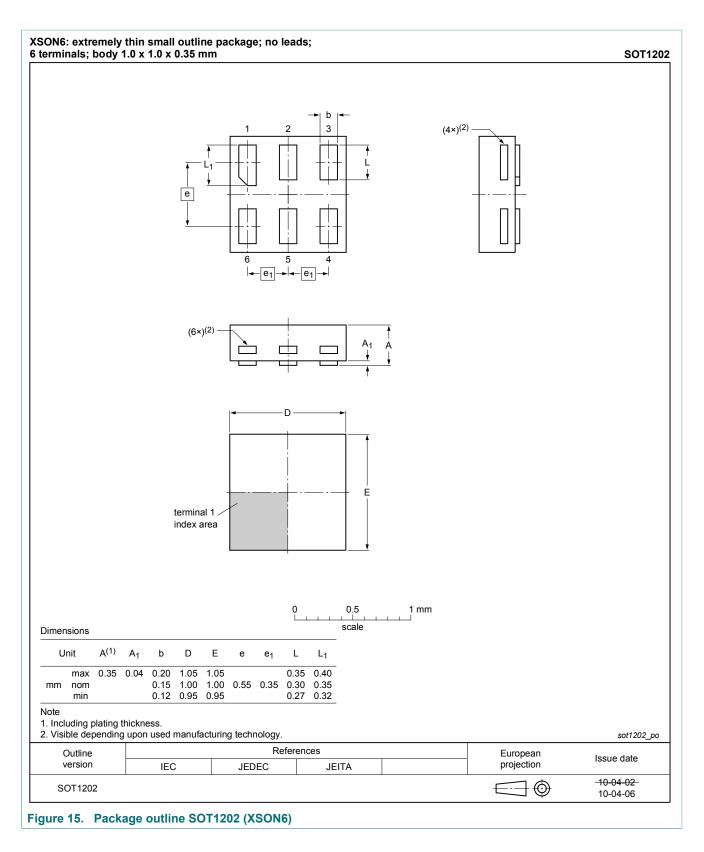
Low-power buffer/line driver; 3-state



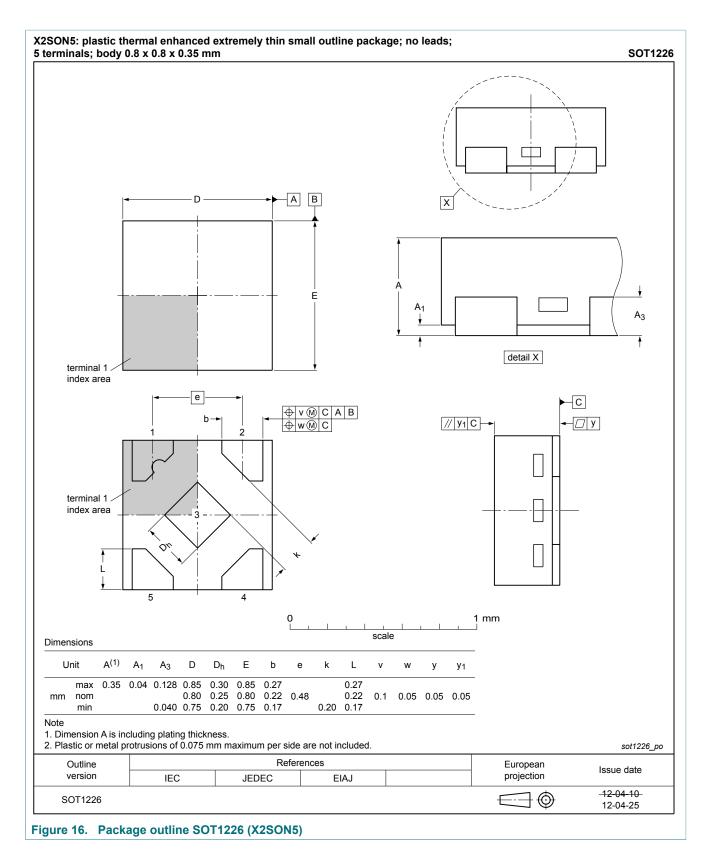
Low-power buffer/line driver; 3-state



Low-power buffer/line driver; 3-state



Low-power buffer/line driver; 3-state



Low-power buffer/line driver; 3-state

13 Abbreviations

Table 13. Abbrevia	ations
Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14 Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes					
74AUP1G126 v.7	20180516	Product data sheet	-	74AUP1G126 v.6					
Modifications:	Nexperia.	data sheet has been redesigned een adapted to the new compar							
74AUP1G126 v.6	20151002	Product data sheet	-	74AUP1G126 v.5					
Modifications:	 I_{OK} minimum changed from -0.5 mA to -50 mA (errata) in <u>Table 5</u> 								
74AUP1G126 v.5	20120628	Product data sheet	-	74AUP1G126 v.4					
Modifications:		er 74AUP1G126GX (SOT1226) rawing of SOT886 (Figure 12) n							
74AUP1G126 v.4	20111124	Product data sheet	-	74AUP1G126 v.3					
74AUP1G126 v.3	20100903	Product data sheet	-	74AUP1G126 v.2					
74AUP1G126 v.2	20060628	Product data sheet	-	74AUP1G126 v.1					
74AUP1G126 v.1	20050725	Product data sheet	-	-					

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

[2] [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability - Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia. In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia

Right to make changes - Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use - Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer

No offer to sell or license - Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Low-power buffer/line driver; 3-state

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications. In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer

design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

74AUP1G126

Low-power buffer/line driver; 3-state

Contents

1	General description	1
2	Features and benefits	1
3	Ordering information	2
4	Marking	
5	Functional diagram	2
6	Pinning information	
6.1	Pinning	
6.2	Pin description	3
7	Functional description	4
8	Limiting values	4
9	Recommended operating conditions	4
10	Static characteristics	5
11	Dynamic characteristics	9
11.1	Waveforms and test circuit	16
12	Package outline	
13	Abbreviations	24
14	Revision history	24
15	Legal information	

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© Nexperia B.V. 2018.

All rights reserved.

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 16 May 2018 Document identifier: 74AUP1G126



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.