AC/DC Drivers

PWM type DC/DC converter IC Included 650V MOSFET

BM2P0391

●General

The PWM type DC/DC converter BM2P0391 for AC/DC provides an optimum system for all products that include an electrical outlet.
BM2P0391 supports both isolated and non-isolated devices, enabling simpler design of various types of low-power electrical converters.
BM2P0391 built in a HV starter circuit that tolerates 650V, it contributes to low-power consumption.
With current detection resistors as external devices, a higher degree of design freedom is achieved.
Switching frequency adopts fixed system. Since current mode control is utilized, current is restricted in each cycle and excellent performance is demonstrated in bandwidth and transient response.
The switching frequency is 100 kHz. At light load, the switching frequency is reduced and high efficiency is achieved.
A frequency hopping function is also on chip, which contributes to low EMI.

●Basic Specifications

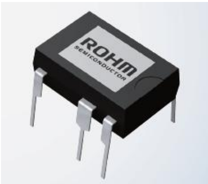
■Operating Power Supply Voltage Range :	VCC: 8.9V to 26.0V
	DRAIN: to 650V
■Operating Current :	Normal Mode 1.000mA (Typ)
	Burst Mode 0.400mA (Typ)
■Oscillation Frequency :	100kHz (Typ)
■Operating Temperature :	-40 °C to +105 °C
■MOSFET ON Resistance :	2.4Ω (Typ)

●Features

- PWM frequency : 100kHz
- PWM current mode method
- Frequency hopping function
- Burst operation when load is light
- Frequency reduction function
- Built-in 650V start circuit
- Built-in 650V switching MOSFET
- VCC pin under voltage protection
- VCC pin overvoltage protection
- SOURCE pin Open protection
- SOURCE pin Short protection
- SOURCE pin Leading-Edge-Blanking function
- Per-cycle over current protection circuit
- Soft start
- Secondary Over current protection circuit
- BR pin AC input low voltage protection

●Package

DIP7K: 9.20mm×6.35mm×4.30mm pitch 2.54mm
(Typ) (Typ) (Max) (Typ)



●Applications

AC adapters, TV and household appliances (vacuum cleaners, humidifiers, air cleaners, air conditioners, IH cooking heaters, rice cookers, etc.)

●Application circuit

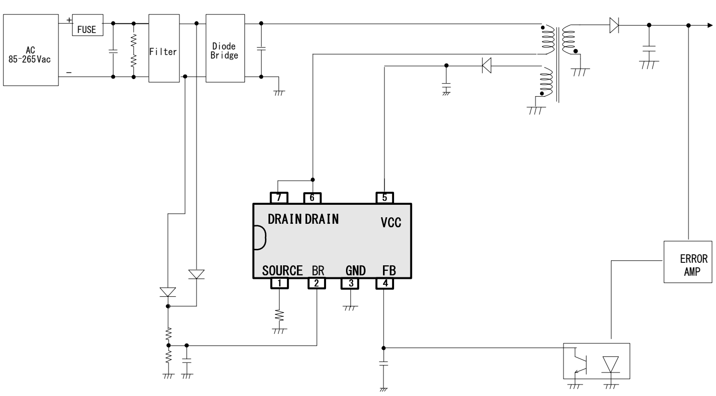


Figure 1. Application circuit

○Product structure : Silicon monolithic integrated circuit ○This product has no designed protection against radioactive rays

●Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit	Conditions
Maximum applied voltage 1	Vmax1	-0.3 to 30	V	VCC
Maximum applied voltage 2	Vmax2	-0.3 to 6.5	V	SOURCE, FB, BR
Maximum applied voltage 3	Vmax3	650	V	DRAIN
Drain current pulse	I_{DP}	5.20	A	P_W=10usec, Duty cycle=1%
Allowable dissipation	Pd	2.00	W	
Operating temperature range	Topr	-40 to +105	°C	
Maximum junction temperature	Tjmax	150	°C	
Storage temperature range	Tstr	-55 to +150	°C	

(Note) DIP7 : When mounted (on 74.2 mm × 74.2 mm,×1.6 mm thick, glass epoxy on double-layer substrate).
Reduce to 16 mW/°C when Ta = 25°C or above.

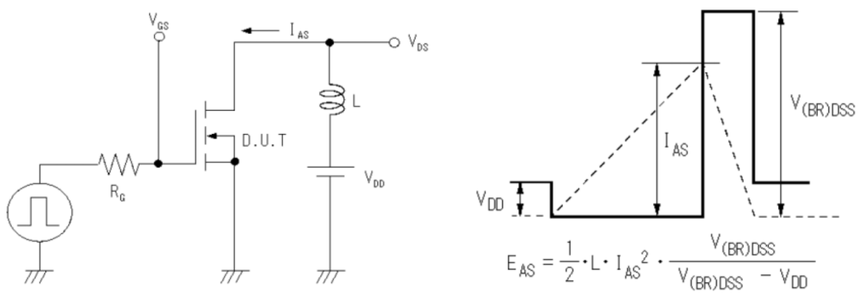
●Operating Conditions (Ta=25°C)

Parameter	Symbol	Rating	Unit	Conditions
Power supply voltage range 1	V_{CC}	8.9 to 26.0	V	VCC pin voltage
Power supply voltage range 2	V_{DRAIN}	to 650	V	DRAIN pin voltage

●Electrical Characteristics of MOSFET part (Unless otherwise noted, Ta=25°C, VCC=15V)

Parameter	Symbol	Specifications			Unit	Conditions
		Minimum	Standard	Maximum		
[MOSFET Block]						
Between drain and source Voltage	$V_{(BR)DDS}$	650	-	-	V	I_D=1mA / V_{GS}=0V
Drain leak current	I_{DSS}	-	-	100	μA	V_{DS}=650V / V_{GS}=0V
On resistance	$R_{DS(ON)}$	-	2.4	3.6	Ω	I_D=0.25A / V_{GS}=10V
Avalanche Energy	E_{AS}	400			μJ	Design assurance

Avalanche Energy circuit



E_{AS}	: Avalanche Energy
I_{AS}	: Avalanche Current
$V_{(BR)DSS}$	: Drain - Source breakdown voltage
V_{GS}	: Gate - Source voltage
V_{DS}	: Drain - Source voltage
V_{DD}	: Power supply voltage
L	: Coil
R_G	: Gate resistance

●Electrical Characteristics (Unless otherwise noted, Ta=25°C, VCC=15V)

Parameter	Symbol	Specifications			Unit	Conditions
		Minimum	Standard	Maximum		
[Circuit current]						
Circuit current (ON) 1	I_{ON1}	650	1000	1350	μA	FB=2.0V (at pulse operation)
Circuit current (ON) 2	I_{ON2}	-	400	500	μA	FB=0.0V (at burst operation)
[VCC protection function]						
VCC UVLO voltage 1	V_{UVLO1}	12.50	13.50	14.50	V	VCC rises
VCC UVLO voltage 2	V_{UVLO2}	7.50	8.20	8.90	V	VCC falls
VCC UVLO hysteresis	V_{UVLO3}	-	5.30	-	V	$V_{UVLO3} = V_{UVLO1} - V_{UVLO2}$
VCC OVP voltage 1	V_{OVP1}	26.0	27.5	29.0	V	VCC rises
VCC OVP voltage 2	V_{OVP2}	-	23.5	-	V	VCC falls
Latch released VCC voltage	V_{LATCH}	-	V_{UVLO2}-0.5	-	V	
VCC recharge start voltage	V_{CHG1}	7.70	8.70	9.70	V	
VCC recharge stop voltage	V_{CHG2}	12.00	13.00	14.00	V	
Latch mask time	T_{LATCH}	50	100	150	μs	
Thermal shut down temperature	TSD	118	145	172	°C	
[PWM type DCDC driver block]						
Oscillation frequency 1	F_{SW1}	90	100	110	kHz	FB=2.0V
Oscillation frequency 2	F_{SW2}	20	25	30	kHz	FB=0.4V
Frequency hopping width 1	F_{DEL1}	-	6.0	-	kHz	FB=2.0V
Hopping fluctuation frequency	F_{CH}	75	125	175	Hz	
Minimum pulse width	Tmin	-	650	1000	ns	
Soft start time 1	T_{SS1}	0.30	0.50	0.70	ms	
Soft start time 2	T_{SS2}	0.60	1.00	1.40	ms	
Soft start time 3	T_{SS3}	1.20	2.00	2.80	ms	
Soft start time 4	T_{SS4}	4.80	8.00	11.20	ms	
Maximum duty	Dmax	68.0	75.0	82.0	%	
FB pin pull-up resistance	R_{FB}	23	30	37	kΩ	
ΔFB / ΔCS gain	Gain	-	4.00	-	V/V	
FB burst voltage	V_{BST}	0.300	0.400	0.500	V	FB falls
FB voltage of starting frequency reduction mode	V_{DLT}	1.100	1.250	1.400	V	
FB OLP voltage 1a	V_{FOLP1A}	2.60	2.80	3.00	V	Overload is detected (FB rise)
FB OLP voltage 1b	V_{FOLP1B}	-	2.60	-	V	Overload is detected (FB drop)
FB OLP ON timer	T_{FOLP1}	40	64	88	ms	
FB OLP Start up timer	T_{FOLP1}	26	32	38	ms	
FB OLP OFF timer	T_{FOLP2}	358	512	666	ms	
[Over current detection block]						
Overcurrent detection voltage	V_{CS}	0.380	0.400	0.420	V	Ton=0us
Overcurrent detection voltage SS1	V_{CS_SS1}	-	0.100	-	V	0 [ms] to T_{SS1} [ms]
Overcurrent detection voltage SS2	V_{CS_SS2}	-	0.150	-	V	T_{SS1} [ms] to T_{SS2} [ms]
Overcurrent detection voltage SS3	V_{CS_SS3}	-	0.200	-	V	T_{SS2} [ms] to T_{SS3} [ms]
Overcurrent detection voltage SS4	V_{CS_SS4}	-	0.300	-	V	T_{SS3} [ms] to T_{SS4} [ms]
Leading Edge Blanking Time	T_{LEB}	-	250	-	ns	
Over current detection AC voltage compensation factor	K_{CS}	12	20	28	mV/μs	
SOURCE pin short protection voltage	V_{CSSHT}	0.020	0.050	0.080	V	
[Start circuit block]						
Start current 1	I_{START1}	0.100	0.500	1.000	mA	VCC=0V
Start current 2	I_{START2}	3.600	6.000	10.500	mA	VCC=10V
OFF current	I_{START3}	-	10	20	uA	Input current of DRAIN pin, when VCC UVLO released. (MOSFET OFF)
Start current switching voltage	V_{SC}	0.800	1.500	2.100	V	
[BR pin function]						
BR UVLO detection voltage1	V_{BR1}	0.45	0.50	0.55	V	BR rises
BR UVLO detection voltage 2	V_{BR2}	0.29	0.35	0.41	V	BR falls
BR UVLO hysteresis	V_{BR3}	-	0.15	-	V	$V_{BR3} = V_{BR1} - V_{BR2}$
BR UVLO detection delay time1	T_{BR1}	50	100	150	μs	BR rises
BR UVLO detection delay time2	T_{BR2}	150	256	350	ms	BR falls

●PIN DESCRIPTIONS

Table 1. Pin Description

NO.	Pin Name	I/O	Function	ESD Diode	
				VCC	GND
1	SOURCE	I/O	MOSFET SOURCE pin	○	○
2	BR	I	Input AC voltage monitor pin	-	○
3	GND	I/O	GND pin	○	-
4	FB	I	Feedback signal input pin	-	○
5	VCC	I	Power supply input pin	-	○
6	DRAIN	I/O	MOSFET DRAIN pin	-	-
7	DRAIN	I/O	MOSFET DRAIN pin	-	-

●I/O Equivalent Circuit Diagram

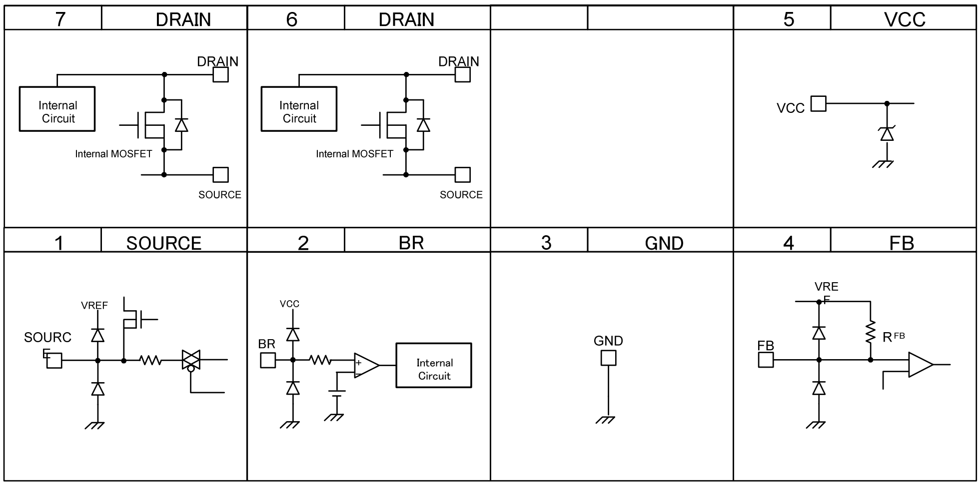


Figure 2. I/O Equivalent Circuit Diagram

●Block Diagram

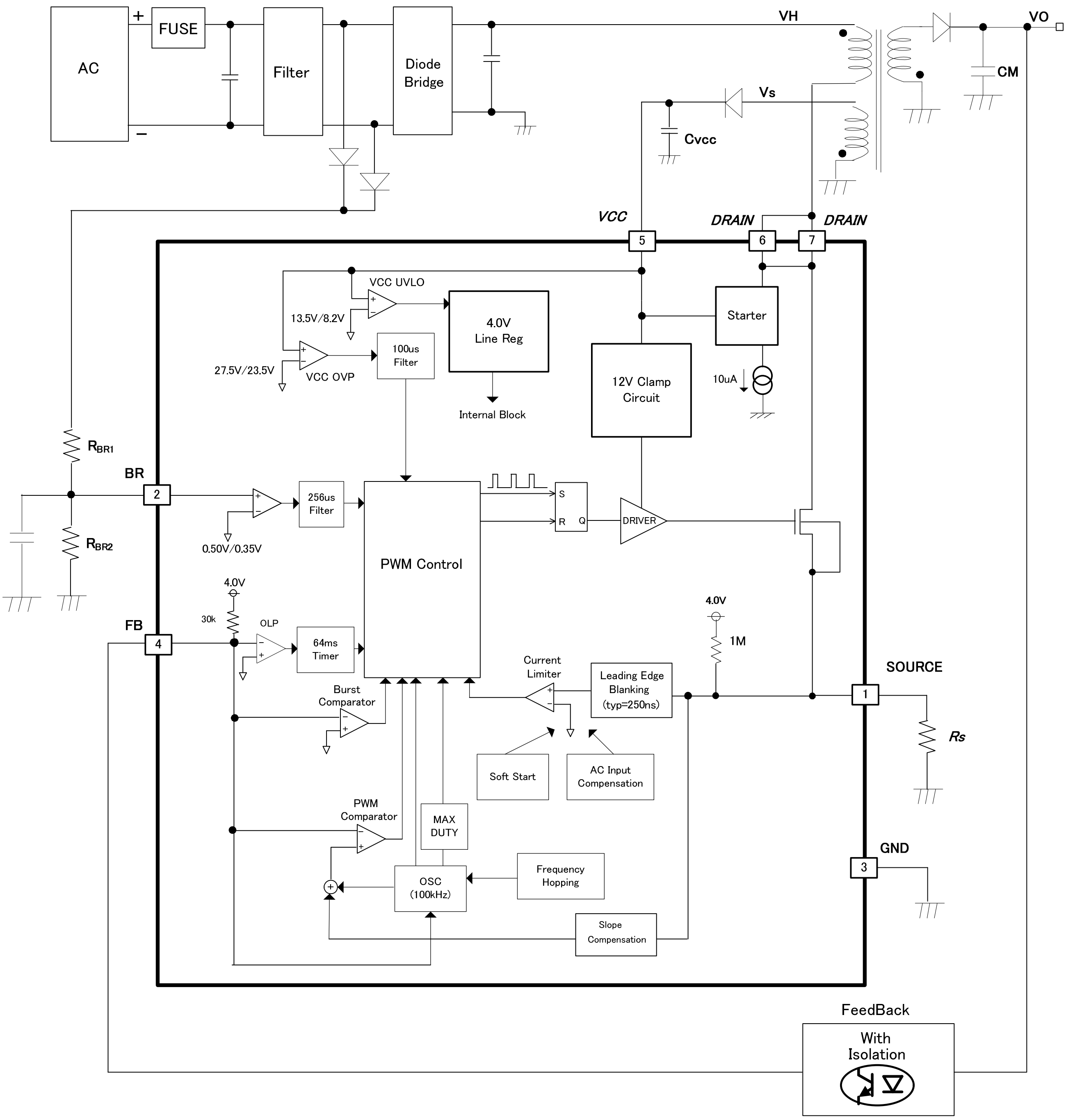


Figure 3. Block Diagram

●Description of Blocks

(1) Start circuit (DRIAN: 6,7pin)

This IC built in Start circuit (tolerates 650V). It enables to be low standby mode electricity and high speed starting.
After starting, consumption power is idling current I_{START3} (Typ=10μA) only.
Reference values of Starting time are shown in Figure 6. When Cvcc=10μF it can start less than 0.1 sec.

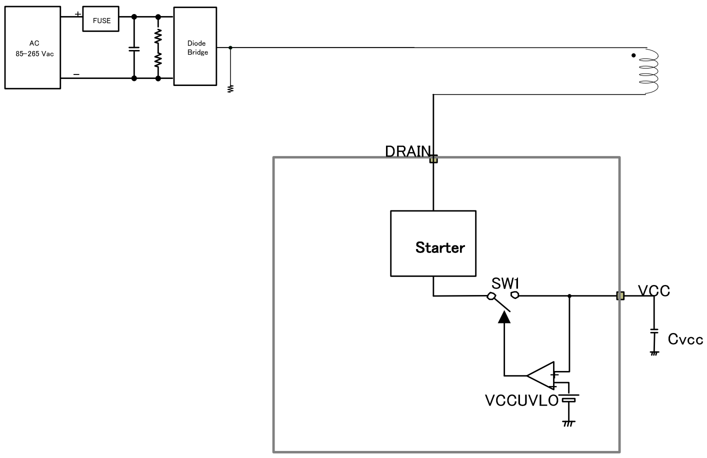


Figure 4. Block diagram of start circuit

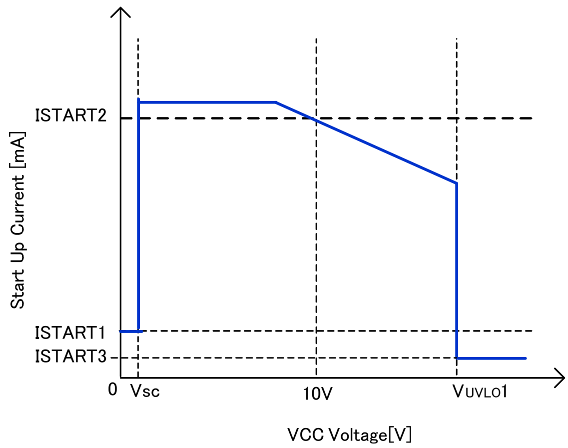


Figure 5. Start current vs VCC voltage

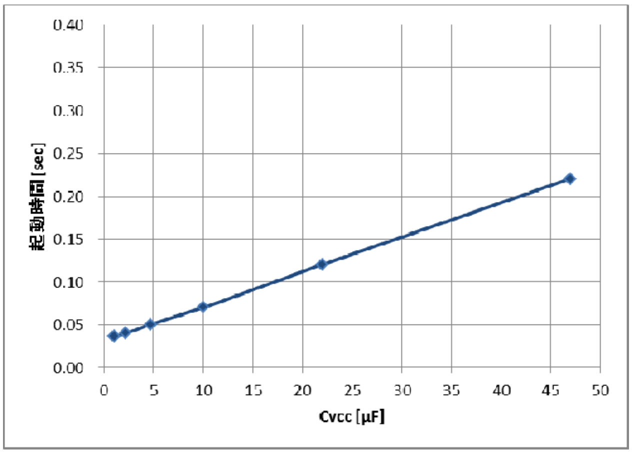


Figure 6. Start time (reference value)

* Start current flows from the DRAIN pin

ex) Consumption power of start circuit only when the Vac=100V
$PVH = 100V * \sqrt{2} * 10uA = 1.41mW$

ex) Consumption power of start circuit only when the Vac=240V
$PVH = 240V * \sqrt{2} * 10uA = 3.38mW$

(2) Start sequences
(Soft start operation, light load operation, and auto recovery operation during overload protection)
Start sequences are shown in Figure 7. See the sections below for detailed descriptions.

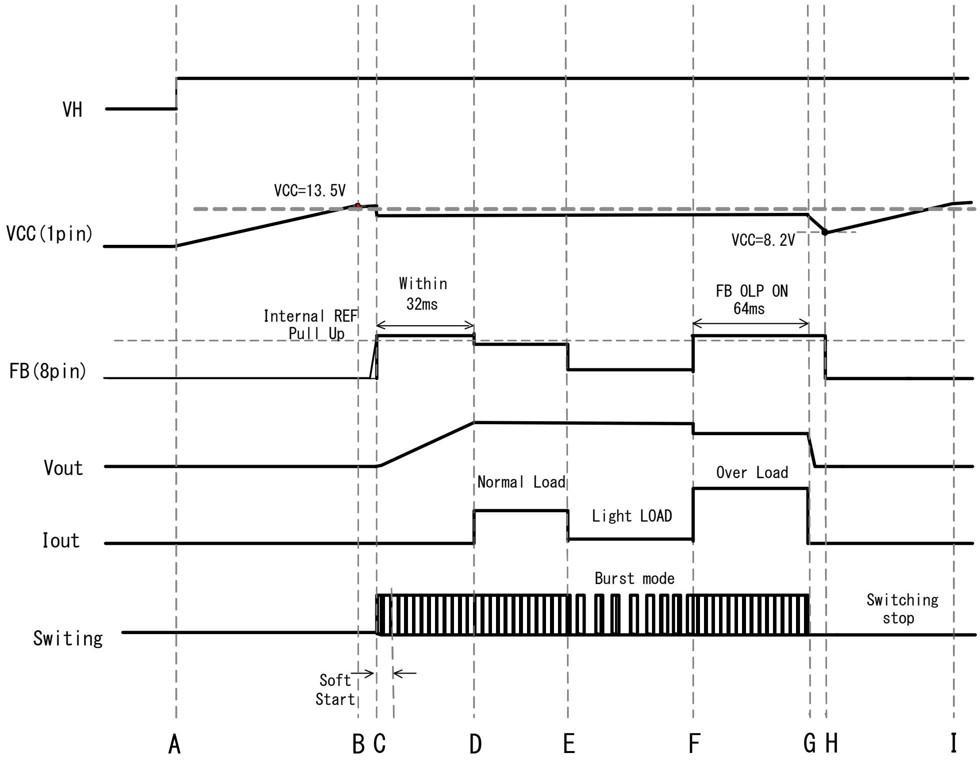


Figure 7. Start sequences Timing Chart

A: Input voltage VH is applied.
B: This IC starts operation when VCC pin voltage rises and VCC > V_{UVLO1} (Typ=13.5V).
Switching function starts when other protection functions are judged as normal.
Then the VCC pin voltage drop because of consumption current of VCC pin. In the case of VCC < V_{CHG1} (Typ=8.7V), the VCC recharge circuit operates.
C: With the soft start function, over current limit value is restricted to prevent any excessive rise in voltage or current.
D: When the switching operation starts, and VOUT rises.
When the output voltage becomes to stable state, VCC voltage also becomes to stable state through auxiliary winding. Please set the rated voltage within the T_{FOLP1b} period (32msec typ) from VCC voltage > V_{UVLO1}.
E: During a light load, if it reaches FB voltage < V_{BST} (Typ=0.4V), the IC starts burst operation to keep power consumption low.
During burst operation, it becomes low-power consumption mode.
F: When the FB Voltage > V_{FOLP1A} (Typ=2.8V), it becomes a overload operation.
G: When FB pin voltage keeps V_{FOLP1A} (Typ=2.8V) at or above T_{FOLP} (Typ=64msec), the overload protection function is triggered and switching stops 64msec later. If the FB pin voltage becomes FB < V_{FOLP1B} even once, the IC's FB OLP timer is reset.
H: If the VCC voltage drops to VCC < V_{UVLO2} (Typ=7.7V) or below, restart is executed.
I: The IC's circuit current is reduced and the VCC pin value rises. (Same as B)

(3) VCC pin protection function

BM2P0391 built in VCC low voltage protection function VCCUVLO (Under Voltage Lock Out), over voltage protection function VCC OVP (Over Voltage Protection) and VCC charge function that operates in case of dropping the VCC voltage. VCC UVLO and VCC OVP monitor VCC pin and prevent VCC pin from destroying switching MOSFET at abnormal voltage.
VCC charge function stabilizes the secondary output voltage by charging from the high voltage line by start circuit at dropping the VCC voltage.

(3-1) VCC UVLO ／ VCC OVP function

VCC UVLO and VCC OVP are auto recovery protections. And they have voltage hysteresis. Refer to the operation Figure 8. Switching is stopped by the VCCOVP function when VCC pin voltage > V_{ovp1} (Typ=27.5V), and switching is restart when VCC pin voltage < V_{ovp2} (Typ=23.5V)

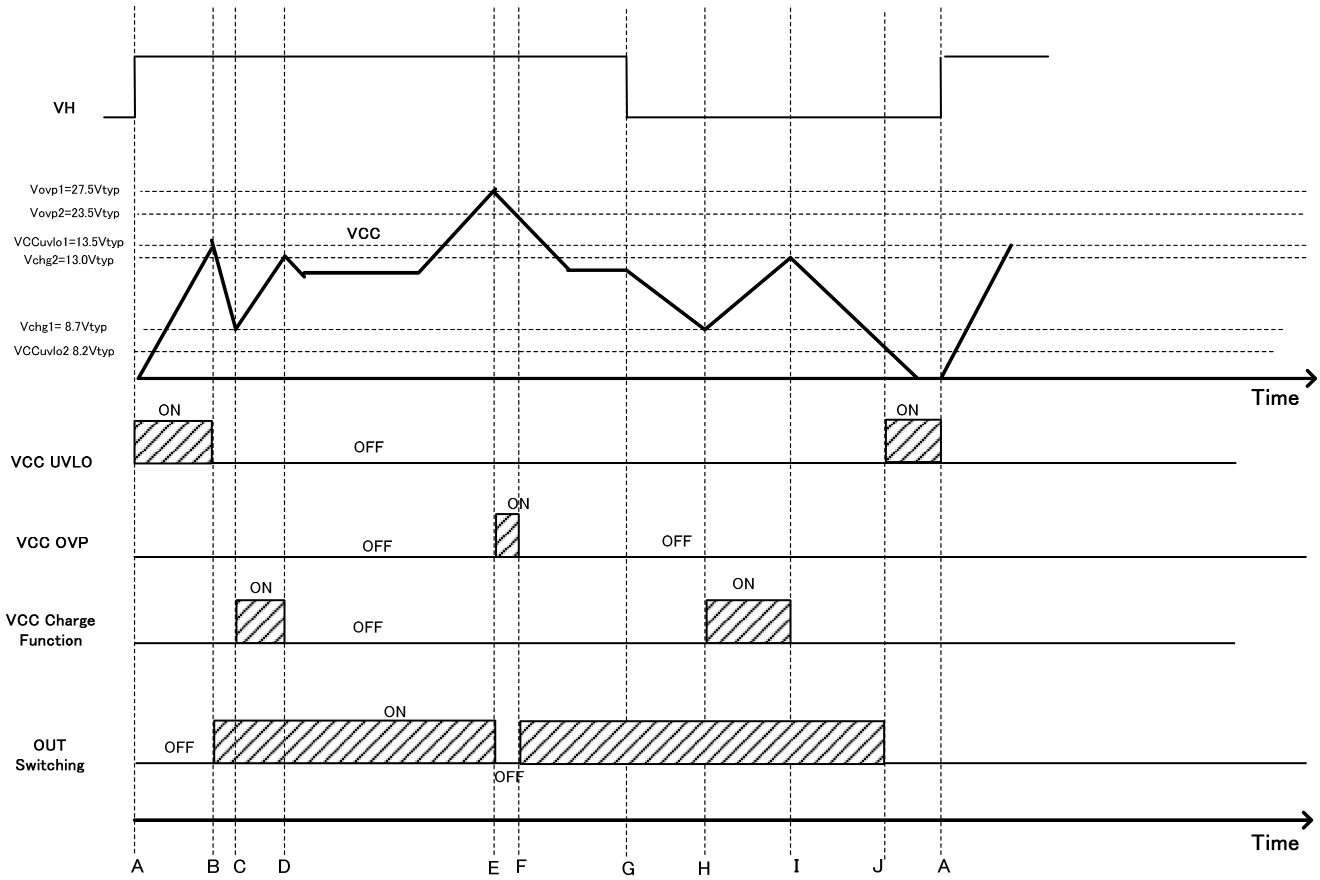


Figure 8. VCC UVLO / OVP Timing Chart

A: When voltage is applied to the DRAIN pin, VCC pin voltage starts rising.
B: When the VCC pin is more than V_{UVLO}, the VCC UVLO function is released and DC/DC operation starts
C: When the VCC pin is less than V_{CHG1}, VCC charge function operates and the VCC voltage rises.
D: When the VCC pin is more than $V_{CHG2,}$ VCC charge function is stopped.
E: The condition the VCC pin is more than V_{OVP1} continues for T_{LATCH} (Typ=100usec), the switching operation is stopped by the VCCOVP function.
F: When the VCC pin less than V_{OVP2}, the switching operation restarts.
G: The high voltage line VH drops.
H: Same as C.
I: Same as D.
J: When the VCC pin is less than V_{UVLO2}, the switching operation is stopped by the VCC UVLO function.

(3-2) VCC Charge function

If the VCC pin drops to V_{CHC1} after once the VCC pin becomes more than V_{UVLO1} and the IC starts to operate, the VCC charge function operates. At that time, the VCC pin is charged from DRAIN pin through start circuit. By this operation, BM2P0391 doesn't occur to start failure. When the VCC pin voltage raises to V_{CHG2} or above, charge is stopped.
The operations are shown in Figure 9.

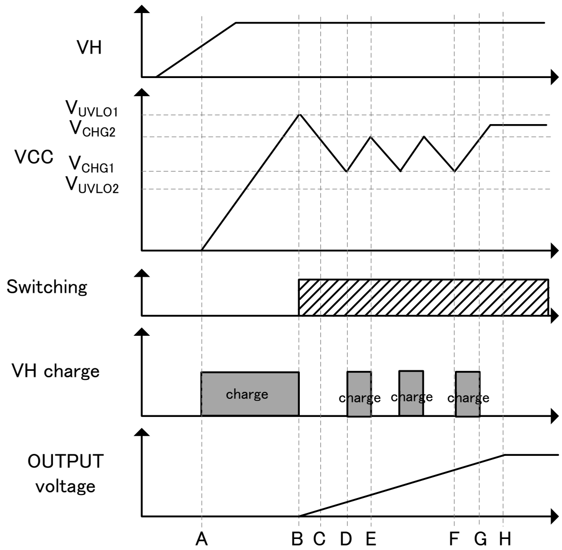


Figure 9. Charge operation VCC pin charge operation

A: DRAIN pin voltage raises and the VCC pin starts to be charged by the VCC charge function.
B: When the VCC pin is more than V_{UVLO1}, the VCC UVLO function releases and VCC charge function stops.
Then the DC/DC operation starts.
C: When DC/DC operation starts, the VCC voltage drops because the output voltage is low.
D: When the VCC pin is less than V_{CHG1}, the VCC recharge function operates and VCC pin voltage rises.
E: When the VCC pin is more than V_{CHG2}, VCC recharge function stops.
F: When the VCC pin is less than V_{CHG1}, VCC recharge function operates and VCC pin voltage rises.
G: When the VCC pin is more than V_{CHG2}, VCC recharge function stops.
H: After starting of the output voltage finished, VCC is charged by the auxiliary winding and VCC pin stabilizes.

(4) DCDC driver (PWM comparator, frequency hopping, slope compensation, OSC, burst)

BM2P0391 performs current mode PWM control. An internal oscillator sets a fixed switching frequency (100 kHz Typ). BM2P0391 is integrated switching frequency hopping function which changes the switching frequency to fluctuate as shown in Figure 10 below.
The fluctuation cycle is 125 Hz typ.

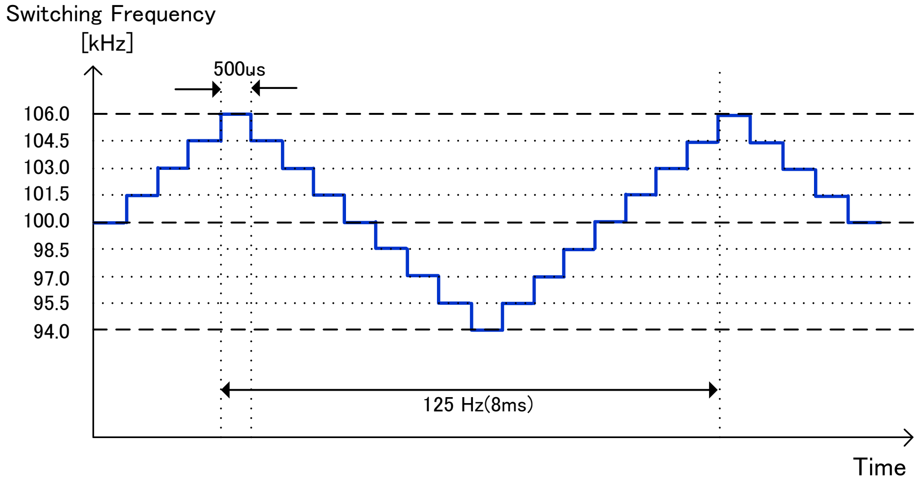


Figure 10. Frequency hopping function

Max duty cycle is fixed as 75% (Typ) and MIN pulse width is fixed as 650 nsec (Typ).
With current mode control, when the duty cycle exceeds 50%, sub harmonic oscillation may occur.
As a countermeasure to this, BM2P0391 is built in slope compensation circuits.

BM2P0391 is built in burst mode circuit and frequency reduction circuit to achieve lower power consumption.
FB pin is pulled up by R_{FB} (30k Ω Typ).FB pin voltage is changed by secondary output voltage (secondary load power).
FB pin is monitored, burst mode operation and frequency detection start.
Figure 11 shows the FB voltage, and switching frequency, DCDC operation.

- mode1 : Burst operation
- mode2 : Frequency reduction operation
- mode3 : Fixed frequency operation (operate at the max frequency)
- mode4 : Over load operation (detect the over load state and stop the pulse operation)

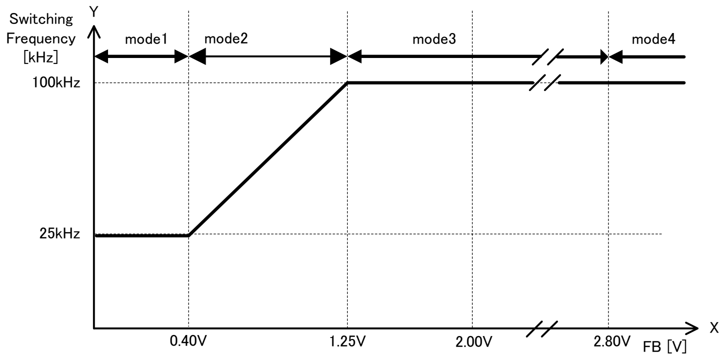


Figure 11. Switching operation state changes by FB pin voltage

(5) Over current limiter

BM2P0391 is built in over current limiter per cycle. If the SOURCE pin exceeds a certain voltage, switching is stopped. It is also built in AC voltage compensation function. This is the function which compensates the maximum power as the AC voltage's change by increasing over current limiter with time.
Shown in Figure 12, 13 and14.

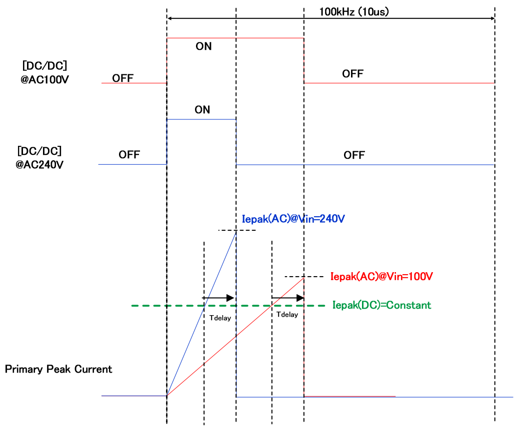


Figure 12. No AC voltage compensation function

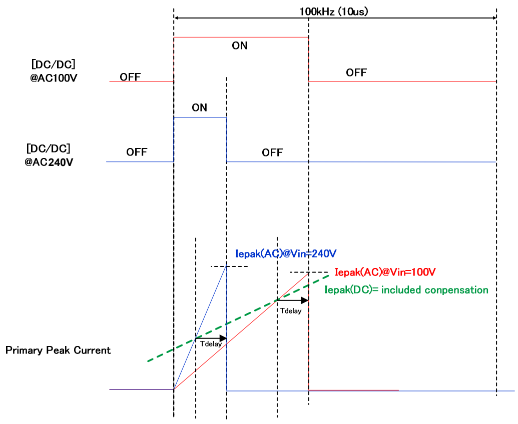


Figure 13. Built-in AC compensation voltage

Primary peak current is decided as the formula below.

Primary peak current: $I_{peak} = V_{cs}/R_s + V_{dc}/L_p * T_{delay}$

V_{cs}: Over current limiter voltage internal IC, Rs: Current detection resistance, V_{dc}: Input DC voltage,
L_p: Primary inductance, T_{delay}: delay time after detection of over current limiter

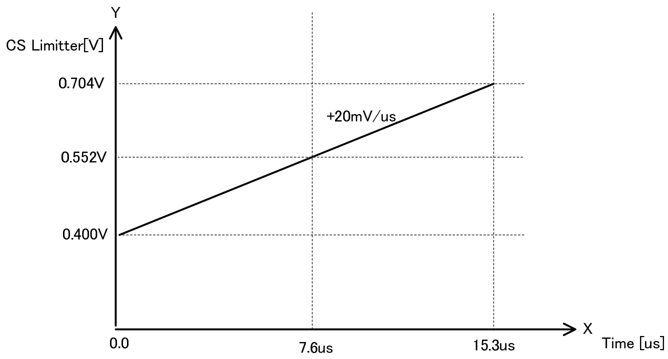


Figure 14. Over current limiter voltage

(6) L.E.B. period

When the driver MOSFET is turned ON, surge current occurs at each capacitor component and drive current. Therefore, because SOURCE pin voltage rises temporarily, the detection errors may occur in the over current limiter circuit. To prevent this detection errors, DRAIN is switched from high to low and the SOURCE signal is masked for 250nsec by the on-chip L.E.B. (Leading Edge Blanking) function.

(7) SOURCE pin short protection function

When the SOURCE pin is shorted, BM2P0391 is over heat.
BM2P0391 built in short protection function to prevent destroying.

(8) SOURCE pin open protection

If the SOURCE pin becomes OPEN, BM2P0391 may be damaged.
To prevent to be damaged, BM2P0391 built in OPEN protection circuit (auto recovery protection).

(9) Output over load protection function (FB OLP Comparator)

The output overload protection function monitors the secondary output load status at the FB pin, and stops switching when an overload occurs.
In case of overload, the output voltage is reduced and current no longer flows to the photo coupler, so the FB pin voltage rises. When the status that FB pin voltage is more than V_{FOLP1A} (Typ=2.8V) continues for the period T_{FOLP1} (Typ=64msec), it is judged as an overload and stops switching. When the FB pin > V_{FOLP1A} (Typ=2.8V), if the voltage goes lower than V_{FOLP1B} (Typ=2.6V) during the period T_{FOLP1} (Typ=64msec), the overload protection timer is reset. The switching operation is performed during this period T_{FOLP1} (Typ=64msec).
At startup, the FB voltage is pulled up to the IC's internal voltage, so operation starts at a voltage of V_{FOLP1A} (Typ=2.8V) or above. Therefore, at startup the FB voltage must be set to go to V_{FOLP1B} (Typ=2.6V) or below during the period T_{FOLP1} (Typ=64msec), and the secondary output voltage's start time must be set within the period T_{FOLP1} (Typ=64msec) following startup of the IC.
Recovery from the once detection of FBOLP, after the period T_{FOLP2} (Typ=512msec).

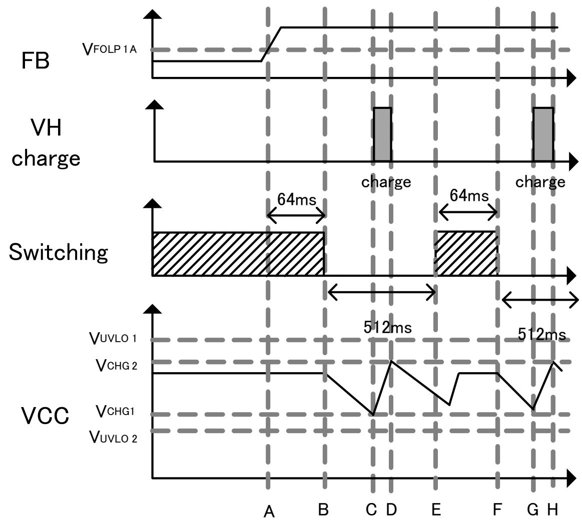


Figure 15. Over load protection (Auto recovery)

A: The FBOLP comparator detects over load because the FB pin is more than V_{FOLP1A}.
B: If the State of A continues for the period T_{FOLP1} (Typ=64msec), switching is stopped after T_{FOLP1} (Typ=64msec) from FB OLP detection.
C: While switching stops by the over load protection function, if the VCC pin voltage drops and VCC pin voltage reaches V_{CHG1} or above, the VCC charge function operates so the VCC pin voltage rises.
D: VCC charge function stops when the VCC pin voltage becomes more than V_{CHG2}.
E: If T_{FOLP2} (Typ=512msec) go on from B point, the switching function starts on soft start.
F: If T_{FOLP1b} (Typ=64msec) go on from E point to continues an overload condition (FB > V_{FOLP1A}), the switching function stops.
G: While the switching stops, VCC pin voltage drops to V_{CHG1} or below. Then the VCC charge function operates and VCC pin voltage rises.
H: If the VCC pin voltage becomes over V_{CHG2} by the VCC charge function, the VCC charge function operation stops.

(10) Input voltage protection function

This IC has BR-UVLO function to monitor input voltage. By monitoring input voltage, it can be prevented from breaking of IC. AC voltage and DC voltage can be monitored by BR pin.

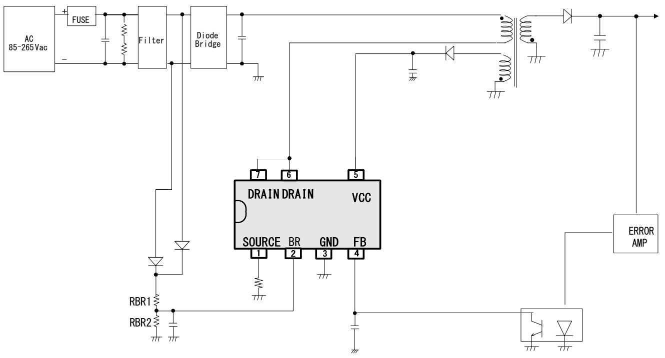


Figure 16(a). AC voltage monitor setting

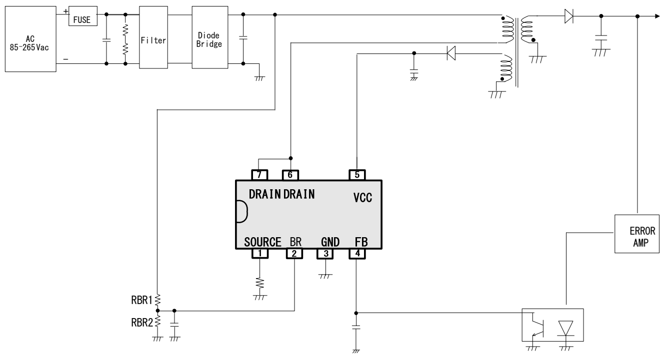


Figure 16(b). DC voltage monitor setting

BR UVLO function can protect the breaking of IC when input voltage is low.

●Operation mode of protection circuit

Operation mode of protection functions are shown in Table 2.

Table 2. Operation mode of protection circuit

Function	Operation mode
VCC Under Voltage Locked Out	Auto recovery
VCC Over Voltage Protection	Auto recovery
TSD	Latch (with 100usec timer)
FB Over Limited Protection	Auto recovery (with 64msec timer)
SOURCE Open Protection	Auto recovery
BR UVLO	Auto recovery (with 256msec timer)

●Sequence

The sequence diagram is show in Figure 17.
In all condition, the operations transit OFF Mode If the VCC voltage becomes less than 8.2V.

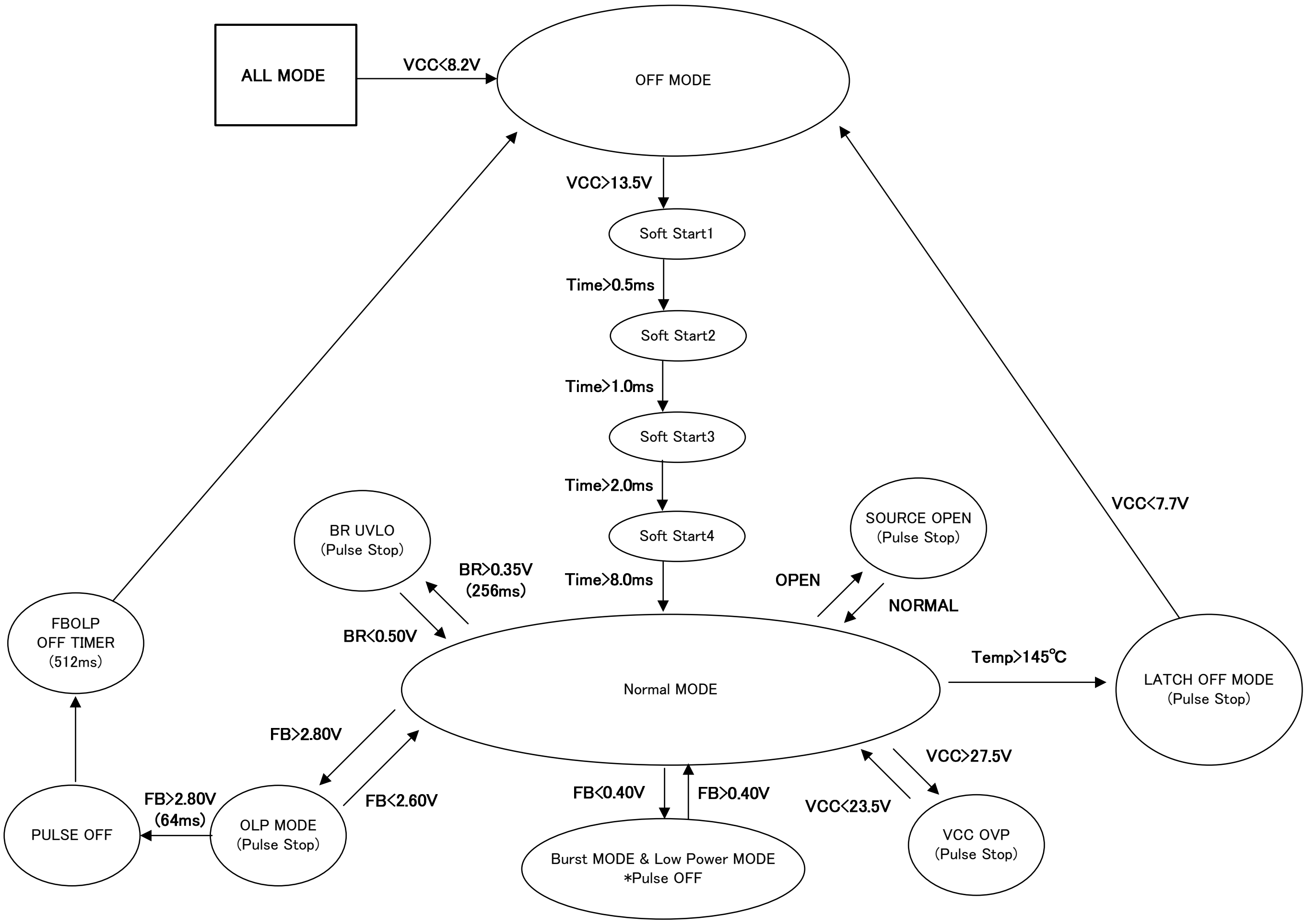


Figure 17. The sequence diagram

●Thermal loss

The thermal design should set operation for the following conditions.
(Since the temperature shown below is the guaranteed temperature, be sure to take a margin into account.)

1. The ambient temperature Ta must be 105 °C or less.
2. The IC's loss must be within the allowable dissipation Pd.

The thermal abatement characteristics are as follows.
(PCB: 74.2 mm × 74.2mm × 1.6 mm, mounted on glass epoxy double-layer substrate.)

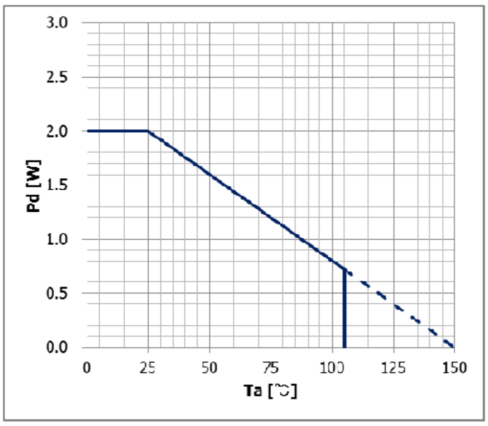


Figure 18. DIP7K Thermal Abatement Characteristics

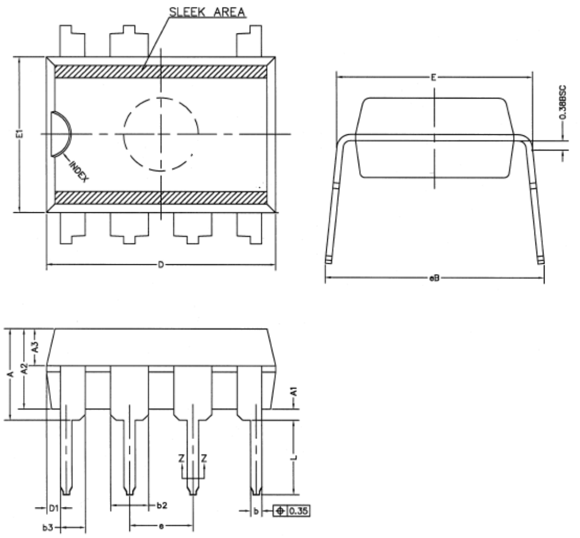
●Ordering Model Name Selection

B M 2 P 0 3 9 1 - Z

Product name

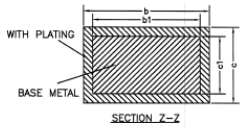
●Physical Dimension Tape and Reel Information

DIP7K



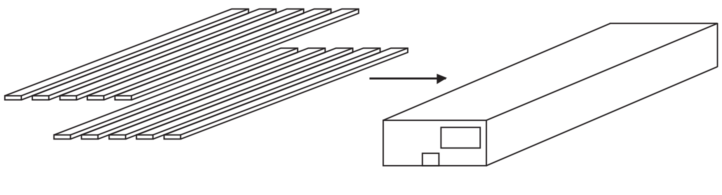
COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	–	–	4.30
A1	0.40	–	–
A2	3.10	3.25	3.50
A3	1.40	1.50	1.60
b	0.42	–	0.55
b1	0.41	0.46	0.51
b2	1.48	1.52	1.61
b3	0.89	0.99	1.09
c	0.21	–	0.35
c1	0.20	0.25	0.30
D	9.10	9.20	9.30
D1	0.40	–	–
E	7.57	7.87	8.17
E1	6.25	6.35	6.45
e	2.44	2.54	2.64
eB	7.90	8.80	10.90
L	2.92	3.30	3.81



<Tape and Reel information>

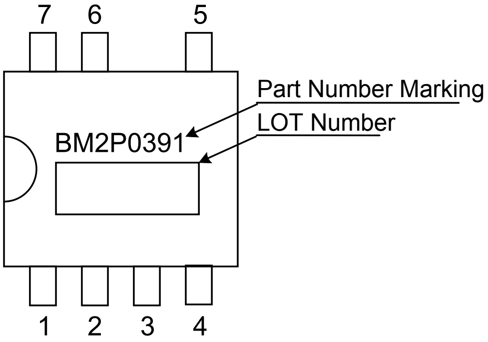
Container	Tube
Quantity	2000pcs
Direction of feed	Direction of products is fixed in a container tube



※Order quantity needs to be multiple of the minimum quantity.

●Making Diagram

DIP7K



Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Thermal Consideration**
   Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the Pd stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

6. **Recommended Operating Conditions**
   These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. **Inter-pin Short and Mounting Errors**
    Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

Operational Notes – continued

11. **Unused Input Pins**
Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

12. **Regarding the Input Pin of the IC**
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

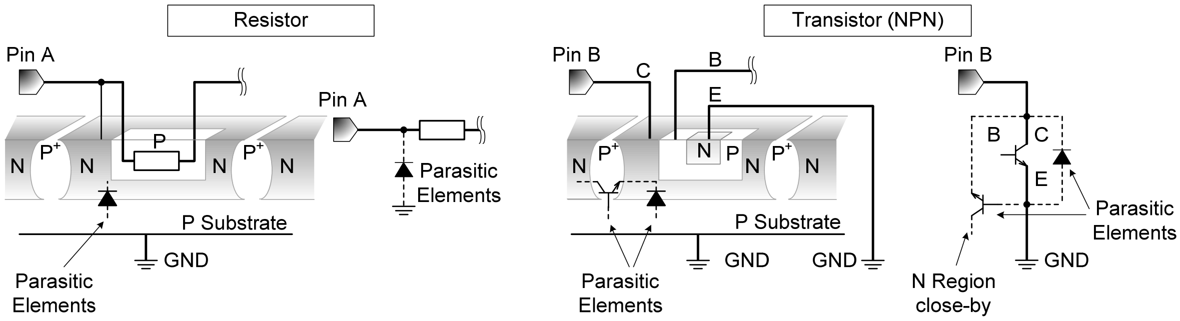


Figure 19. Example of monolithic IC structure

13. **Ceramic Capacitor**
When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

14. **Area of Safe Operation (ASO)**
Operate the IC such that the output voltage, output current, and power dissipation are all within the Area of Safe Operation (ASO).

15. **Thermal Shutdown Circuit(TSD)**
This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. The IC should be powered down and turned ON again to resume normal operation because the TSD circuit keeps the outputs at the OFF state even if the TJ falls below the TSD threshold.
Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

16. **Over Current Protection Circuit (OCP)**
This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.

date	Rev. No.	Revision Point
2016.7.12	001	New Release

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

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   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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- Техническая поддержка проекта;
- Защита от снятия компонента с производства.

Как с нами связаться

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