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KSD1692

Feature

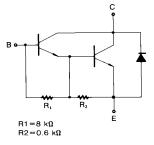
- High Dc Durrent Gain
- Low Collector Saturation Voltage
- Built-in a Damper Diode at E-C
- High Power Dissipation : P_C = 1.3W (Ta=25°C)



NPN Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Sym- bol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	150	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	8	V
I _C	Collector Current (DC)	3	Α
I _{CP}	*Collector Current (Pulse)	5	Α
P _C	Collector Dissipation (T _a =25°C)	1.3	Α
P _C	Collector Dissipation (T _C =25°C)	15	W
T _J	Junction Temperature	150	W
T _{STG}	Storage Temperature	- 55 ~ 150	°C



Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 100V, I_{E} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			2	mA
h _{FE1}	*DC Current Gain	$V_{CE} = 2V, I_{C} = 1.5A$ $V_{CE} = 2V, I_{C} = 3A$	2K 1K		20K	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_C = 1.5A, I_B = 1.5mA$		0.9	1.2	V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	$I_C = 1.5A, I_B = 1.5mA$		1.5	2	V
t _{ON}	Turn ON Time	$V_{CC} = 40V, I_{C} = 1.5A$		0.5		μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 1.5 \text{mA}$		2		μs
t _F	Fall Time	$R_L = 27\Omega$		1		μs

^{*} Pulse test: PW≤350μs, duty Cycle≤2% Pulsed

h_{FE} Classificntion

Classification	0	Y	G
h _{FF1}	2000 ~ 5000	4000 ~ 12000	6000 ~ 20000

^{*} PW≤10ms, duty Cycle≤50%

Typical Characteristics

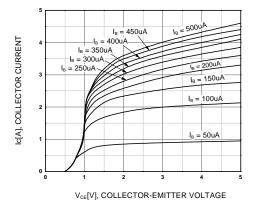


Figure 1. Static Characteristic

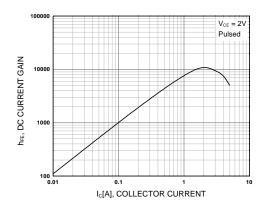


Figure 2. DC current Gain

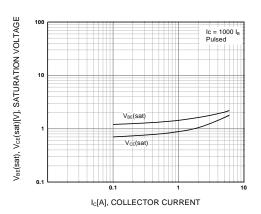


Figure 3. Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage

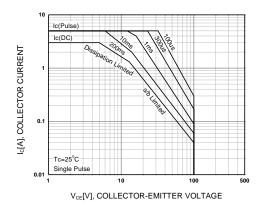


Figure 4. Forward Bias Safe Operating Areas

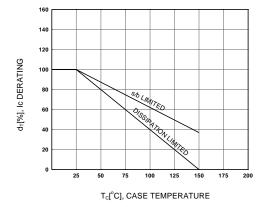


Figure 5. Derating Curve of Safe Operating Areas

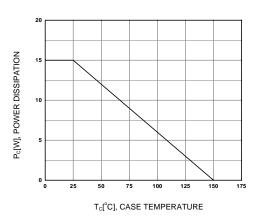


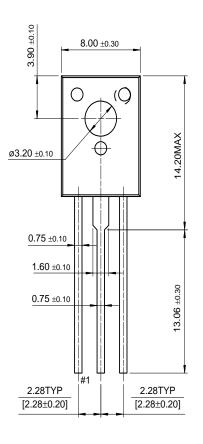
Figure 6. Power Derating

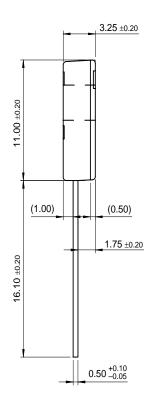
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KSD1692

Package Demensions

TO-126





Dimensions in Millimeters

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