

MOSFETs Silicon N-channel MOS (U-MOSVIII-H)

# TK35A08N1

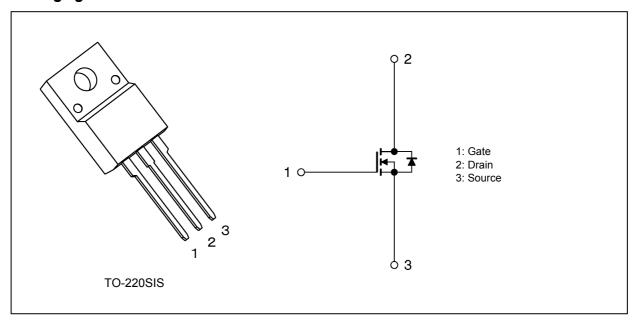
### 1. Applications

• Switching Voltage Regulators

### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 10.0 \text{ m}\Omega$  (typ.) ( $V_{GS} = 10 \text{ V}$ )
- (2) Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 80 \text{ V)}$
- (3) Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 0.3 mA)

### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			$V_{DSS}$	80	V
Gate-source voltage			$V_{GSS}$	±20	
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	I <sub>D</sub>	55	Α
Drain current (DC)	(T <sub>c</sub> = 25°C)	(Note 1)	I <sub>D</sub>	35	
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I <sub>DP</sub>	116	
Power dissipation	(T <sub>c</sub> = 25°C)		$P_D$	30	W
Single-pulse avalanche energy		(Note 3)	E <sub>AS</sub>	38	mJ
Avalanche current			I <sub>AR</sub>	35	Α
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	4.16	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Limited by silicon chip capability.

Note 3:  $V_{DD}$  = 64 V,  $T_{ch}$  = 25°C (initial), L = 24.1  $\mu$ H,  $I_{AR}$  = 35 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



### 6. Electrical Characteristics

## 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	80	_	_	V
Drain-source breakdown voltage (Note 4)	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	60	_	_	
Gate threshold voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.3 mA	2.0	_	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17.5 A	_	10.0	12.2	mΩ

Note 4: If a reverse bias is applied between gate and source, this device enters  $V_{(BR)DSX}$  mode. Note that the drain-source breakdown voltage is lowered in this mode.

## 6.2. Dynamic Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1700	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	18	_	
Output capacitance	C <sub>oss</sub>			440	_	
Gate resistance	r <sub>g</sub>	_	_	2.7	_	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	11	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	28	_	
Switching time (fall time)	t <sub>f</sub>		_	8.8	_	
Switching time (turn-off time)	t <sub>off</sub>		_	35	_	

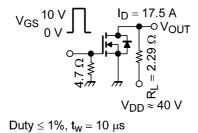


Fig. 6.2.1 Switching Time Test Circuit

## 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	$Q_g$	$V_{DD} \approx 64 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$	-	25		nC
Gate-source charge 1	Q <sub>gs1</sub>			9.8		
Gate-drain charge	$Q_{gd}$			5.8		
Gate switch charge	$Q_SW$		_	10	_	



### 6.4. Source-Drain Characteristics (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I <sub>DR</sub>	_	_	_	35	Α
Reverse drain current (pulsed)	(Note 5)	I <sub>DRP</sub>	_	_	_	116	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 35 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time	(Note 6)	t <sub>rr</sub>	I <sub>DR</sub> = 35 A, V <sub>GS</sub> = 0 V	_	52		ns
Reverse recovery charge	(Note 6)	Q <sub>rr</sub>	-dl <sub>DR</sub> /dt = 100 A/μs	_	82		nC

Note 5: Ensure that the channel temperature does not exceed 150°C.

Note 6: Ensure that V<sub>DS</sub> peak does not exceed V<sub>DSS</sub>.

## 7. Marking (Note)

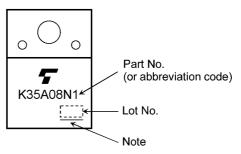


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Characteristics Curves (Note)

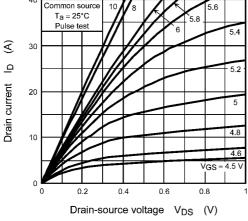
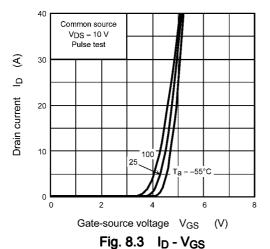


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>



100

Common source T<sub>a</sub> = 25°C Pulse test

Drain current  $I_D$  (A) Fig. 8.5  $R_{DS(ON)} - I_D$ 

100

1000

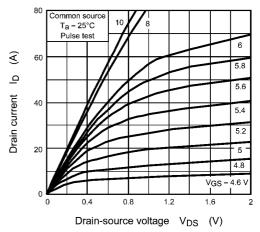


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

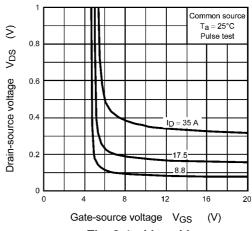


Fig. 8.4 V<sub>DS</sub> - V<sub>GS</sub>

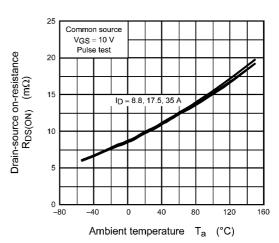


Fig. 8.6 R<sub>DS(ON)</sub> - T<sub>a</sub>

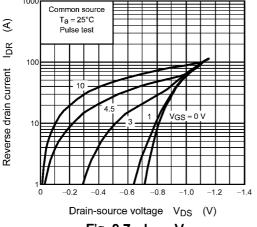


Fig. 8.7 IDR - VDS

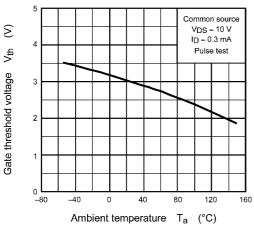


Fig. 8.9 V<sub>th</sub> - T<sub>a</sub>

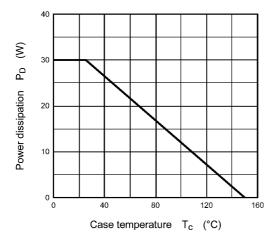


Fig. 8.11 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

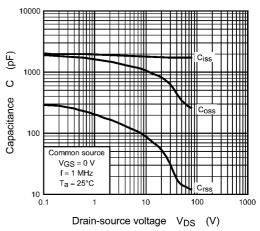


Fig. 8.8 Capacitance - V<sub>DS</sub>

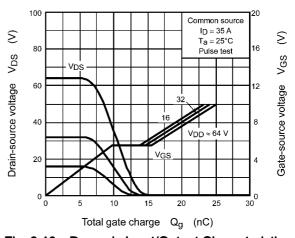


Fig. 8.10 Dynamic Input/Output Characteristics

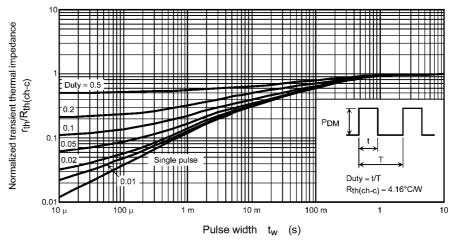


Fig. 8.12  $r_{th}/R_{th(ch-c)} - t_w$  (Guaranteed Maximum)

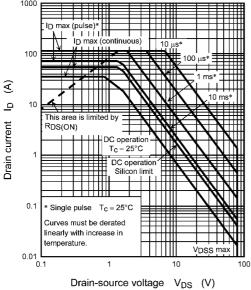


Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

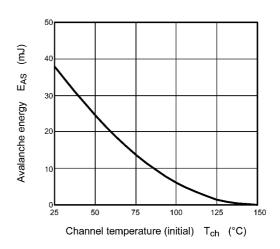


Fig. 8.14 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)

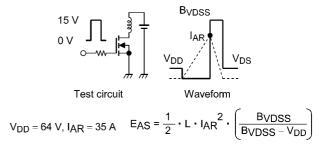


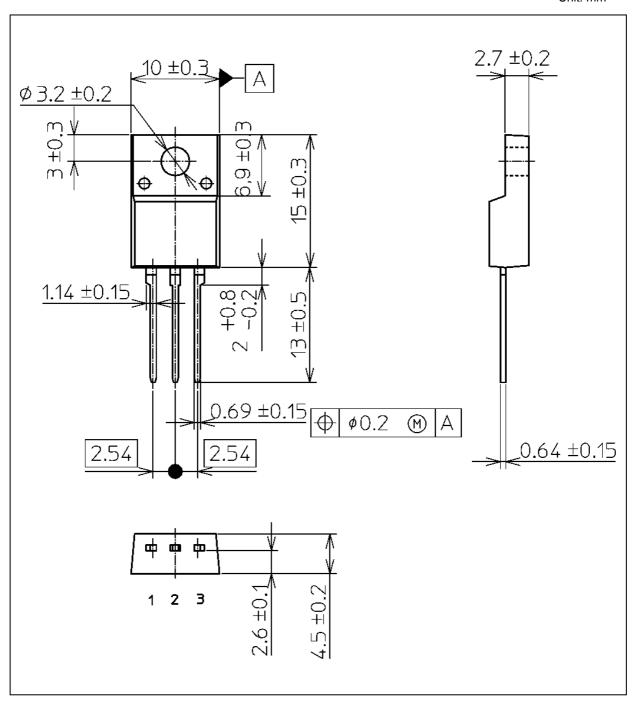
Fig. 8.15 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 1.7 g (typ.)

	Package Name(s)
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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