

Low capacitance ESD Protection Diode

UDZU5.6B

● **Applications**

ESD Protection

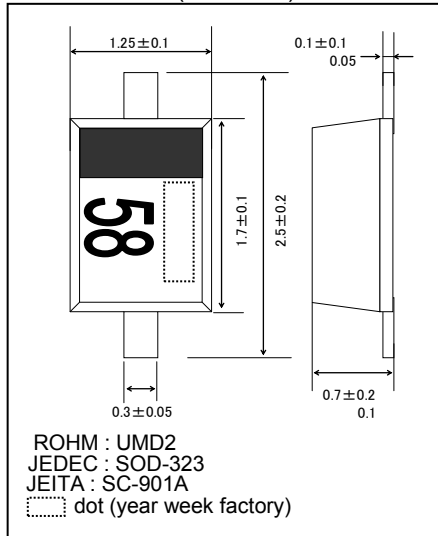
● **Features**

- 1) Small mold type. (UMD2)
- 2) Low Capacitance
- 3) By chip-mounter, automatic mouting is possible.

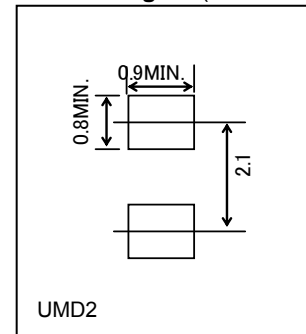
● **Construction**

Silicon epitaxial planer

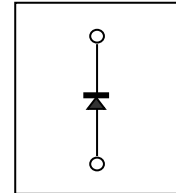
● **Dimensions (Unit : mm)**



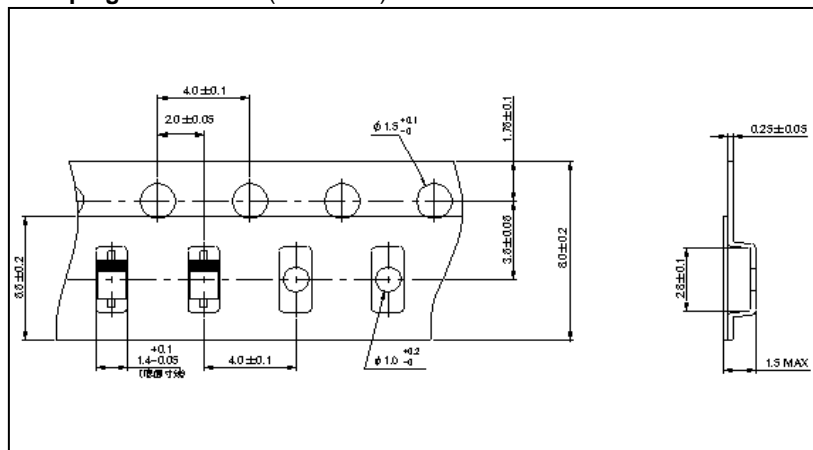
● **Land size figure (Unit : mm)**



● **Structure**



● **Taping dimensions (Unit : mm)**



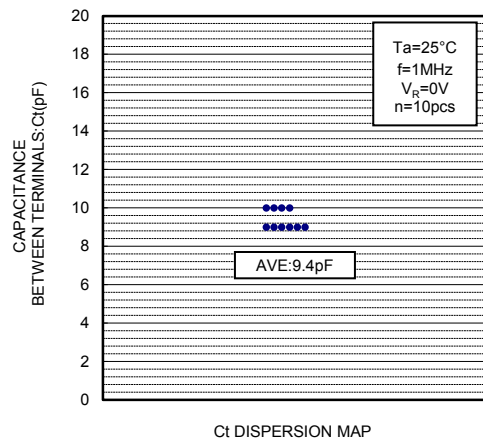
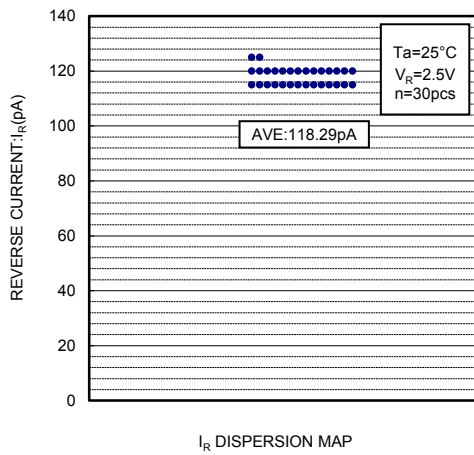
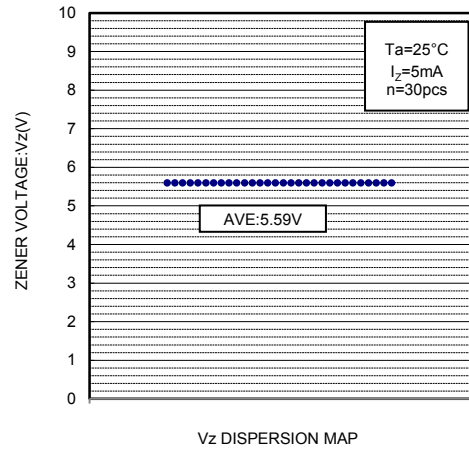
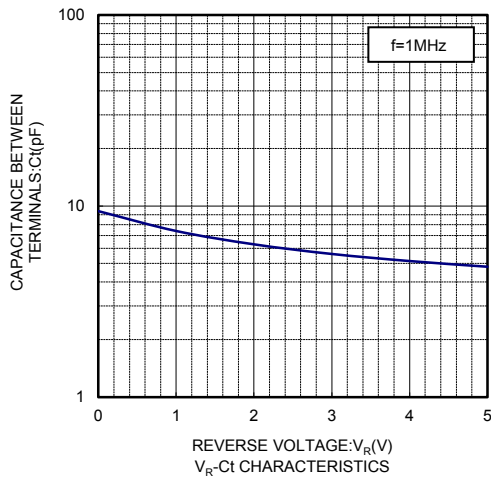
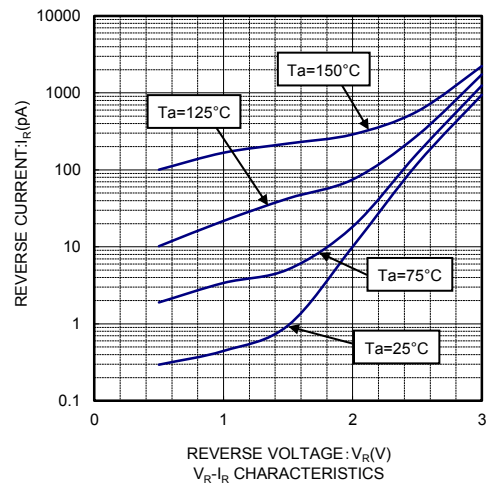
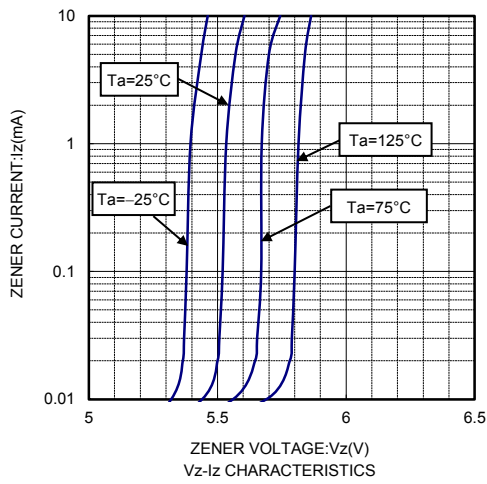
● **Absolute maximum ratings (Ta=25°C)**

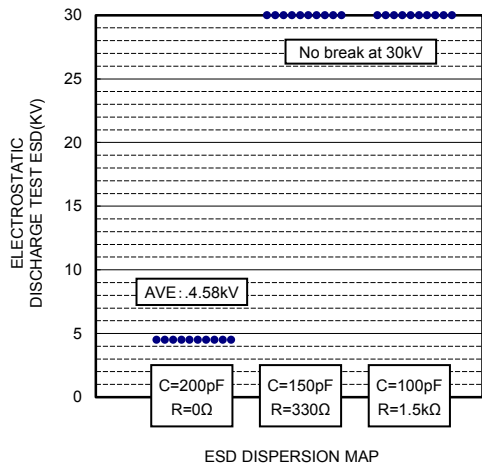
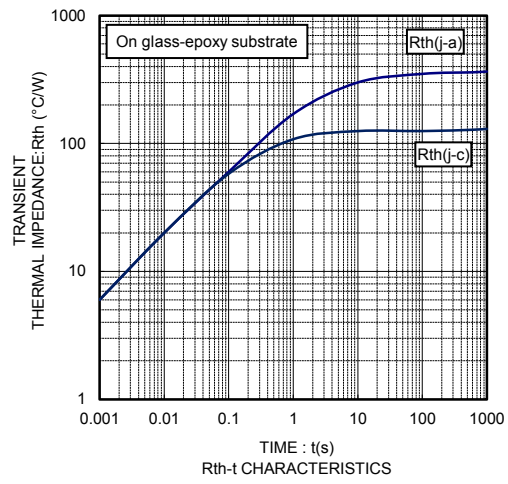
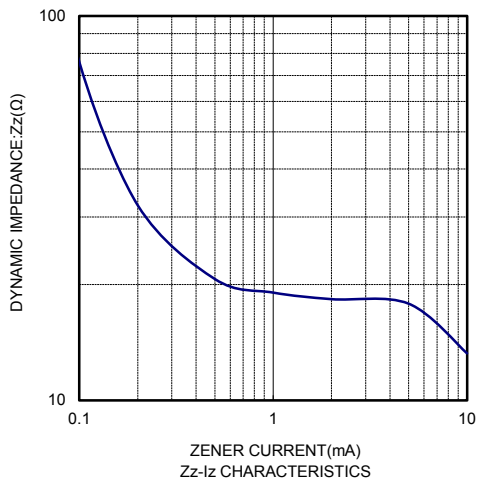
Parameter	Symbol	Limits	Unit
Power dissipation	P	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C
Operation temperature	Topr	-55 to +150	°C

● **Electrical characteristics (Ta=25°C)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Zener voltage	V _Z	5.49	-	5.73	V	I _Z =5mA
Reverse current	I _R	-	-	1.0	µA	V _R =2.5V
Capacitance between terminals	C _t	-	8.0	-	pF	f=1MHz, V _R =0V

*Zener voltage(V_Z) shall be measured at 40ms after loading current.





Notes

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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
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