DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

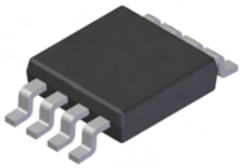
NEW PRODUCT

Features

- Dual N-Channel MOSFET
- Low On-Resistance
  - 24mΩ @ V_{GS} = 4.5V
  - 29mΩ @ V_{GS} = 2.5V
  - 37mΩ @ V_{GS} = 1.8V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

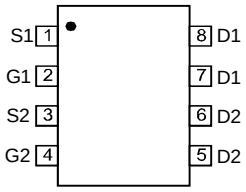
Mechanical Data

- Case: SOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072g (approximate)

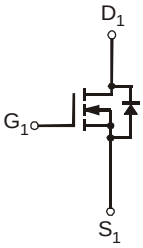


TOP VIEW

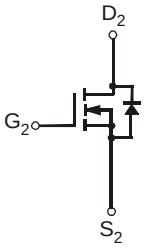
SOP-8L



TOP VIEW
Internal Schematic



N-Channel MOSFET



N-Channel MOSFET

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Drain Current (Note 1)	Steady State	T_A = 25°C T_A = 70°C	I_D	8 6.7	A
Pulsed Drain Current (Note 3)			I_{DM}	30	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_D	1.3	W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	96	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Notes:
1. Device mounted on FR-4 PCB with minimum recommended pad layout.
2. No purposefully added lead.
3. Repetitive rating, pulse width limited by function temperature.
4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

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Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	V_{GS} = 0V, I_D = 250μA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	V_{DS} = 20V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(th)}$	0.5	—	0.9	V	V_{DS} = V_{GS}, I_D = 250μA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	—	19	24	mΩ	V_{GS} = 4.5V, I_D = 8.2A
			23	29		V_{GS} = 2.5V, I_D = 3.3A
			29	37		V_{GS} = 1.8V, I_D = 2.0A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	7	—	S	V_{DS} = 10V, I_D = 4A
Diode Forward Voltage (Note 5)	V_{SD}	0.5	—	0.9	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	867	—	pF	V_{DS} = 15V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	85	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	81	—	pF	
Gate Resistance	R_G	—	1.29	—	Ω	V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_g	—	8.8	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 8.2A
Gate-Source Charge	Q_{gs}	—	1.2	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.0	—	nC	
Turn-On Delay Time	$t_{d(on)}$	—	13.2	—	ns	V_{DD} = 10V, V_{GS} = 4.5V, R_L = 10Ω, R_G = 6Ω
Turn-On Rise Time	t_r	—	12.6	—	ns	
Turn-Off Delay Time	$t_{d(off)}$	—	64.8	—	ns	
Turn-Off Fall Time	t_f	—	21.7	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.

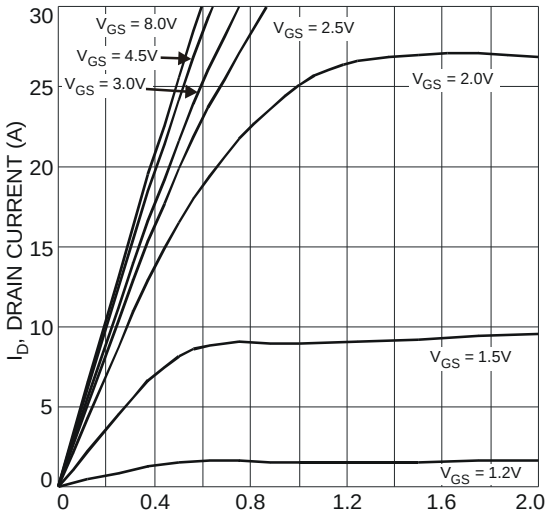

Fig. 1 Typical Output Characteristics

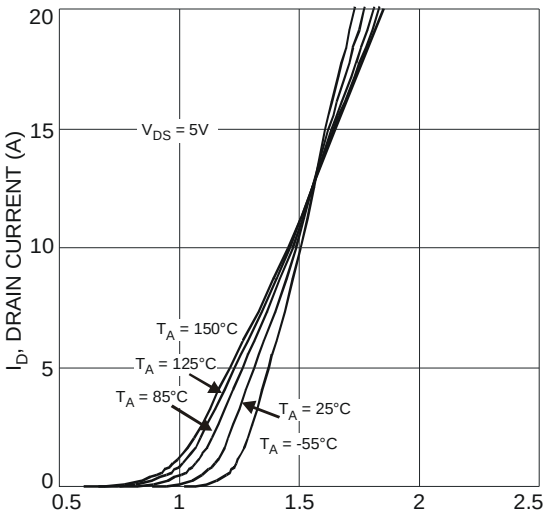

Fig. 2 Typical Transfer Characteristics

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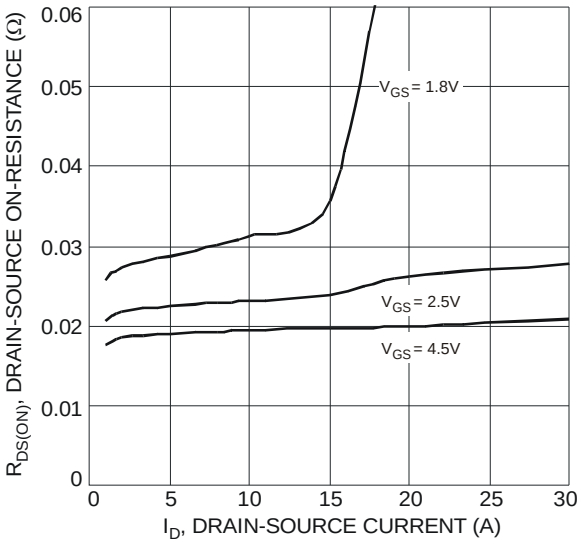

Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

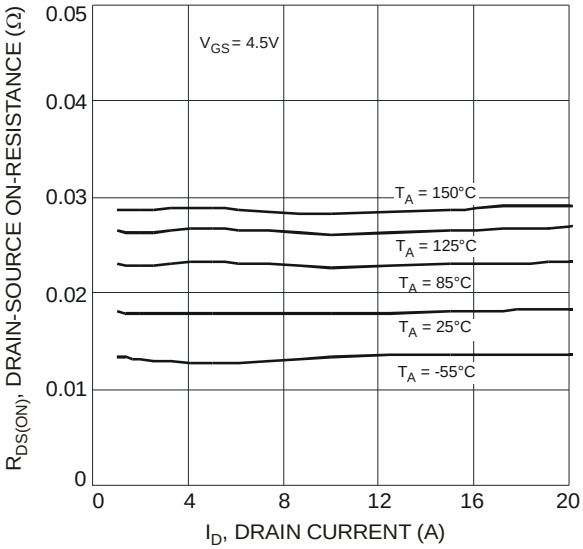

Fig. 4 Typical Drain-Source On-Resistance
vs. Drain Current and Temperature

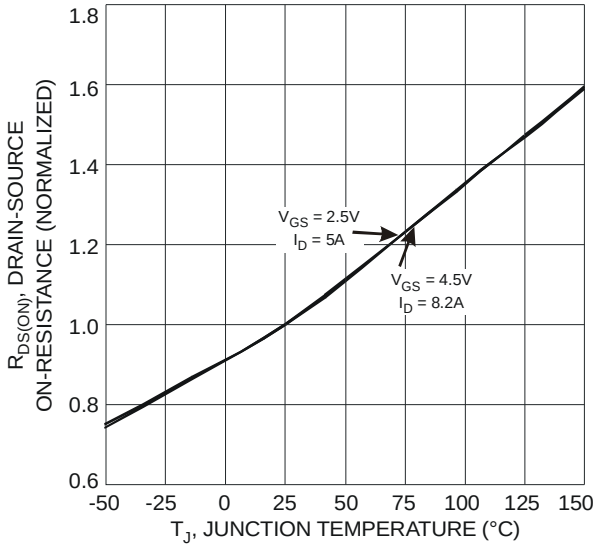

Fig. 5 On-Resistance Variation with Temperature

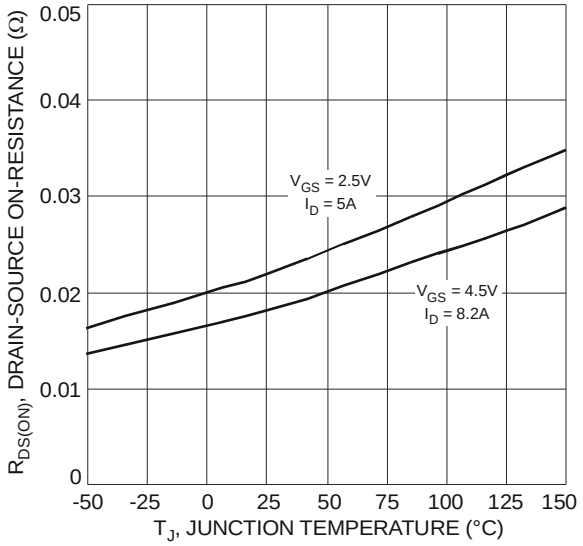

Fig. 6 On-Resistance Variation with Temperature

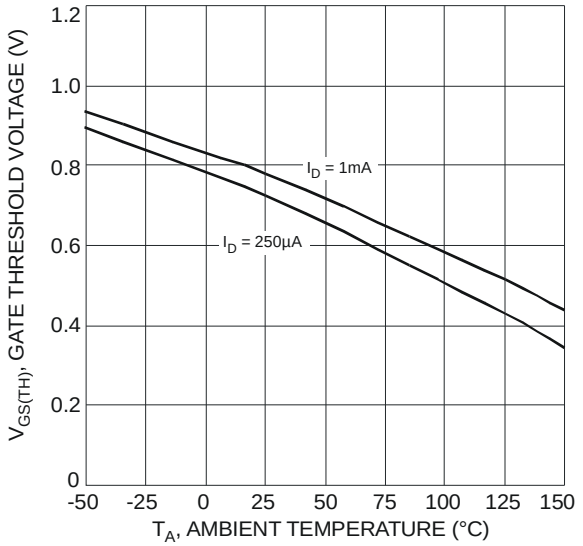

Fig. 7 Gate Threshold Variation vs. Ambient Temperature

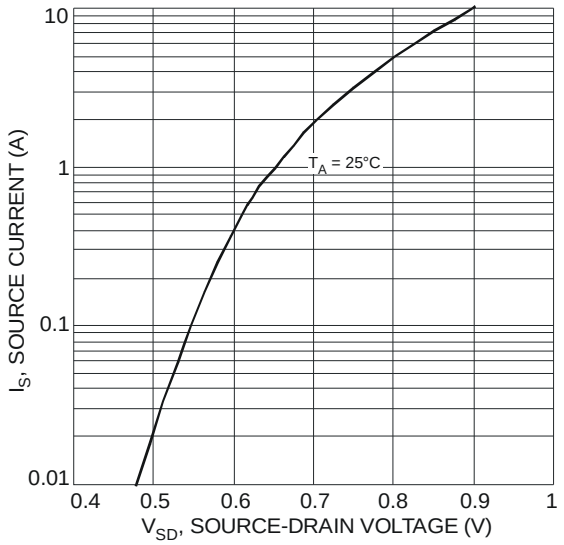

Fig. 8 Diode Forward Voltage vs. Current

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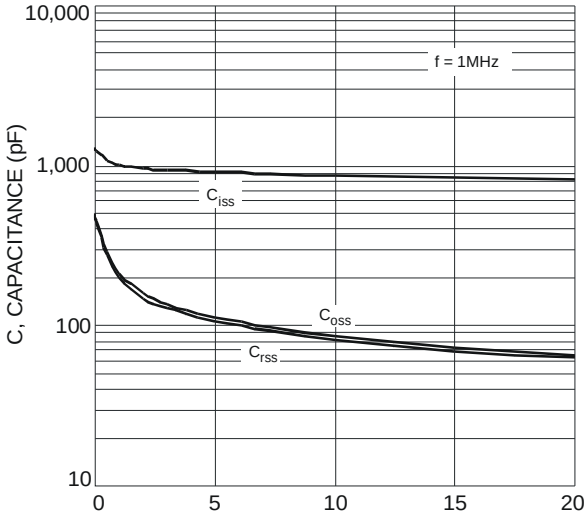


Fig. 9 Typical Capacitance

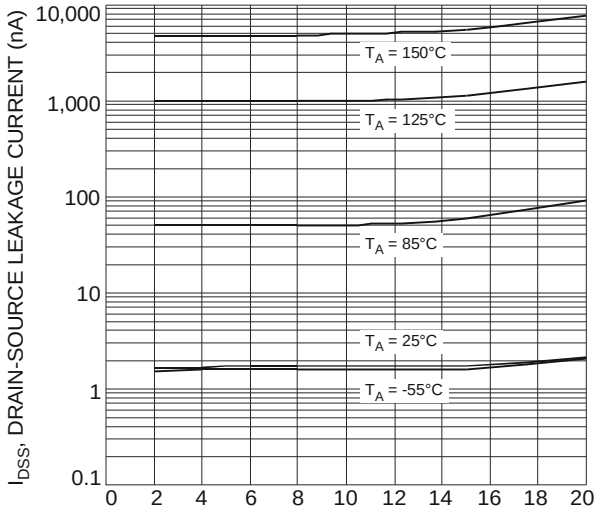


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

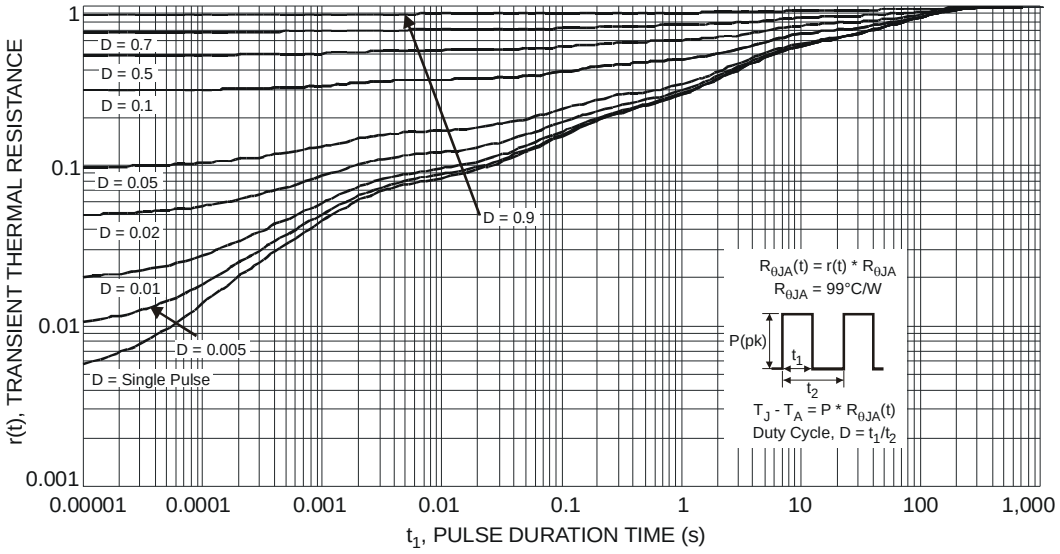


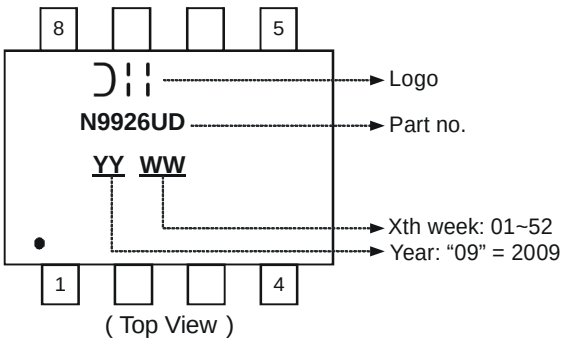
Fig. 11 Transient Thermal Response

Ordering Information (Note 6)

Part Number	Case	Packaging
DMG9926USD-13	SOP-8L	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



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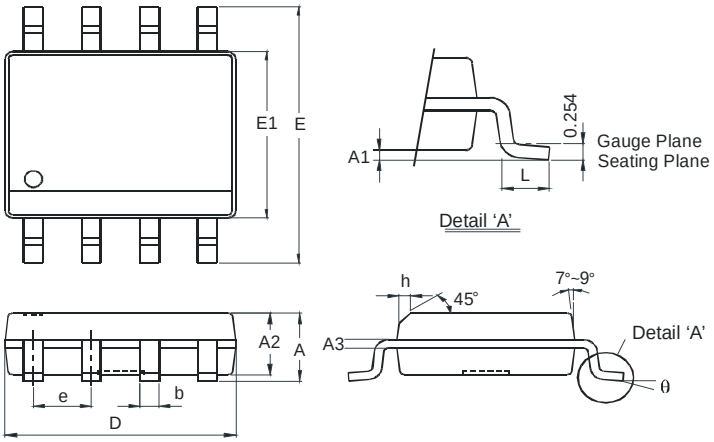
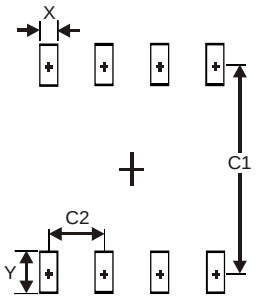
Package Outline Dimensions



SOP-8L		
Dim	**Min**	**Max**
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

NEW PRODUCT

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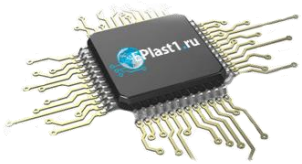
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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