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October 2035

# ISL9V2040D3S / ISL9V2040S3S / ISL9V2040P3

EcoSPARK<sup>a</sup> 200mJ, 400V, N-Channel Ignition IGBT

## **General Description**

Formerly Developmental Type 49444

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The ISL9V2040D3S, ISL9V2040S3S, and ISL9V2040P3 are the next generation ignition IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D<sup>2</sup>-Pak (TO-263) and TO-220 plastic packages. This device is intended for use in automotive ignition circuits, specifically as a coil driver. Internal diodes provide voltage clamping without the need for external components.

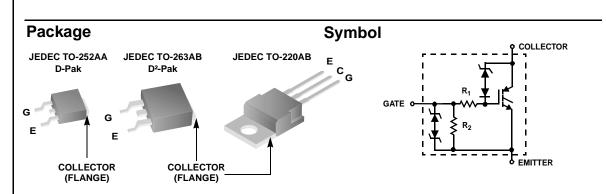
**EcoSPARK¤** devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

## Applications

Automotive Ignition Coil Driver CircuitsCoil- On Plug Applications

### Features

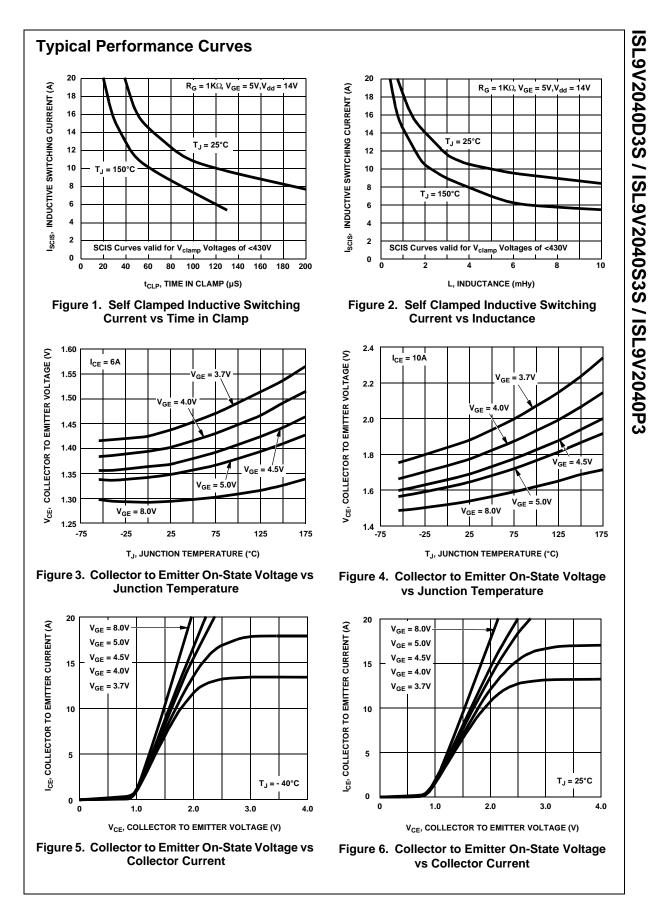
- Space saving D Pak package available
- SCIS Energy = 200mJ at T<sub>J</sub> = 25°C
- Logic Level Gate Drive

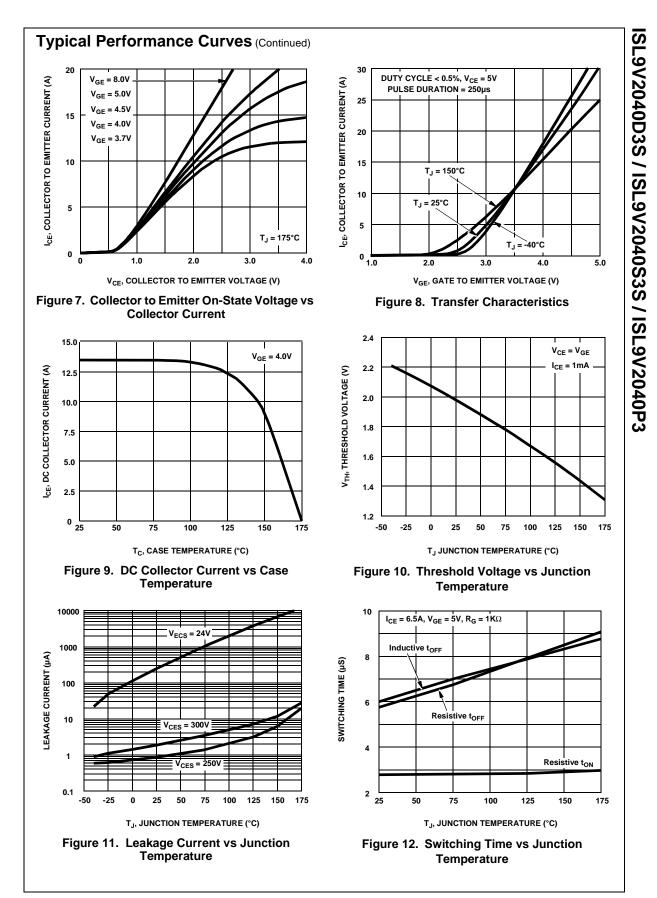


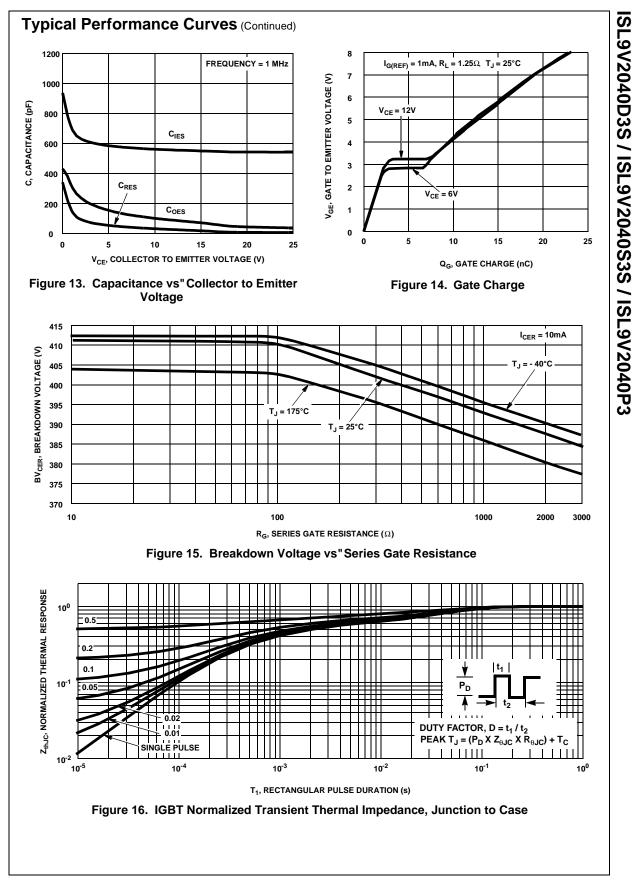
### Device Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

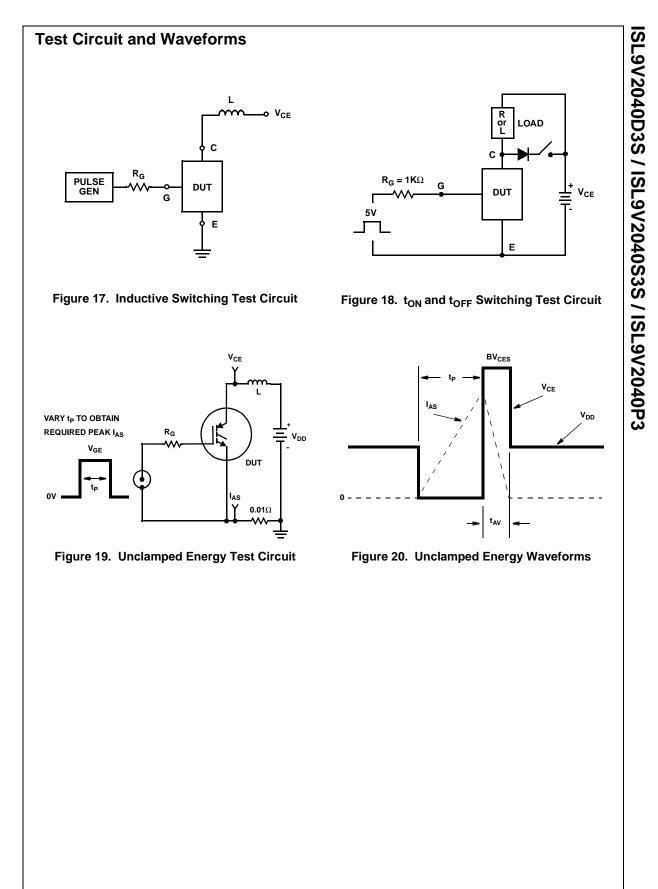
Symbol	Parameter	Ratings	Units	
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	430	V	
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10 mA)	24	V	
E <sub>SCIS25</sub>	At Starting $T_J = 25^{\circ}$ C, $I_{SCIS} = 11.5$ A, L = 3.0mHy	200	mJ	
E <sub>SCIS150</sub>	At Starting T <sub>J</sub> = 150°C, I <sub>SCIS</sub> = 8.9A, L = 3.0mHy	120	mJ	
I <sub>C25</sub>	Collector Current Continuous, At T <sub>C</sub> = 25°C, See Fig 9	10	Α	
I <sub>C110</sub>	Collector Current Continuous, At T <sub>C</sub> = 110°C, See Fig 9	10	Α	
V <sub>GEM</sub>	Gate to Emitter Voltage Continuous	±10	V	
PD	$P_D$ Power Dissipation Total $T_C = 25^{\circ}C$		W	
Power Dissipation Derating T <sub>C</sub> > 25°C		0.87	W/°C	
ТJ	T <sub>J</sub> Operating Junction Temperature Range		°C	
T <sub>STG</sub> Storage Junction Temperature Range		-40 to 175	°C	
T <sub>L</sub> Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)		300	°C	
T <sub>pkg</sub> Max Lead Temp for Soldering (Package Body for 10s)		260	°C	
ESD	Electrostatic Discharge Voltage at 100pF, 1500 $\Omega$	4	kV	

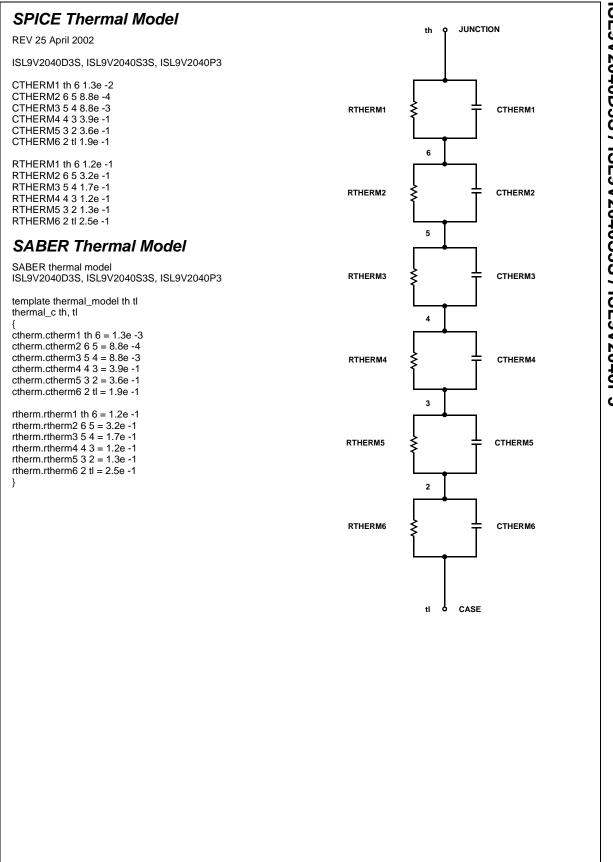
Device Marking		Device P		ackage	Reel Size	Та	pe Width	Qu	Quantity	
V204	0D	ISL9V2040D3ST	TC	)-252AA	330mm	16mm		2	2500	
V2040S ISL9V2040S3ST TO		0-263AB 330mm		24mm			800			
V2040P ISL9V2040P3 TC		D-220AB Tube		N/A			50			
V2040D ISL9V2040D3S TO		TC	D-252AA Tube		N/A			75		
V2040S ISL9V2040S3S TO			D-263AB Tube		N/A			50		
	al Chai	racteristics T <sub>A</sub> = 2	5°C un		1					
Symbol		Parameter		Test Con	ditions	Min	Тур	Max	Units	
ff State	Charact	eristics								
BV <sub>CER</sub>	Collector	or to Emitter Breakdown Voltage		$I_C = 2mA, V_{GE} = 0,$ $R_G = 1K\Omega$ , See Fig. 15 $T_J = -40$ to 150°C		370	400	430	V	
BV <sub>CES</sub>	Collector	to Emitter Breakdown Voltage		$I_{C} = 10$ mA, $V_{GE} = 0$ , $R_{G} = 0$ , See Fig. 15 $T_{J} = -40$ to 150°C		390	420	450	V	
BV <sub>ECS</sub>	Emitter to	o Collector Breakdown Vo	oltage	$I_{C} = -75$ mA, $V_{GE} = 0$ V, $T_{C} = 25$ °C		30	-	-	V	
$BV_{GES}$	Gate to E	Emitter Breakdown Voltag	e	$I_{GES} = \pm 2mA$		±12	±14	-	V	
I <sub>CER</sub>	Collector	to Emitter Leakage Curr	ent	$V_{CER} = 250V,$	$T_{C} = 25^{\circ}C$	-	-	25	μA	
				R <sub>G</sub> = 1KΩ, See Fig. 11	T <sub>C</sub> = 150°C	-	-	1	mA	
I <sub>ECS</sub>	Emitter to	o Collector Leakage Curr	ent	V <sub>EC</sub> = 24V, See	÷	-	-	1	mA	
				Fig. 11	T <sub>C</sub> = 150°C	-	-	40	mA	
R <sub>1</sub>		ate Resistance				-	70	-	Ω	
R <sub>2</sub>		Emitter Resistance				10K	-	26K	Ω	
n State ( V <sub>CE(SAT)</sub>		to Emitter Saturation Vol	tage	I <sub>C</sub> = 6A,	T <sub>C</sub> = 25°C,	-	1.45	1.9	V	
V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage		$V_{GE} = 4V$ $I_C = 10A$ ,	See Fig. 3 T <sub>C</sub> = 150°C	-	1.95	2.3	V		
ynamic	Charact	oristics		V <sub>GE</sub> = 4.5V	See Fig. 4					
	1			I <sub>C</sub> = 10A, V <sub>CE</sub> :	- 12\/		12	_	nC	
Q <sub>G(ON)</sub> Gate Charge		aige			- 12 v, Fig. 14	-	12	_	no	
V <sub>GE(TH)</sub>	Gate to I	Emitter Threshold Voltage	;		T <sub>C</sub> = 25°C	1.3	-	2.2	V	
02()		_		V <sub>CE</sub> = V <sub>GE,</sub> See Fig. 10	$T_{\rm C} = 150^{\circ}{\rm C}$	0.75	-	1.8	V	
$V_{GEP}$	Gate to I	Emitter Plateau Voltage		$I_{C} = 10A, V_{CE} = 10A$	= 12V	-	3.4	-	V	
witching	Charac	teristics								
t <sub>d(ON)R</sub>	Current <sup>-</sup>	ent Turn-On Delay Time-Resistive		V <sub>CE</sub> = 14V, R <sub>L</sub> = 1Ω,		-	0.61	-	μs	
t <sub>riseR</sub>	Current I	Rise Time-Resistive			$V_{GE} = 5V, R_G = 1K\Omega$ T <sub>J</sub> = 25°C		2.17	-	μs	
t <sub>d(OFF)L</sub>	Current	Turn-Off Delay Time-Indu	ctive	V <sub>CE</sub> = 300V, L = 500µHy,		-	3.64	-	μs	
t <sub>fL</sub>		Fall Time-Inductive		$V_{GE} = 5V$ , $R_G = 1K\Omega$ $T_J = 25^{\circ}C$ , See Fig. 12		-	2.36	-	μs	
SCIS	Self Clar	nped Inductive Switching		$T_J = 25^{\circ}C$ , L = 3.0mHy, R <sub>G</sub> = 1K $\Omega$ , V <sub>GE</sub> = 5V, See Fig. 1 & 2		-	-	200	mJ	
nermal C	Characte	eristics								
$R_{\theta JC}$		Resistance Junction-Cas		TO-252, TO-26	2 TO 220	-		1.15	°C/W	











ISL9V2040D3S / ISL9V2040S3S / ISL9V2040P3

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