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December 2007

# 74VHCT04A Hex Inverter

#### **Features**

- High speed:  $t_{PD} = 4.7$ ns (Typ.) at  $T_A = 25$ °C
- High noise immunity:  $V_{IH} = 2.0V$ ,  $V_{IL} = 0.8V$
- Power down protection is provided on all inputs and outputs
- Low noise: V<sub>OLP</sub> = 1.0V (Max.)
- Low power dissipation:  $I_{CC} = 2\mu A$  (Max.) @  $T_A = 25$ °C
- Pin and function compatible with 74HCT04

### **General Description**

The VHCT04A is an advanced high speed CMOS Inverter fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

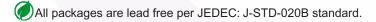
The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

Protection circuits ensure that 0V to 7V can be applied to the input pins without regard to the supply voltage and to the output pins with  $V_{CC} = 0V$ . These circuits prevent device destruction due to mismatched supply and input/output voltages. This device can be used to interface 3V to 5V systems and two supply systems such as battery backup.

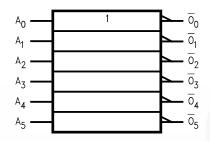
# **Ordering Information**

Order Number	Package Number	Package Description
74VHCT04AM	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
74VHCT04ASJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74VHCT04AMTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.



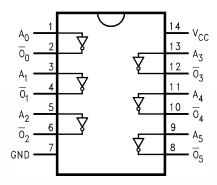
# **Logic Symbol**



# **Pin Description**

Pin Names	Description				
A <sub>n</sub>	Inputs				
$\overline{O}_{n}$	Outputs				

# **Connection Diagram**



# **Truth Table**

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## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	-0.5V to +7.0V
V <sub>IN</sub>	DC Input Voltage	-0.5V to +7.0V
V <sub>OUT</sub>	DC Output Voltage	
	Note 1	-0.5V to V <sub>CC</sub> + 0.5V
	Note 2	–0.5V to 7.0V
I <sub>IK</sub>	Input Diode Current	–20mA
I <sub>OK</sub>	Output Diode Current <sup>(3)</sup>	±20mA
I <sub>OUT</sub>	DC Output Current	±25mA
I <sub>CC</sub>	DC V <sub>CC</sub> /GND Current	±50mA
T <sub>STG</sub>	Storage Temperature	–65°C to +150°C
TL	Lead Temperature (Soldering, 10 seconds)	260°C

# Recommended Operating Conditions<sup>(4)</sup>

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating				
V <sub>CC</sub>	Supply Voltage	4.5V to +5.5V				
V <sub>IN</sub>	Input Voltage	0V to +5.5V				
V <sub>OUT</sub>	Output Voltage					
	Note 2	0V to V <sub>CC</sub>				
	Note 3	0V to 5.5V				
T <sub>OPR</sub>	Operating Temperature	-40°C to +85°C				
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time, V <sub>CC</sub> = 5.0V ±0.5V	0ns/V ~ 20ns/V				

#### Notes:

- 1. HIGH or LOW state. I<sub>OUT</sub> absolute maximum rating must be observed.
- 2.  $V_{CC} = 0V$ .
- 3. V<sub>OUT</sub> < GND, V<sub>OUT</sub> > V<sub>CC</sub> (Outputs Active).
- 4. Unused inputs must be held HIGH or LOW. They may not float.

### **DC Electrical Characteristics**

					T <sub>A</sub> = 25°C		T <sub>A</sub> = -40°C to +85°C			
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions		Min.	Тур.	Max.	Min.	Max.	Units
V <sub>IH</sub>	HIGH Level Input	4.5			2.0			2.0		V
	Voltage	5.5			2.0			2.0		]
V <sub>IL</sub>	LOW Level Input	4.5					0.8		0.8	V
	Voltage	5.5					0.8		0.8	]
V <sub>OH</sub>	HIGH Level Output	4.5	$V_{IN} = V_{IH}$	$I_{OH} = -50\mu A$	4.40	4.50		4.40		V
	Voltage			$I_{OH} = -8mA$	3.94			3.80		]
V <sub>OL</sub>	LOW Level Output	4.5	$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$		0.0	0.1		0.1	V
	Voltage			$I_{OL} = 8mA$			0.36		0.44	]
I <sub>IN</sub>	Input Leakage Current	0 – 5.5	V <sub>IN</sub> = 5.5V or GND				±0.1		±1.0	μA
I <sub>CC</sub>	Quiescent Supply Current	5.5	$V_{IN} = V_{CC}$ or GND				2.0		20.0	μA
I <sub>CCT</sub>	Maximum I <sub>CC</sub> /Input	5.5	V <sub>IN</sub> = 3.4V, Other Inputs = V <sub>CC</sub> or GND				1.35		1.50	mA
l <sub>OFF</sub>	Output Leakage Current (Power Down State)	0.0	V <sub>OUT</sub> = 5.5V				0.5		5.0	μА

### **Noise Characteristics**

				$T_A =$	$T_A = 25^{\circ}C$	
Symbol	Parameter	Conditions	$V_{CC}(V)$	Тур.	Limit	Units
V <sub>OLP</sub> <sup>(5)</sup>	Quiet Output Maximum Dynamic V <sub>OL</sub>	$C_L = 50pF$	5.0	0.8	1.0	V
V <sub>OLV</sub> <sup>(5)</sup>	Quiet Output Minimum Dynamic V <sub>OL</sub>	$C_L = 50pF$	5.0	-0.8	1.0	V
V <sub>IHD</sub> <sup>(5)</sup>	Minimum HIGH Level Dynamic Input Voltage	$C_L = 50pF$	5.0		2.0	V
V <sub>ILD</sub> <sup>(5)</sup>	Maximum LOW Level Dynamic Input Voltage	C <sub>L</sub> = 50pF	5.0		0.8	V

#### Note:

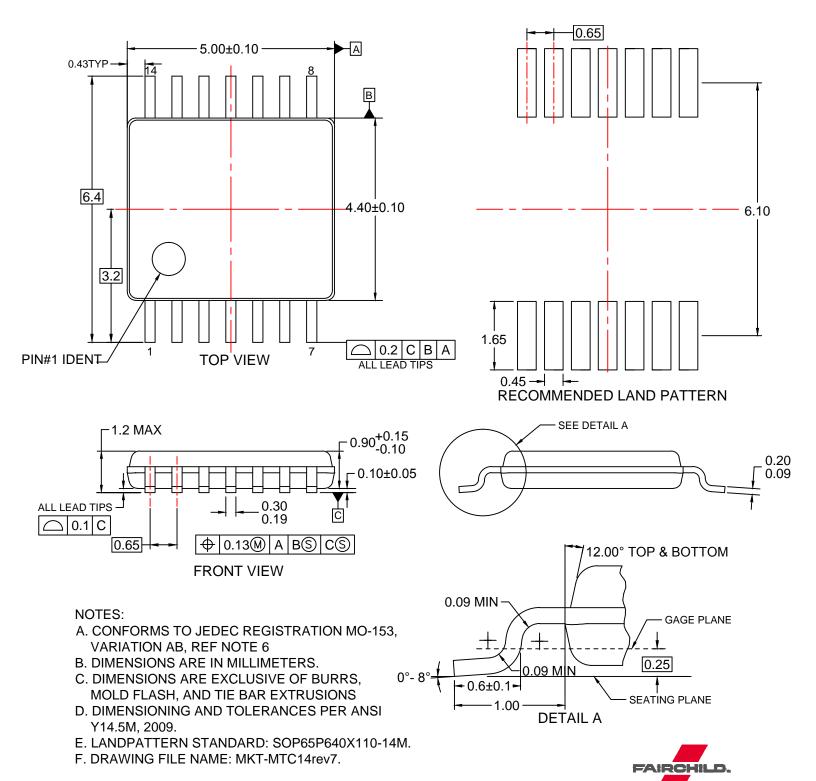
5. Parameter guaranteed by design.

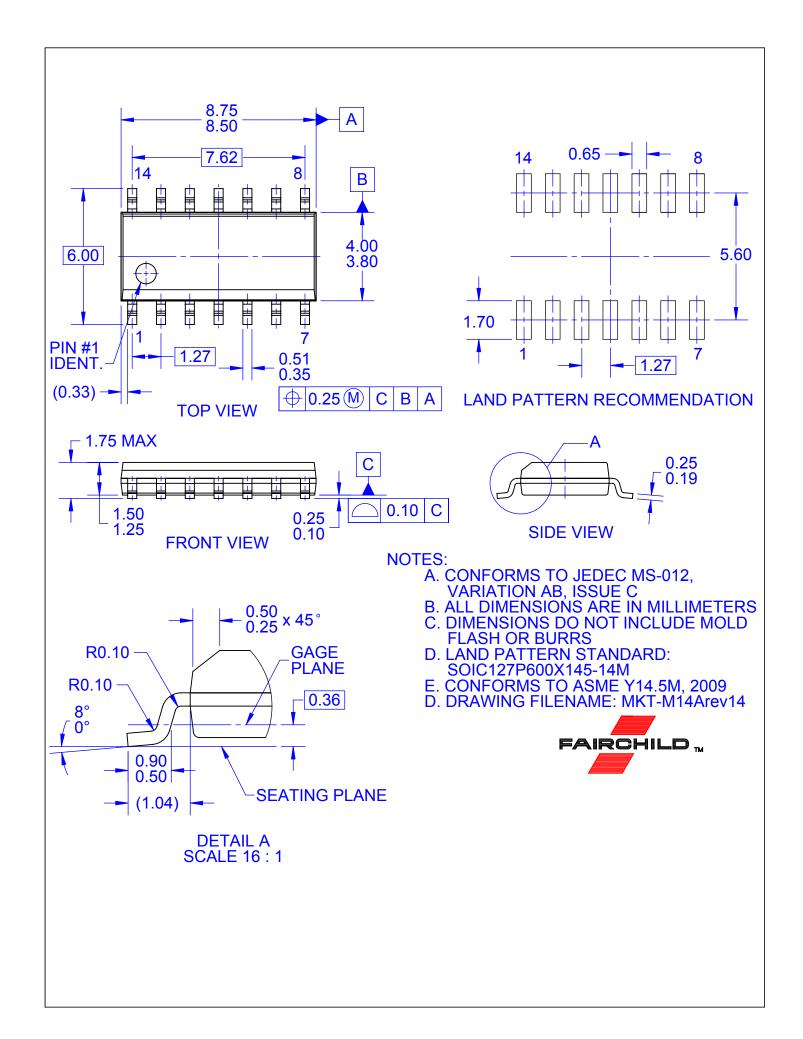
### **AC Electrical Characteristics**

				T <sub>A</sub> = 25°C		$T_A = -40$ °C to +85°C			
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min.	Тур.	Max.	Min.	Max.	Units
t <sub>PHL</sub> , t <sub>PLH</sub>	Propagation Delay	5.0 ± 0.5	C <sub>L</sub> = 15pF		4.7	6.7	1.0	7.5	ns
			C <sub>L</sub> = 50pF		5.5	7.7	1.0	8.5	
C <sub>IN</sub>	Input Capacitance		V <sub>CC</sub> = Open		4	10		10	pF
C <sub>PD</sub>	Power Dissipation Capacitance		(6)		17				pF

#### Note:

6. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance, which is calculated from the operating current consumption without load. Average operating current can be obtained from the equation:
I<sub>CC</sub> (Opr.) = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>IN</sub> + I<sub>CC</sub> / 6 (per gate)





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