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FAIRCHILD SEMICONDUCTOR®

November 2013

# FDB024N06

## N-Channel PowerTrench® MOSFET

**60 V, 265 A, 2.4 mΩ**

### Features

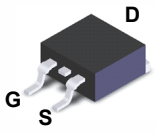
- $R_{DS(on)}$ = 1.8 mΩ (Typ.) @ $V_{GS}$ = 10 V, $I_D$ = 75 A
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(on)}$
- High Power and Current Handling Capability
- RoHS Compliant

### Description

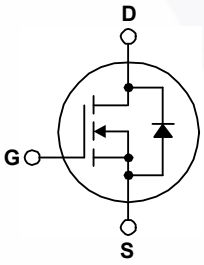
This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

### Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Renewable System




$D^2$-PAK




### MOSFET Maximum Ratings $T_C$ = 25°C unless otherwise noted.

| Symbol | Parameter | | FDB024N06 | Unit |
|---|---|---|---|---|
| $V_{DSS}$ | Drain to Source Voltage | | 60 | V |
| $V_{GSS}$ | Gate to Source Voltage | | ±20 | V |
| $I_D$ | Drain Current | - Continuous ($T_C$ = 25°C, Silicon Limited) | 265 | A |
| | | - Continuous ($T_C$ = 100°C, Silicon Limited) | 190 | |
| | | - Continuous ($T_C$ = 25°C, Package Limited) | 120 | |
| $I_{DM}$ | Drain Current | - Pulsed (Note 1) | 1060 | A |
| $E_{AS}$ | Single Pulsed Avalanche Energy | (Note 2) | 2531 | mJ |
| dv/dt | Peak Diode Recovery dv/dt | (Note 3) | 6.0 | V/ns |
| $P_D$ | Power Dissipation | ($T_C$ = 25°C) | 395 | W |
| | | - Derate Above 25°C | 2.6 | W/°C |
| $T_J$, $T_{STG}$ | Operating and Storage Temperature Range | | -55 to +175 | °C |
| $T_L$ | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds | | 300 | °C |

### Thermal Characteristics

| Symbol | Parameter | FDB024N06 | Unit |
|---|---|---|---|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case, Max. | 0.38 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max. | 62.5 | |
| | Thermal Resistance, Junction to Ambient (1 $in^2$ Pad of 2-oz Copper), Max. | 40 | |

## Package Marking and Ordering Information

| Part Number | Top Mark | Package | Packing Method | Reel Size | Tape Width | Quantity |
|---|---|---|---|---|---|---|
| FDB024N06 | FDB024N06 | $D^2$-PAK | Tape and Reel | 330 mm | 24 mm | 800 units |

## Electrical Characteristics $T_C = 25^oC$ unless otherwise noted.

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| **Off Characteristics** | | | | | | |
| $BV_{DSS}$ | Drain to Source Breakdown Voltage | $I_D$ = 250 μA, $V_{GS}$ = 0 V | 60 | - | - | V |
| $\Delta BV_{DSS}$ / $\Delta T_J$ | Breakdown Voltage Temperature Coefficient | $I_D$ = 250 μA, Referenced to 25°C | - | 0.04 | - | V/°C |
| $I_{DSS}$ | Zero Gate Voltage Drain Current | $V_{DS}$ = 60 V, $V_{GS}$ = 0 V | - | - | 1 | μA |
| | | $V_{DS}$ = 60 V, $V_{GS}$ = 0 V, $T_C$ = 150°C | - | - | 500 | |
| $I_{GSS}$ | Gate to Body Leakage Current | $V_{GS}$ = ±20 V, $V_{DS}$ = 0 V | - | - | ±100 | nA |
| **On Characteristics** | | | | | | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}$ = $V_{DS}$, $I_D$ = 250 μA | 2.5 | 3.5 | 4.5 | V |
| $R_{DS(on)}$ | Static Drain to Source On Resistance | $V_{GS}$ = 10 V, $I_D$ = 75 A | - | 1.8 | 2.4 | mΩ |
| $g_{FS}$ | Forward Transconductance | $V_{DS}$ = 10 V, $I_D$ = 75 A | - | 200 | - | S |
| **Dynamic Characteristics** | | | | | | |
| $C_{iss}$ | Input Capacitance | $V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz | - | 11190 | 14885 | pF |
| $C_{oss}$ | Output Capacitance | | - | 1610 | 2140 | pF |
| $C_{rss}$ | Reverse Transfer Capacitance | | - | 750 | 1125 | pF |
| $Q_{g(tot)}$ | Total Gate Charge at 10V | $V_{DS}$ = 48 V, $I_D$ = 75 A, $V_{GS}$ = 10 V (Note 4) | - | 174 | 226 | nC |
| $Q_{gs}$ | Gate to Source Gate Charge | | - | 54 | - | nC |
| $Q_{gd}$ | Gate to Drain “Miller” Charge | | - | 50 | - | nC |
| **Switching Characteristics** | | | | | | |
| $t_{d(on)}$ | Turn-On Delay Time | $V_{DD}$ = 30 V, $I_D$ = 75 A, $V_{GS}$ = 10 V, $R_G$ = 25 Ω (Note 4) | - | 134 | 278 | ns |
| $t_r$ | Turn-On Rise Time | | - | 324 | 658 | ns |
| $t_{d(off)}$ | Turn-Off Delay Time | | - | 348 | 706 | ns |
| $t_f$ | Turn-Off Fall Time | | - | 250 | 510 | ns |
| **Drain-Source Diode Characteristics** | | | | | | |
| $I_S$ | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 265 | A |
| $I_{SM}$ | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 1060 | A |
| $V_{SD}$ | Drain to Source Diode Forward Voltage | $V_{GS}$ = 0 V, $I_{SD}$ = 75 A | - | - | 1.3 | V |
| $t_{rr}$ | Reverse Recovery Time | $V_{GS}$ = 0 V, $I_{SD}$ = 75 A, $dI_F/dt$ = 100 A/μs | - | 69 | - | ns |
| $Q_{rr}$ | Reverse Recovery Charge | | - | 152 | - | nC |

**Notes:**

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 0.9 mH, $I_{AS}$ = 75 A, $V_{DD}$ = 50 V, $R_G$ = 25 Ω, starting $T_J$ = 25°C.
3. $I_{SD}$ ≤ 75 A, di/dt ≤ 200 A/μs, $V_{DD}$ ≤ $BV_{DSS}$, starting $T_J$ = 25°C.
4. Essentially independent of operating temperature typical characteristics.

## Typical Performance Characteristics

**Figure 1. On-Region Characteristics**

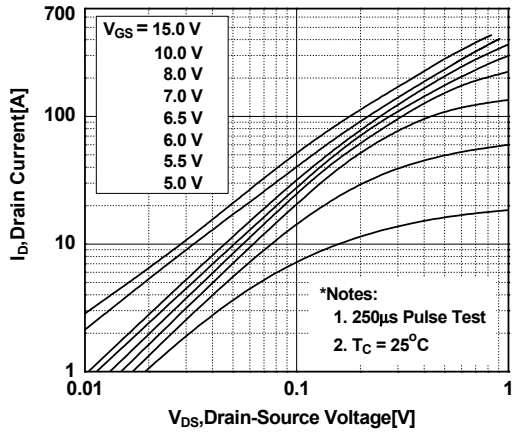



**Figure 2. Transfer Characteristics**

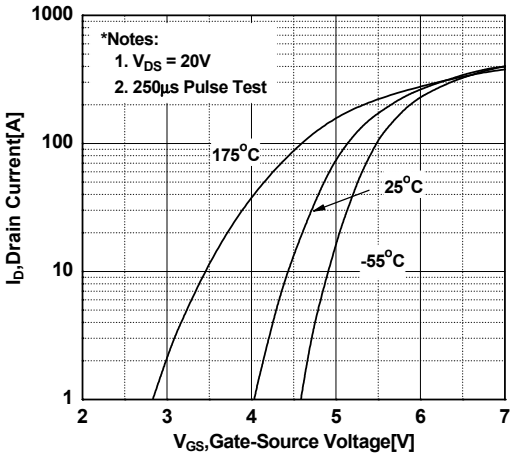



**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**

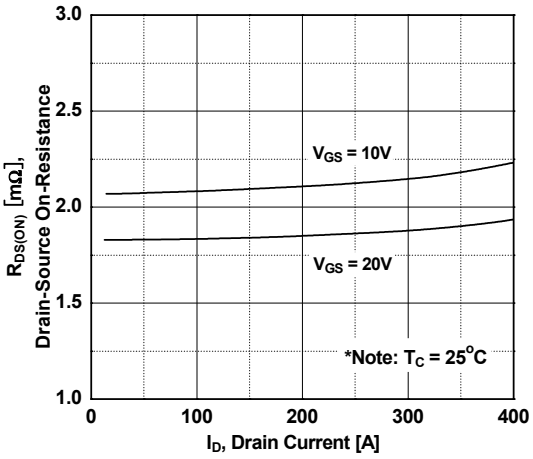



**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**

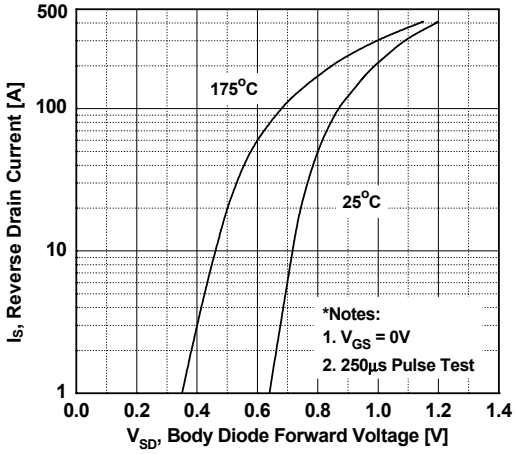



**Figure 5. Capacitance Characteristics**

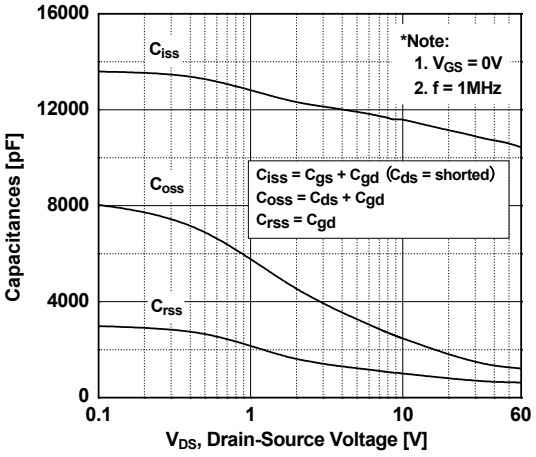
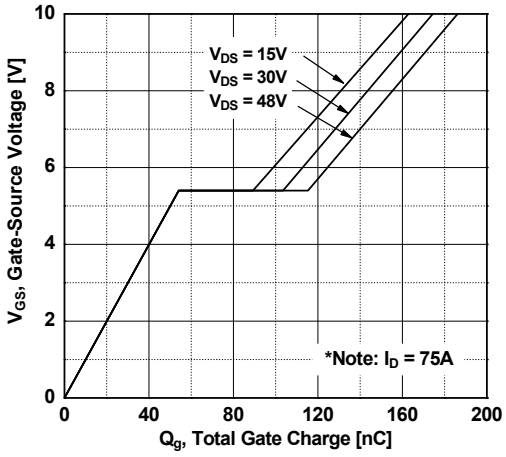



**Figure 6. Gate Charge Characteristics**



## Typical Performance Characteristics (Continued)

**Figure 7. Breakdown Voltage Variation vs. Temperature**

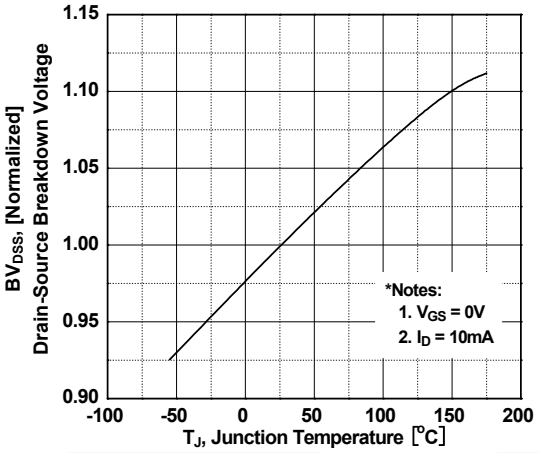



**Figure 8. On-Resistance Variation vs. Temperature**

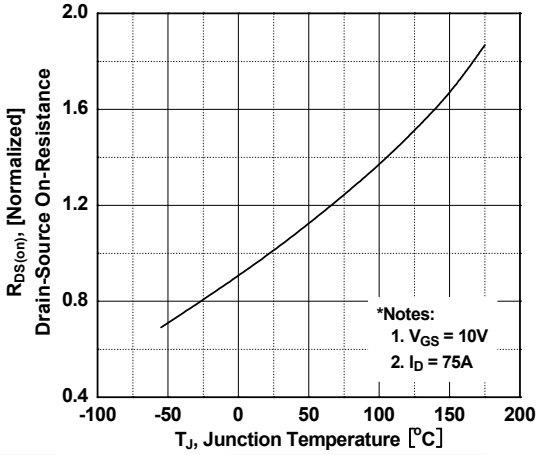



**Figure 9. Maximum Safe Operating Area**

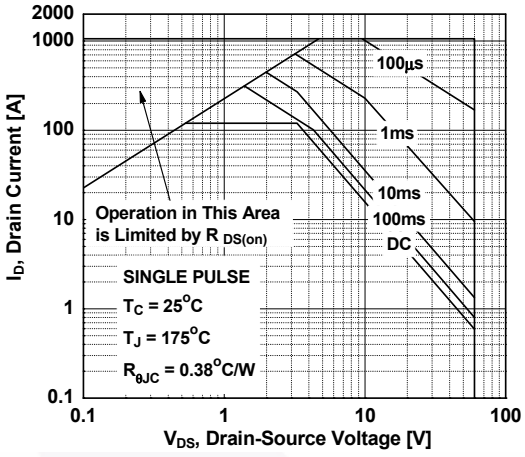



**Figure 10. Maximum Drain Current vs. Case Temperature**

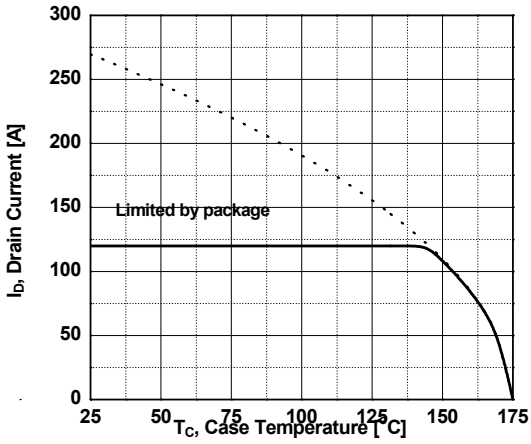
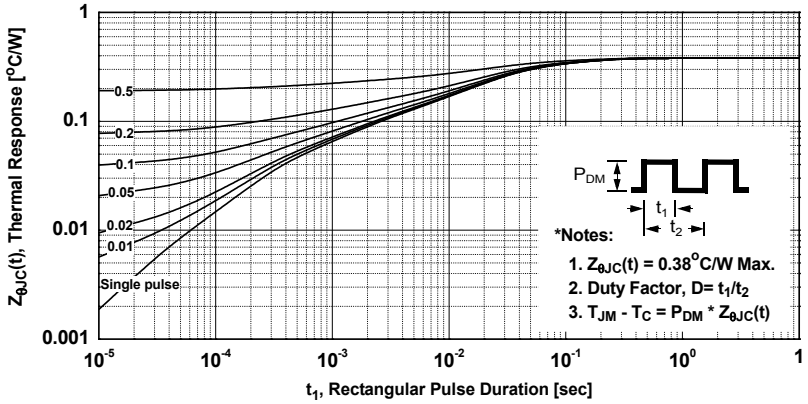



**Figure 11. Transient Thermal Response Curve**



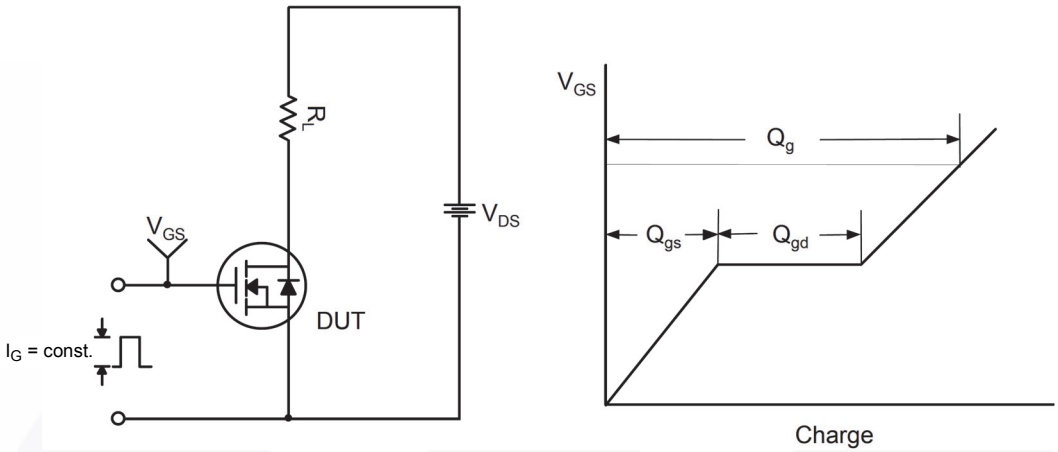



**Figure 12. Gate Charge Test Circuit & Waveform**

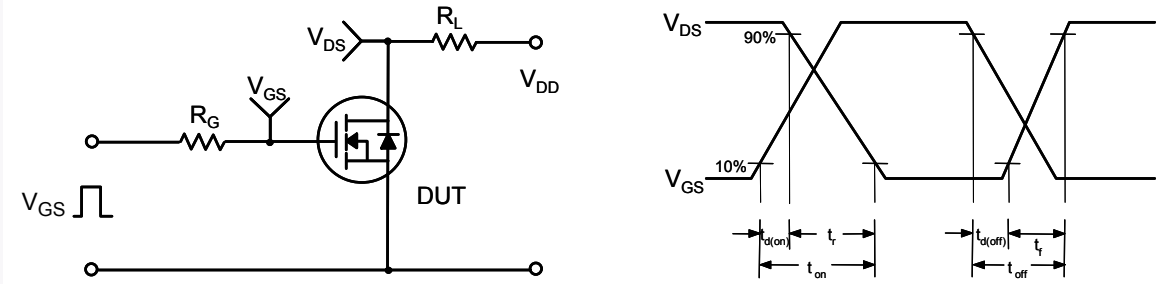



**Figure 13. Resistive Switching Test Circuit & Waveforms**

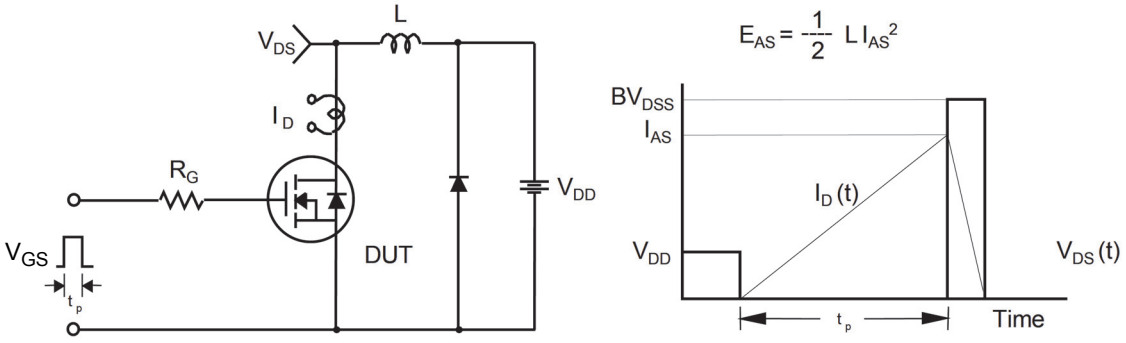



**Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms**

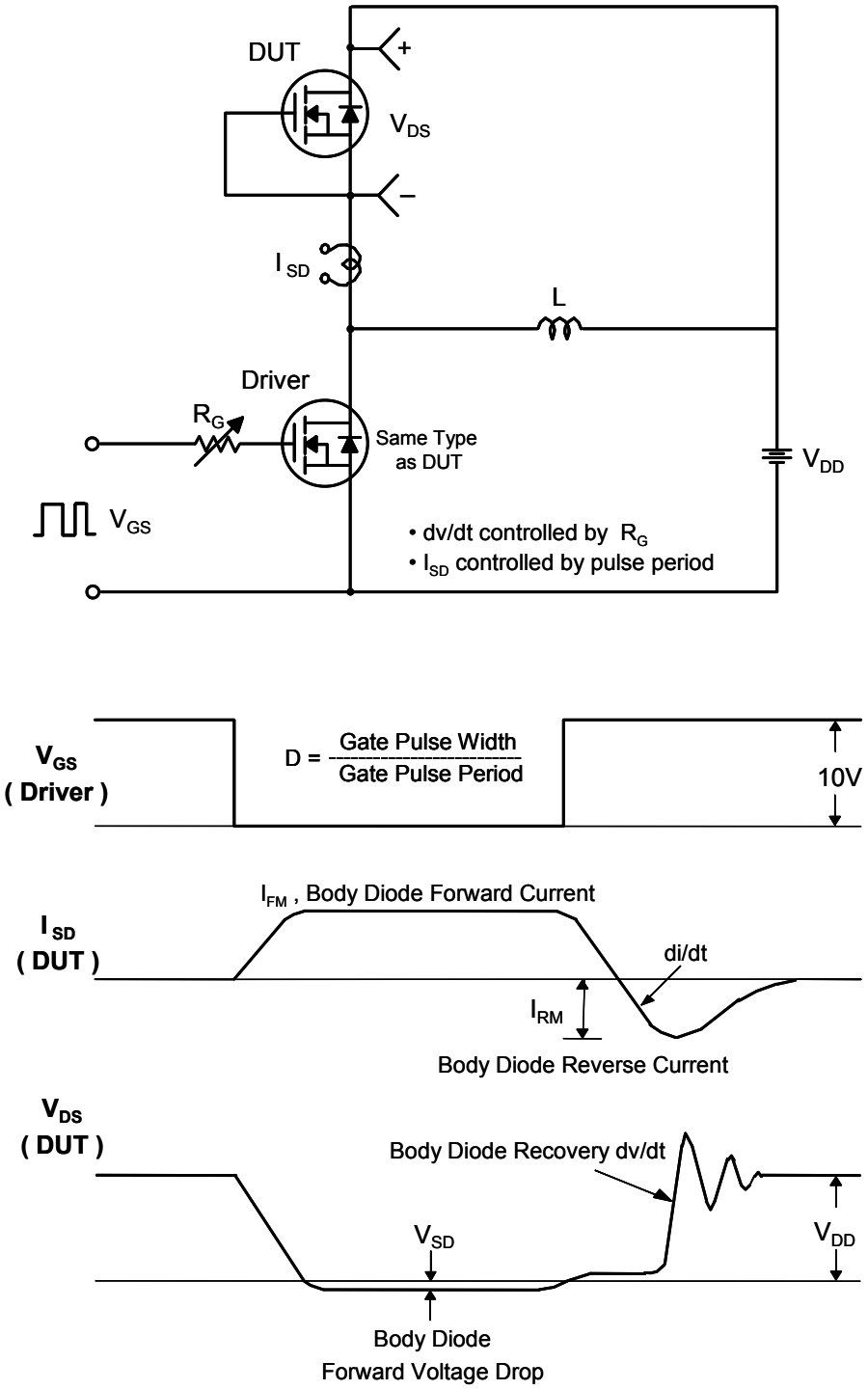



**Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms**

## Mechanical Dimensions

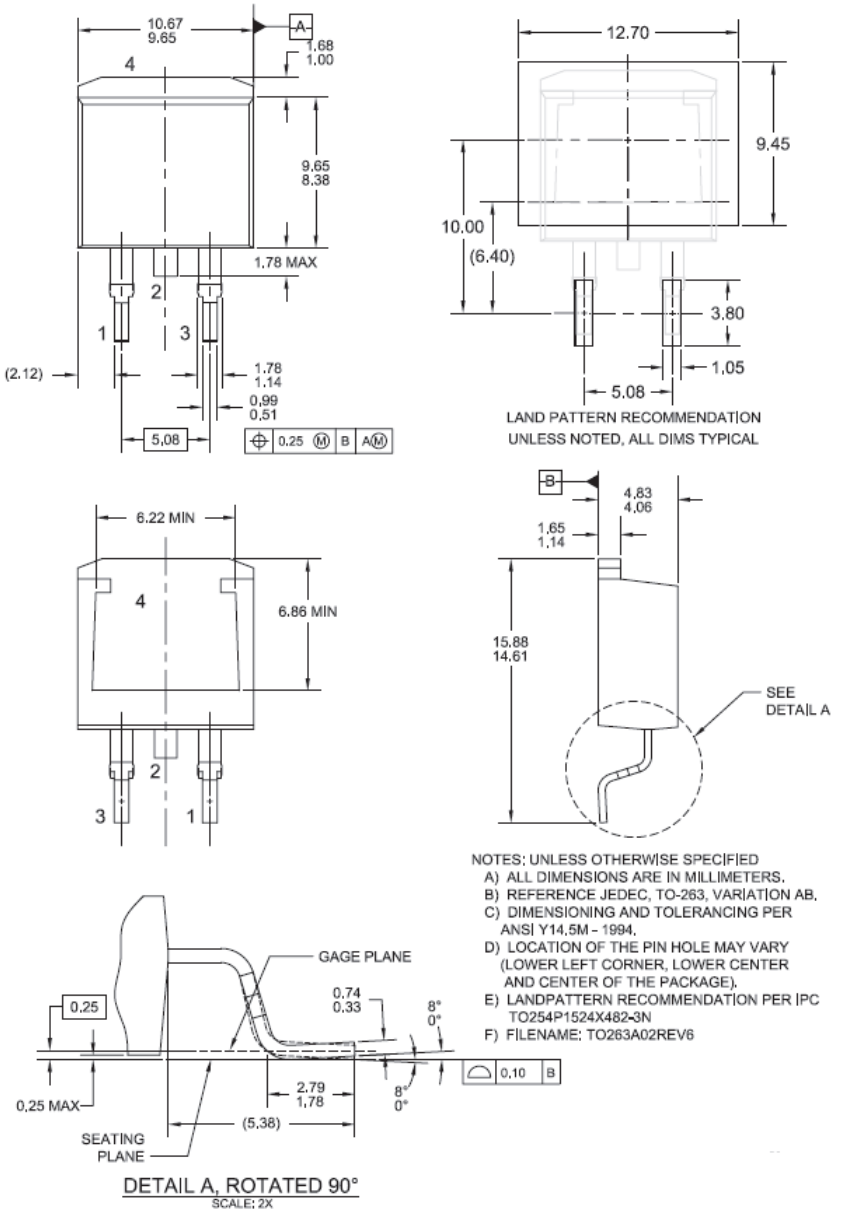



**Figure 16. TO263 ($D^2$PAK), Molded, 2-Lead, Surface Mount**

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Rev. I66

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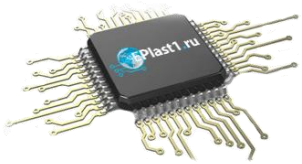
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.

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**Факс:** 8 (812) 320-02-42
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**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.