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June 2015

MMBF4416A N-Channel RF Amplifier

Features

- · This device is designed for RF amplifiers.
- · Sourced from process 50.



Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBF4416A	6BG	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{DG}	Drain-Gate Voltage	35	V
V_{GS}	Gate-Source Voltage	-35	V
I_{GF}	Forward Gate Current	10	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics(3)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
D	Total Device Dissipation	225	mW
P_{D}	Derate Above 25°C	1.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	556	°C/W

Note:

3. Device mounted on FR-4 PCB 1.6" x 1.6" x 0.06".

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Characte	eristics				
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$V_{DS} = 0$, $I_{G} = 1.0 \mu\text{A}$	-35		V
I _{GSS}	Gate Reverse Current	V _{GS} = -20 V, V _{DS} = 0		-100	pA
V _{GS} (off)	Gate-Source Cut-Off Voltage	V _{DS} = 15 V, I _D = 1.0 nA	-2.5	-6.0	V
V _{GS}	Gate-Source Voltage	V _{DS} = 15 V, I _D = 500 μA	-1.0	-5.5	V
On Characte	eristics				
I _{DSS}	Zero-Gate Voltage Drain Current	V _{DS} = 15 V, V _{GS} = 0	5	15	mA
V _{GS} (f)	Gate-Source Forward Voltage	V _{DS} = 0, I _G = 1.0 mA		1	V
Small Signal	Characteristics				
9 _{fs}	Forward Transfer Conductance ⁽⁴⁾	V _{DS} = 15 V, V _{GS} = 0, f = 1.0 kHz	4500	7500	μmhos
g _{os}	Output Conductance ⁽⁴⁾	V _{DS} = 15 V, V _{GS} = 0, f = 1.0 kHz		50	μmhos
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz}$		4.0	pF
C _{rss}	Reverse Transfer Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz}$		0.8	pF
C _{oss}	Output Capacitance	V _{DS} = 15 V, V _{GS} = 0, f = 1.0 MHz		2.0	pF
NF	Noise Figure	$V_{DS} = 15 \text{ V}, V_{GS} = 0, I_{D} = 5 \text{ mA}, R_{g} = 1 \text{ k}\Omega, f = 400 \text{ MHz}$		4.0	dB

Note:

4. Pulse test: pulse width ≤ 300 ms, duty cycle ≤ 2%

Physical Dimensions 0.95 2.92±0.20 3 1.40 1.30+0.20 2.20 2 0.60 (0.29) -0.37 0.95 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A 1.20 MAX (0.93)0.10 0.00 △ 0.10M C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. B) ALL DIMENSIONS ARE IN MILLIMETERS. 0.23 0.08 C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER

Figure 1. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE

SEATING PLANE

ASME Y14.5M - 1994.

E) DRAWING FILE NAME: MA03DREV10

0.20 MIN

(0.55)

DETAIL A
SCALE: 2X





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Definition of Terms			
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Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.