

ZXTN25020DG 20V NPN high gain transistor in SOT223

Summary

 $BV_{CEX} > 100V$

 $BV_{CEO} > 20V$

 $BV_{ECX} > 6V$

 $I_{C(cont)} = 7A$

V_{CE(sat)} < 48mV @ 1A

 $R_{CE(sat)} = 31m\Omega$

 $P_{D} = 3.0W$



Complementary part number ZXTP25020DG

Description

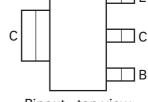
Packaged in the SOT223 outline this new low saturation NPN transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

Features

- · Higher power dissipation SOT223 package
- · High gain
- · High peak current
- · Low saturation voltage
- 100V forward blocking voltage
- · 6V reverse blocking voltage

Applications

- · DC DC converters
- · Motor drive
- · Relay, lamp and solenoid drive
- · Regulator circuits



Pinout - top view

Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTN25020DGTA	7	12	1000

Device marking

ZXTN25 020D

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Collector-Base voltage	V _{CBO}	100	V
Collector-Emitter voltage (forward blocking)	V _{CEX}	100	V
Collector-Emitter voltage	V _{CEO}	20	V
Emitter-Collector voltage (reverse blocking)	V _{ECX}	6	V
Emitter-Base voltage	V _{EBO}	7	V
Continuous Collector current(c)	Ic	7	Α
Base current	I _B	1	Α
Peak pulse current	I _{CM}	15	Α
Power dissipation at T _A =25°C ^(a)	P _D	1.2	W
Linear derating factor		9.6	mW/°C
Power dissipation at T _A =25°C ^(b)	P _D	1.6	W
Linear derating factor		12.8	mW/°C
Power dissipation at T _A =25°C ^(c)	P _D	3.0	W
Linear derating factor		24	mW/°C
Power dissipation at T _A =25°C ^(d)	P _D	5.3	W
Linear derating factor		42	mW/°C
Power dissipation at T _C =25°C ^(e)	P _D	7.3	W
Linear derating factor		58	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient ^(a)	$R_{\Theta JA}$	104	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	78	°C/W
Junction to ambient ^(c)	$R_{\Theta JA}$	42	°C/W
Junction to ambient ^(d)	$R_{\Theta JA}$	23.5	°C/W
Junction to case ^(e)	$R_{\Theta JC}$	16	°C/W

NOTES:

⁽a) For a device surface mounted on 15mm x 15mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

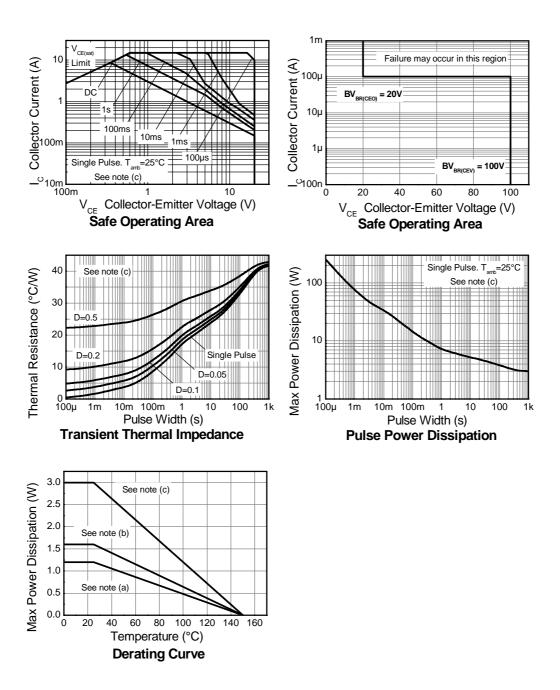
⁽b) Mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽c) Mounted on 50mm x 50mm x 0.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

⁽d) As (c) above measured at t<5 seconds.

⁽e) Junction to case (collector tab). Typical

Thermal characteristics



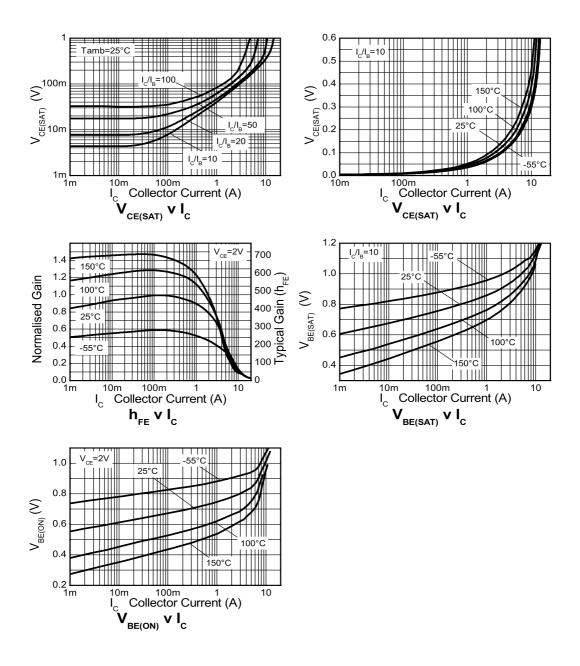
Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated).

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-Base breakdown voltage	BV _{CBO}	100	125		V	$I_C = 100 \mu A$
Collector-Emitter breakdown voltage (forward blocking)	BV _{CEX}	100	120		V	I_C = 100μA, R_{BE} < 1k Ω or -1V < V_{BE} < 0.25V
Collector-Emitter breakdown voltage	BV _{CEO}	20	35		V	I _C = 10mA ^(*)
Emitter-Collector breakdown voltage (reverse blocking)	BV _{ECX}	6	8.3		V	I_E = 100μA, R_{BC} < 1kΩ or 0.25V > V_{BC} > -0.25V
Emitter-Collector breakdown voltage (reverse blocking)	BV _{ECO}	5	6.1		V	I _E = 100μA
Emitter-Base breakdown voltage	BV _{EBO}	7	8.35		V	I _E = 100μA
Collector-Base cut-off current	I _{CBO}		<1	50 0.5	nA μA	V _{CB} = 100V V _{CB} = 100V, T _{amb} = 100°C
Collector-Emitter cut-off current	I _{CEX}			100	nA	$V_{CE} = 100V, R_{BE} < 1k\Omega$ or $-1V < V_{BE} < 0.25V$
Emitter cut-off current	I _{EBO}		<1	50	nA	V _{EB} = 5.6V
Collector-Emitter	V _{CE(sat)}		40	48	mV	$I_C = 1A$, $I_B = 100 \text{mA}^{(*)}$
saturation voltage			60	75	mV	$I_C = 1A$, $I_B = 20mA^{(*)}$
			100	120	mV	$I_C = 2A$, $I_B = 40mA^{(*)}$
			130	180	mV	$I_C = 2A$, $I_B = 20mA^{(*)}$
			225	290	mV	$I_C = 7A$, $I_B = 700 \text{mA}^{(*)}$
Base-Emitter saturation voltage	V _{BE(sat)}		1090	1150	mV	$I_C = 7A$, $I_B = 700 \text{mA}^{(*)}$
Base-Emitter turn-on voltage	V _{BE(on)}		950	1050	mV	$I_C = 7A$, $V_{CE} = 2V^{(*)}$
Static forward current	h _{FE}	300	450	900		$I_C = 10 \text{mA}, V_{CE} = 2V^{(*)}$
transfer ratio		250	360			$I_C = 2A, V_{CE} = 2V^{(*)}$
		50	85			$I_C = 7A$, $V_{CE} = 2V^{(*)}$
			15			$I_C = 15A, V_{CE} = 2V^{(*)}$
Transition frequency	f _T		215		MHz	I _C = 50mA, V _{CE} = 10V f = 100MHz
Input capacitance	C _{ibo}		152		pF	$V_{EB} = 0.5V, f = 1MHz^{(*)}$
Output capacitance	C _{obo}		16.5	25	pF	V _{CB} = 10V, f = 1MHz ^(*)
Delay time	t _d		67.7		ns	44.1/
Rise time	t _r		72.2		ns	$I_C = 1A, V_{CC} = 10V,$
Storage time	t _s		361		ns	$I_{B1} = -I_{B2} = 10 \text{mA}$
Fall time	t _f		63.9		ns	

NOTES:

(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$

Typical characteristics



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