General Description

The MAX6709/MAX6714 quad voltage monitors provide accurate monitoring of up to four supplies without any external components. A variety of factory-trimmed threshold voltages and supply tolerances are available to optimize the MAX6709/MAX6714 for specific applications. The selection includes input options for monitoring 5.0V, 3.3V, 3.0V, 2.5V, and 1.8V voltages. Additional high-inputimpedance comparator options can be used as adjustable voltage monitors, general-purpose comparators, or digital-level translators.

The MAX6709 provides four independent open-drain outputs with 10 μ A internal pullup to V_{CC}. The MAX6714 provides an active-low, open-drain RESET output with integrated reset timing and three power-fail comparator outputs.

Each of the monitored voltages is available with trip thresholds to support power-supply tolerances of either 5% or 10% below the nominal voltage. An internal bandgap reference ensures accurate trip thresholds across the operating temperature range.

The MAX6709 consumes only 35μ A (typ) of supply current. The MAX6714 consumes only 60μ A (typ) of supply current. The MAX6709/MAX6714 operate with supply voltages of 2.0V to 5.5V. An internal undervoltage lock-out circuit forces all four digital outputs low when V_{CC} drops below the minimum operating voltage. The four digital outputs have weak internal pullups to V_{CC}, accommodating wire-ORed connections. Each input threshold voltage has an independent output. The MAX6709/MAX6714 are available in a 10-pin μ MAX package and operate over the extended (-40°C to +85°C) temperature range.

Applications

Telecommunications Servers High-End Printers Desktop and Notebook Computers Data Storage Equipment Networking Equipment Multivoltage Systems

Typical Operating Circuits appear at end of data sheet. Selector Guides appear at end of data sheet.

Features

- Monitor Four Power-Supply Voltages
- Precision Factory-Set Threshold Options for 5.0V, 3.3V, 3.0V, 2.5V, and 1.8V (Nominal) Supplies
- Adjustable Voltage Threshold Monitors Down to 0.62V
- High-Accuracy (±2.0%) Adjustable Threshold Inputs
- ♦ Low Supply Current MAX6709: 35µA MAX6714: 60µA
- Four Independent, Active-Low, Open-Drain Outputs with 10µA Internal Pullup to V_{CC}
- 140ms (min) Reset Timeout Period (MAX6714 only)
- ♦ 2.0V to 5.5V Supply Voltage Range
- Immune to Supply Transients
- ♦ Fully Specified from -40°C to +85°C
- Small 10-Pin µMAX Package

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE		
MAX6709_UB*	-40°C to +85°C	10 µMAX		
MAX6714_UB*	-40°C to +85°C	10 µMAX		

*Insert the desired letter from the Selector Guide into the blank to complete the part number.

Devices are available in both leaded and lead-free packaging. Specify lead free by adding the + symbol at the end of the part number when ordering.

Pin Configurations



Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

All Pins to GND	0.3V to +6V
Input/Output Current (all pins)	20mA
Continuous Power Dissipation ($T_A = +$	
10-Pin µMAX (derate 5.6mW/°C abo	ove +70°C)444mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS (MAX6709)

 $(V_{CC} = 2.0V \text{ to } 5.5V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } V_{CC} = 5V \text{ and } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	MAX	UNITS
Supply Voltage Range	VCC			2.0		5.5	V
		$V_{CC} = 3V$			25	50	
Supply Current	ICC	$V_{CC} = 5V$			35	65	μΑ
		V _{IN} = input threshold voltage			25	40	
Input Current	l _{IN} _	$V_{IN_{}} = 0$ to 0.85V (for adjustable threshold)				0.2	μA
			5.0V (-5%)	4.50	4.63	4.75	
			5.0V (-10%)	4.25	4.38	4.50	1
			3.3V (-5%)	3.00	3.08	3.15	- V
			3.3V (-10%)	2.85	2.93	3.00	
			3.0V (-5%)	2.70	2.78	2.85	
Threshold Voltage	VTH	IN_ decreasing	3.0V (-10%)	2.55	2.63	2.70	
			2.5V (-5%)	2.25	2.32	2.38	
			2.5V (-10%)	2.13	2.19	2.25	
			1.8V (-5%)	1.62	1.67	1.71	
			1.8V (-10%)	1.53	1.58	1.62	
Adjustable Threshold	VTH	IN_ decreasing		0.609	0.623	0.635	V
Threshold Voltage Temperature Coefficient	TCVTH				60		ppm/°C
Threshold Hysteresis	V _{HYST}				0.3 x VTH	1	%
Propagation Delay		V _{IN} _ falling at 10mV/µs from V _{TH} to (V _{TH} - 50mV)			30		
Tropagation Delay	tpD	V _{IN} _rising at 10mV/µs from V _{TH} to (V _{TH} + 50mV)			5		μs
Output Low Voltage	Vol	$V_{CC} = 5V$, $I_{SINK} = 2mA$				0.3	
		V _{CC} = 2.5V, I _{SINK} = 1.2mA				0.3	V
		V _{CC} = 1V, I _{SINK} = 50µA (Note 2)				0.3]
Output High Voltage	V _{OH}	$V_{CC} \ge 2.0V$, I_{SOURC} unasserted	0.8 x V(CC		V	
Output High Source Current	IOH	$V_{CC} \ge 2.0V$, PWRG		10		μA	

ELECTRICAL CHARACTERISTICS (MAX6714)

 $(V_{CC} = 2.0V \text{ to } 5.5V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } V_{CC} = 5V \text{ and } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONE	MIN	ТҮР	MAX	UNITS	
Supply Voltage Range	Vcc			2.0		5.5	V
Supply Current (Nate 2)	laa	V _{CC} = 3V			60	90	
Supply Current (Note 3)	Icc	$V_{CC} = 5V$			80	105	μA
Power-Fail Input Current	IPFI_	V _{PFI} = 0 to 0.85V				0.2	μΑ
			MAX6714B (-5%)	4.50	4.63	4.75	
Vee Deest Threehold	\/		MAX6714A (-10%)	4.25	4.38	4.50	
V _{CC} Reset Threshold	V _{TH}	V _{CC} decreasing	MAX6714D (-5%)	3.00	3.08	3.15	V
		MAX6714C (-10%	MAX6714C (-10%)	2.85	2.93	3.00	1
Power-Fail Input Threshold	VPFI	V _{PFI} _ decreasing	·	0.609	0.623	0.635	V
Threshold Hysteresis	VHYST	VPFI_ increasing relat	tive to V _{PFI} decreasing		0.3 x V _{TH}		%
Reset Timeout Period	t _{RP}			140	210	280	ms
Reset Delay	t _{RD}	V _{CC} falling at 10mV/µs from (V _{TH} + 100mV) to (V _{TH} - 100mV)			30		μs
	ower-Fail Propagation Delay t_{PFD} t_{PFD} V_{PFL} falling at 10mV/µs from V _{TH} to (V _{TH} - 50mV) V _{CC} falling at 10mV/µs from (V _{TH} + 100mV) to (V _{TH} - 100mV)		µs from V _{TH} to		30		
Power-Fail Propagation Delay				5		μs	
	VIL			0.3 × V _{CC}		V	
MR Input Voltage	VIH	0.7 × V _{CC}					
MR Minimum Input Pulse				1			μs
MR Glitch Rejection					100		ns
MR to RESET Delay	t _{MRD}				200		ns
MR Pullup Resistance		$\overline{\text{MR}}$ to V _{CC}		10	20	50	kΩ
Output Low Voltage		$V_{CC} = 5V, I_{SINK} = 2mA$				0.3	
	VOL	V _{CC} = 2.5V, I _{SINK} = 1.2mA				0.3	V
		V _{CC} = 1V, I _{SINK} = 50µA (Note 2)				0.3	
Output High Voltage	V _{OH}	$V_{CC} \ge 2.0V$, $I_{SOURCE} = 6mA$ (min), \overline{RESET} , $\overline{PFO}_$ unasserted		0.8 x V _C	C		V
Output High Source Current	IOH	V _{CC} ≥ 2.0V, RESET a		10		μA	
-		i		<u>i</u>			

Note 1: 100% production tested at $T_A = +25^{\circ}C$. Overtemperature limits guaranteed by design.

Note 2: Condition at $V_{CC} = 1V$ is guaranteed only from $T_A = 0^{\circ}C$ to $+70^{\circ}C$.

Note 3: Monitored voltage 5V/3.3V is also the device supply. In the typical condition, supply current splits as follows: 25µA for the resistor-divider, and the rest for other circuitry.

(V_{CC} = 5V, T_A = +25°C, unless otherwise noted.)

Typical Operating Characteristics



MAX6709/MAX6714

Typical Operating Characteristics (continued)

(V_{CC} = 5V, T_A = +25°C, unless otherwise noted.)





Pin Description

Р	IN								
MAX6709	MAX6714	NAME	FUNCTION						
1	_	IN1	Input Voltage 1. See Selector Guide for monitored voltages.						
2	_	IN2	Input Voltage 2. See Selector Guide for monitored voltages.						
3	_	IN3	Input Voltage 3. See Selector Guide for monitored voltages.						
4	_	IN4	Input Voltage 4. See Selector Guide for monitored voltages.						
5	5	GND	Ground						
6		PWRGD4	Output 4. PWRGD4 asserts low when IN4 falls below its threshold voltage. PWRGD4 is open drain with a 10 μ A internal pullup current source to V _{CC} .						
7	_	PWRGD3	Output 3. PWRGD3 asserts low when IN3 falls below its threshold voltage. PWRGD3 is open drain with a 10 μ A internal pullup current source to V _{CC} .						
8	—	PWRGD2	Output 2. PWRGD2 asserts low when IN2 falls below its threshold voltage. PWRGD2 is open drain with a 10 μ A internal pullup current source to V _{CC} .						
9	_	PWRGD1	Output 1. PWRGD1 asserts low when IN1 falls below its threshold voltage. PWRGD1 is open drain with a 10 μ A internal pullup current source to V _{CC} .						
10	10	Vcc	Power-Supply Input. Connect V _{CC} to a 2.0V to 5.5V supply. An undervoltage lockout circuit forces all PWRGD_ outputs low when V _{CC} drops below the minimum operating voltage. V _{CC} is not a monitored voltage for the MAX6709. For the MAX6714, RESET asserts low when V _{CC} drops below its threshold.						
	1	MR	Manual Reset Input. Force $\overline{\text{MR}}$ low to assert the $\overline{\text{RESET}}$ output. $\overline{\text{RESET}}$ remains asserted for the reset timeout period after $\overline{\text{MR}}$ goes high. $\overline{\text{MR}}$ is internally pulled up to V _{CC} .						
_	2	PFI1	Power-Fail Input 1. Input to noninverting input of the power-fail comparator. PFI1 is compared to an internal 0.62V reference. Use an external resistor-divider network to adjust the monitor threshold.						
_	3	PFI2	Power-Fail Input 2. Input to noninverting input of the power-fail comparator. PFI2 is compared to an internal 0.62V reference. Use an external resistor-divider network to adjust the monitor threshold.						
_	4	PFI3	Power-Fail Input 3. Input to noninverting input of the power-fail comparator. PFI3 is compared to an internal 0.62V reference. Use an external resistor-divider network to adjust the monitor threshold.						
_	6	PFO3	Power-Fail Output 3. $\overline{\text{PFO3}}$ is an active-low, open-drain output with a 10µA internal pullup to V _{CC} . $\overline{\text{PFO3}}$ asserts low when PFI3 is below the selected threshold.						
	7	PFO2	Power-Fail Output 2. $\overline{\text{PFO2}}$ is an active-low, open-drain output with a 10µA internal pullup to V _{CC} . $\overline{\text{PFO2}}$ asserts low when PFI2 is below the selected threshold.						
	8	PF01	Power-Fail Output 1. $\overrightarrow{\text{PFO1}}$ is an active-low, open-drain output with a 10µA internal pullup to V _{CC} . $\overrightarrow{\text{PFO1}}$ asserts low when PFI1 is below the selected threshold.						
	9	RESET	Reset Output. RESET is an active-low, open-drain output that asserts low when V_{CC} drops below its preset threshold voltage or when a manual reset is initiated. RESET remains low for the reset timeout period after V_{CC} exceeds the selected reset threshold or \overline{MR} is released.						

Detailed Description

The MAX6709/MAX6714 are low-power, quad voltage monitors designed for multivoltage systems. Preset voltage options for 5.0V, 3.3V, 3.0V, 2.5V, and 1.8V make these quad monitors ideal for applications such as telecommunications, desktop and notebook computers, high-end printers, data storage equipment, and networking equipment.

The MAX6709/MAX6714 have an internally trimmed threshold that minimizes or eliminates the need for external components. The four open-drain outputs have weak (10 μ A) internal pullups to V_{CC}, allowing them to interface easily with other logic devices. The weak internal pullups can be overdriven by external pullups to any voltage from 0 to 5.5V. Internal circuitry prevents current

flow from the external pullup voltage to V_{CC} . The outputs can be wire-ORed for a single power-good signal.

The MAX6709 quad voltage monitor includes an accurate reference, four precision comparators, and a series of internally trimmed resistor-divider networks to set the factory-fixed threshold options. The resistor networks scale the specified IN_ reset voltages to match the internal reference/comparator voltage. Adjustable threshold options bypass the internal resistor networks and connect directly to one of the comparator inputs (an external resistor-divider network is required for threshold matching). The MAX6709 monitors power supplies with either 5% or 10% tolerance specifications, depending on the selected version. Additional high-input-impedance comparator options can be used



Figure 1. MAX6709 Functional Diagram

MAX6709/MAX6714



Figure 2. MAX6714 Functional Diagram

as an adjustable voltage monitor, general-purpose comparator, or digital-level translator.

The MAX6714 quad voltage monitor/reset offers one fixed input with internal timing for μ P reset, three power-fail comparators, and a manual reset input (MR). RESET asserts low when V_{CC} drops below its threshold or MR is driven low. Each of the three power-fail inputs connects directly to one of the comparator inputs.

When any input is higher than the threshold level, the output is high. The output goes low as the input drops below the threshold voltage. The undervoltage lockout circuitry remains active and all outputs remain low with V_{CC} down to 1V (Figures 1 and 2).

Applications Information

Hysteresis

When the voltage on one comparator input is at or near the voltage on another input, ambient noise generally causes the comparator output to oscillate. The most common way to eliminate this problem is through hysteresis. When the two comparator input voltages are equal, hysteresis causes one comparator input voltage to move quickly past the other, thus taking the input out of the region where oscillation occurs. Standard comparators require hysteresis to be added through the use of external resistors. The external resistive network usually provides a positive feedback to the input in order to cause a jump in the threshold voltage when output toggles in one direction or the other. These





Figure 3. Quad Undervoltage Detector with LED Indicators



Figure 5. Window Detection

resistors are not required when using the MAX6709/ MAX6714 because hysteresis is built into the device. MAX6709/MAX6714 hysteresis is typically 0.3% of the threshold voltage.

Undervoltage Detection Circuit

The open-drain outputs of the MAX6709/MAX6714 can be configured to detect an undervoltage condition. Figure 3 shows a configuration where an LED turns on when the comparator output is low, indicating an undervoltage condition.

The MAX6709/MAX6714 can also be used in applications such as system supervisory monitoring, multivoltage level detection, and V_{CC} bar graph monitoring (Figure 4).



Figure 4. V_{CC} Bar Graph Monitoring



Figure 6. Output Response of Window Detector Circuit

Window Detection

A window detector circuit uses two auxiliary inputs in a configuration such as the one shown in Figure 5. External resistors R1–R4 set the two threshold voltages (VTH1 and VTH4) of the window detector circuit. Window width (Δ VTH) is the difference between the threshold voltages (Figure 6).

Adjustable Input

The MAX6709 offers several monitor options with adjustable reset thresholds. The MAX6714 has three monitored inputs with adjustable thresholds. The threshold voltage at each adjustable IN_ (PFI_) input is typically 0.62V. To monitor a voltage >0.62V, connect a resistor-divider network to the circuit as shown in Figure 7.

WAX6709/MAX6714



MAX6709/MAX6714



 $R1 = R2 ((V_{INTH} / 0.62V) - 1)$

Figure 7. Setting the Auxiliary Monitor

Or, solved in terms of R1:

Unused Inputs

The unused inputs (except the adjustable) are internally connected to ground through the lower resistors of the threshold-setting resistor pairs. The adjustable input, however, must be connected to ground if unused.

Reset Output

The MAX6714 RESET output asserts low when V_{CC} drops below its specified threshold or MR asserts low and remains low for the reset timeout period (140ms min) after V_{CC} exceeds its threshold and MR deasserts (Figure 8). The output is open drain with a weak (10µA) internal pullup to V_{CC}. For many applications, no external pullup resistor is required to interface with other logic devices. An external pullup resistor to any voltage from 0 to 5.5V overdrives the internal pullup if interfacing to different logic supply voltages (Figure 9). Internal circuitry prevents reverse current flow from the external pullup voltage to V_{CC}.

Manual Reset Input

Many μ P-based products require manual reset capability, allowing the operator, a test technician, or external logic circuitry to initiate a reset. A logic low on MR asserts RESET low. RESET remains asserted while MR is low, and during the reset timeout period (140ms min) after MR returns high. The MR input has an internal 20k Ω pullup resistor to V_{CC}, so it can be left open if unused. Drive MR with TTL or CMOS-logic levels, or with opendrain/collector outputs. Connect a normally open momentary switch from MR to GND to create a manual reset function; external debounce circuitry is not required. If MR is driven from long cables or if the device is used in a noisy environment, connecting a 0.1 μF capacitor from $\overline{\text{MR}}$ to GND provides additional noise immunity.

Reseting the μP from a 2nd Voltage (MAX6714)

The MAX6714 can be configured to assert a reset from a second voltage by connecting the power-fail output to manual reset. As the V_{PFI} falls below its threshold, PFO goes low and asserts the reset output for the reset time-out period after the manual reset input is deasserted. (See *Typical Operating Circuit*.)

Power-Supply Bypassing and Grounding The MAX6709/MAX6714 operate from a single 2.0V to

The MAX6709/MAX6714 operate from a single 2.0V to 5.5V supply. In noisy applications, bypass V_{CC} with a 0.1μ F capacitor as close to V_{CC} as possible.



Figure 8. RESET Output Timing Diagram



Figure 9. Interfacing to Different Logic Supply Voltage



	NOMINAL INPUT VOLTAGE					
PART	IN1 (V)	IN2 (V)	IN3 (V)	IN4 (V)	SUPPLY TOLERANCE (%)	
MAX6709AUB	5	3.3	2.5	Adj*	10	
MAX6709BUB	5	3.3	2.5	Adj*	5	
MAX6709CUB	5	3.3	1.8	Adj*	10	
MAX6709DUB	5	3.3	1.8	Adj*	5	
MAX6709EUB	Adj*	3.3	2.5	1.8	10	
MAX6709FUB	Adj*	3.3	2.5	1.8	5	
MAX6709GUB	5	3.3	Adj*	Adj*	10	
MAX6709HUB	5	3.3	Adj*	Adj*	5	
MAX6709IUB	Adj*	3.3	2.5	Adj*	10	
MAX6709JUB	Adj*	3.3	2.5	Adj*	5	
MAX6709KUB	Adj*	3.3	1.8	Adj*	10	
MAX6709LUB	Adj*	3.3	1.8	Adj*	5	
MAX6709MUB	Adj*	3	Adj*	Adj*	10	
MAX6709NUB	Adj*	3	Adj*	Adj*	5	
MAX6709OUB	Adj*	Adj*	Adj*	Adj*	N/A	

Selector Guide (MAX6709)

_Selector Guide (MAX6714)

	NOMINAL INPUT VOLTAGE					
PART	Vcc (V)	PFI1 (V)	PFI2 (V)	PFI3 (V)	SUPPLY TOLERANCE (%)	
MAX6714AUB	5	Adj*	Adj*	Adj*	10	
MAX6714BUB	5	Adj*	Adj*	Adj*	5	
MAX6714CUB	3.3	Adj*	Adj*	Adj*	10	
MAX6714DUB	3.3	Adj*	Adj*	Adj*	5	

*Adjustable voltage based on 0.62V internal threshold. External threshold voltage can be set using an external resistor-divider.

_Pin Configurations (continued)



*Adjustable voltage based on 0.62V internal threshold. External threshold voltage can be set using an external resistor-divider.

Chip Information

TRANSISTOR COUNT: 1029 PROCESS: BICMOS



Typical Operating Circuit (MAX6714)



Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



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