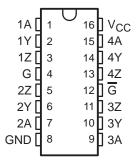
- Meet or Exceed the Requirements of TIA/EIA-422-B and ITU Recommendation V 11
- Low Power, I_{CC} = 100 μA Typ
- Operate From a Single 5-V Supply
- High Speed, t_{PLH} = t_{PHL} = 7 ns Typ
- Low Pulse Distortion, $t_{sk(p)} = 0.5$ ns Typ
- High Output Impedance in Power-Off Conditions
- Improved Replacement for AM26LS31

description

The AM26C31C, AM26C31I, and AM26C31M are four complementary-output line drivers designed to meet the requirements of TIA/EIA-422-B and ITU (formerly CCITT). The 3-state outputs have high-current capability for driving balanced lines, such as twisted-pair or parallel-wire transmission lines, and they provide the high-impedance state in the power-off condition. The enable function is common to all four drivers and offers the choice of an active-high or active-low enable input. BiCMOS circuitry reduces power consumption without sacrificing speed.

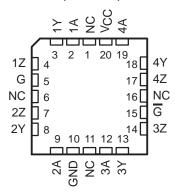
The AM26C31C is characterized for operation from 0°C to 70°C, the AM26C31I is characterized for operation from -40°C to 85°C, and the AM26C31M is characterized for operation from -55°C to 125°C.

AM26C31C, AM26C31I . . . D, DB[†], OR N PACKAGE AM26C31M . . . J OR W PACKAGE (TOP VIEW)



[†]The DB package is only available left-ended taped (order AM26C31IDBLE or AM26C31CDBLE).

AM26C31M...FK PACKAGE (TOP VIEW)



NC - No internal connection

FUNCTION TABLE (each driver)

INPUT	ENA	BLES	OUTPUTS		
Α	G	G	Υ	Z	
Н	Н	Х	Н	L	
L	Н	X	L	Н	
Н	Х	L	Н	L	
L	Х	L	L	Н	
Х	L	Н	Z	Z	

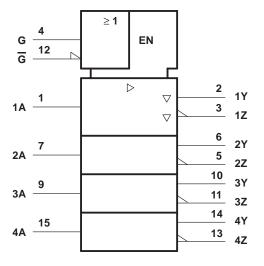
H = high level, L = low level, X = irrelevant, Z = high impedance (off)



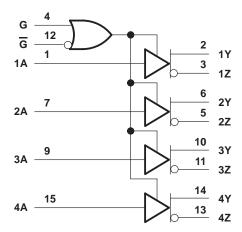
Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



logic symbol†

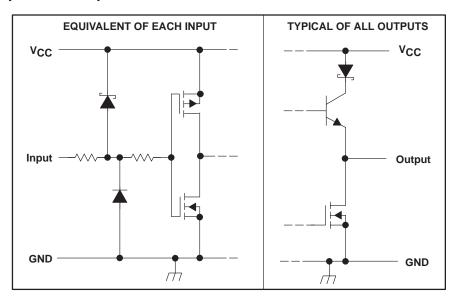


logic diagram (positive logic)



The terminal numbers shown are for the D, DB, J, N, and W packages.

schematics of inputs and outputs



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC} (see Note 1)	–0.5 V to 7 V
Input voltage range, V _I	0.5 V to V _{CC} + 0.5 V
Differential input voltage range, V _{ID}	
Output voltage range, V _O	0.5 V to 7 V
Input or output clamp current, I _{IK} or I _{OK}	±20 mA
Output current, IO	±150 mA
V _{CC} current	200 mA
GND current	–200 mA
Continuous total power dissipation	See Dissipation Rating Table
Storage temperature range, T _{stg}	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values, except differential output voltage (VOD), are with respect to the network ground terminal.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{\scriptsize A}} \le 25^{\circ}\mbox{\scriptsize C}$ POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING	T _A = 125°C POWER RATING
D	950 mW	7.6 mW/°C	608 mW	494 mW	_
DB	781 mW	6.2 mW/°C	502 mW	409 mW	_
N	1150 mW	9.2 mW/°C	736 mW	598 mW	_
FK	1375 mW	11 mW/°C	_	_	275 mW
J	1375 mW	11 mW/°C	_	_	275 mW
W	1000 mW	8.0 mW/°C	_	_	200 mW

recommended operating conditions

		MIN	NOM	MAX	UNIT
Supply voltage, V _{CC}		4.5	5	5.5	V
Differential input voltage, V _{ID}			±7		V
High-level input voltage, VIH		2			V
Low-level input voltage, V _{IL}				0.8	V
High-level output current, IOH				-20	mA
Low-level output current, IOL				20	mA
	AM26C31C	0		70	
Operating free-air temperature, TA	AM26C31I	-40		85	°C
	AM26C31M	-55		125	

AM26C31C, AM26C31I, AM26C31M QUADRUPLE DIFFERENTIAL LINE DRIVERS

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electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		AM26C31C AM26C31I			UNIT	
				MIN	TYP [†]	MAX		
Vон	High-level output voltage	$I_O = -20 \text{ mA}$		2.4	3.4		V	
VOL	Low-level output voltage	$I_O = 20 \text{ mA}$			0.2	0.4	V	
IVodl	Differential output voltage magnitude			2	3.1		V	
Δ V _{OD}	Change in magnitude of differential output voltage [‡]	B. = 100 O	Soo Figure 1			±0.4	V	
Voc	Common-mode output voltage	R_L = 100 Ω, See Figure 1				3	V	
Δ V _{OC}	Change in magnitude of common-mode output voltage‡					±0.4	V	
II	Input current	$V_I = V_{CC}$ or GND				±1	μΑ	
1	Driver output current with power off	$V_{CC} = 0$,	V _O = 6 V			100		
IO(off)		$V_{CC} = 0$,	$V_0 = -0.25 \text{ V}$			-100	μΑ	
los	Driver output short-circuit current	V _O = 0		-30		-150	mA	
1	High impodence off state output ourrest	V _O = 2.5 V				20	μΑ	
loz	High-impedance off-state output current	V _O = 0.5 V				-20	μΑ	
		I _O = 0,	V _I = 0 V or 5 V			100	μΑ	
ICC	Quiescent supply current	I _O = 0, See Note 2	$V_{I} = 2.4 \text{ V or } 0.5 \text{ V},$		1.5	3	mA	
Ci	Input capacitance				6		pF	

 $^{^{\}dagger}$ All typical values are at $V_{CC} = 5 \text{ V}$ and $T_A = 25^{\circ}\text{C}$.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		AM26C31C AM26C31I			UNIT	
				MIN	TYP	MAX		
tPLH	Propagation delay time, low- to high-level output			3	7	12	ns	
^t PHL	Propagation delay time, high- to low-level output	S1 is open,	See Figure 2	3	7	12	ns	
^t sk(p)	Pulse skew time (tpLH - tpHL)				0.5	4	ns	
tr(OD), tf(OD)	Differential output rise and fall times	S1 is open,	See Figure 3		5	10	ns	
^t PZH	Output enable time to high level				10	19	ns	
tPZL	Output enable time to low level	S1 is closed,	Soo Eiguro 4		10	19	ns	
^t PHZ	Output disable time from high level	ST is closed,	31 is ciosed, See Figure	See Figure 4		7	16	ns
tPLZ	Output disable time from low level				7	16	ns	
C _{pd}	Power dissipation capacitance (each driver) (see Note 3)	S1 is open,	See Figure 2		170		pF	

 $^{^{\}dagger}$ All typical values are at VCC = 5 V and TA = 25°C.

NOTE 3: C_{pd} is used to estimate the switching losses according to $P_D = C_{pd} \times V_{CC}^2 \times f$, where f is the switching frequency.



[‡] $\Delta |V_{OD}|$ and $\Delta |V_{OC}|$ are the changes in magnitude of V_{OD} and V_{OC} , respectively, that occur when the input is changed from a high level to a low level.

NOTE 2: This parameter is measured per input. All other inputs are at 0 or 5 V.

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		A	AM26C31M		
			TEST CONDITIONS		TYP†	MAX	UNIT
Vон	High-level output voltage	$I_O = -20 \text{ mA}$		2.2	3.4		V
VOL	Low-level output voltage	$I_O = 20 \text{ mA}$			0.2	0.4	V
IVodl	Differential output voltage magnitude			2	3.1		V
Δ V _{OD}	Change in magnitude of differential output voltage‡	D 400.0	Con Firmure 4			±0.4	V
Voc	Common-mode output voltage	$RL = 100 \Omega$	See Figure 1			3	V
Δ V _{OC}	Change in magnitude of common-mode output voltage‡					±0.4	V
lį	Input current	VI = VCC or (GND			±1	μΑ
lo (m	Driver output current with power off	VCC = 0,	V _O = 6 V			100	μА
^I O(off)	Driver output current with power on	$V_{CC} = 0$,	$V_0 = -0.25 \text{ V}$			-100	μΑ
los	Driver output short-circuit current	$V_O = 0$				-170	mA
1	High-impedance off-state output current	$V_0 = 2.5 \text{ V}$				20	μΑ
loz	riigh-impedance on-state output current	$V_0 = 0.5 \text{ V}$				-20	μΑ
la a	Outconent outpubly output	I _O = 0,	V _I = 0 V or 5 V			100	μΑ
ICC	Quiescent supply current	I _O = 0,	V _I = 2.4 V or 0.5 V, See Note 2			3.2	mA
Ci	Input capacitance				6		pF

[†] All typical values are at $V_{CC} = 5 \text{ V}$ and $T_A = 25^{\circ}\text{C}$.

NOTE 2: This parameter is measured per input. All other inputs are at 0 V or 5 V.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CO	TEST CONDITIONS		AM26C31M		
	PARAWETER				TYP†	MAX	UNIT
^t PLH	Propagation delay time, low- to high-level output				7	12	ns
^t PHL	Propagation delay time, high- to low-level output	S1 is open, See Figure 2			6.5	12	ns
t _{sk(p)}	Pulse skew time (t _{PLH} - t _{PHL})				0.5	4	ns
t _{r(OD)} , t _{f(OD)}	Differential output rise and fall times	S1 is open,	See Figure 3		5	12	ns
^t PZH	Output enable time to high level				10	19	ns
t _{PZL}	Output enable time to low level	Od in alexand One Figure 4			10	19	ns
^t PHZ	Output disable time from high level	ST is closed,	1 is closed, See Figure 4		7	16	ns
tPLZ	Output disable time from low level	1			7	16	ns
C _{pd}	Power dissipation capacitance (each driver) (see Note 3)	S1 is open,	See Figure 2		100		pF

† All typical values are at V_{CC} = 5 V and T_A = 25°C. NOTE 3: C_{pd} is used to estimate the switching losses according to $P_D = C_{pd} \times V_{CC}^2 \times f$, where f is the switching frequency.



[‡] $\Delta |V_{OD}|$ and $\Delta |V_{OC}|$ are the changes in magnitude of V_{OD} and V_{OC} , respectively, that occur when the input is changed from a high level to a low level.

PARAMETER MEASUREMENT INFORMATION

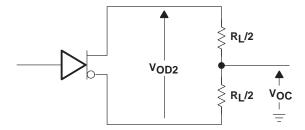
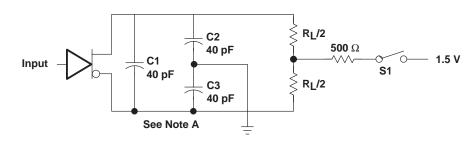
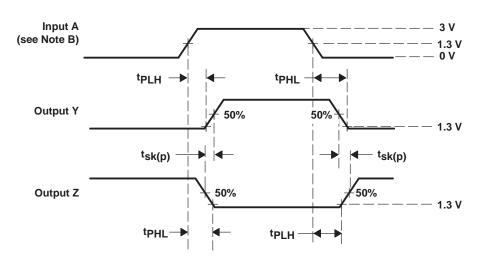


Figure 1. Differential and Common-Mode Output Voltages



TEST CIRCUIT

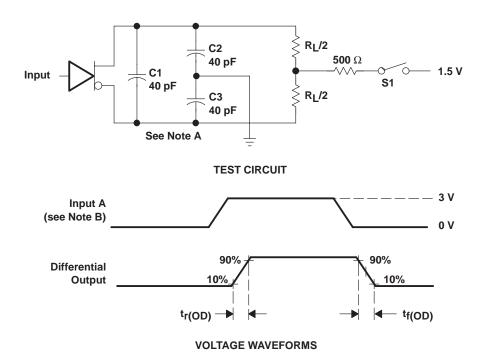


NOTES: A. C1, C2, and C3 include probe and jig capacitance.

B. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, duty cycle \leq 50%, and t_{Γ} $t_{\tilde{f}} \leq$ 6 ns.

Figure 2. Propagation Delay Time and Skew Waveforms and Test Circuit

PARAMETER MEASUREMENT INFORMATION

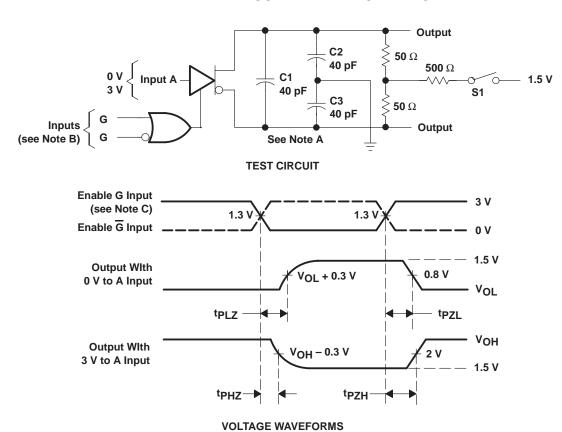


NOTES: A. C1, C2, and C3 include probe and jig capacitance.

B. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, duty cycle \leq 50%, and t_f , $t_f \leq$ 6 ns.

Figure 3. Differential Output Rise and Fall Time Waveforms and Test Circuit

PARAMETER MEASUREMENT INFORMATION



10211102 1111121 01111

NOTES: A. C1, C2, and C3 includes probe and jig capacitance.

- B. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, duty cycle \leq 50%, t_{Γ} < 6 ns, and t_{f} < 6 ns.
- C. Each enable is tested separately.

Figure 4. Output Enable and Disable Time Waveforms and Test Circuit

TYPICAL CHARACTERISTICS

SUPPLY CURRENT ٧s **SWITCHING FREQUENCY** 300 250 I_{CC} - Supply Current - mA 200 150 100 **V_{CC}** = 5 **V** T_A = 25°C See Figure 2 50 S1 Open All Four Channels Switching Simultaneously N Package 0 5 10 15 20 25 30 35 40 0 f - Switching Frequency - MHz

Figure 5

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