



#### **60V SOT223 N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(on)</sub> Max (Ω)	I <sub>D</sub> Max (A) T <sub>A</sub> = +25°C	
60V	0.08 @ V <sub>GS</sub> = 10V	5.3	
	0.15 @ V <sub>GS</sub> = 4.5V	2.8	

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- BLDC Motors
- DC-DC Converters
- Load Switch

#### **Features and Benefits**

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

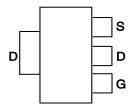
#### **Mechanical Data**

- Case: SOT223
- Case Material: Molded Plastic.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

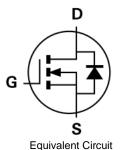




Top View



Pin Out - Top View



### Ordering Information (Note 5)

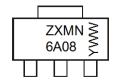
Part Number	Case	Packaging
ZXMN6A08GQTA	SOT223	1000/Tape & Reel
ZXMN6A08GQTC	SOT223	4000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/product-compliance-definitions/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### **Marking Information**

SOT223



ZXMN6A08 =Product Type Marking Code YWW = Date Code Marking Y or Y = Last Digit of Year (ex: 7 = 2017) WW or WW = Week Code (01 to 53)



#### **Absolute Maximum Ratings**

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
	$T_A = +25^{\circ}C \text{ (Note 7)}$		5.3	Α
Continuous Drain Current @ V <sub>GS</sub> = 10V	$T_A = +70^{\circ}C \text{ (Note 7)}$	I <sub>D</sub>	4.2	А
	$T_A = +25^{\circ}C \text{ (Note 6)}$		3.8	Α
Pulsed Drain Current (Note 8)		I <sub>DM</sub>	20	Α
Continuous Source Current (body diode)( Note 7)		Is	2.1	А
Pulsed Source Current (body diode)( Note 8)		I <sub>SM</sub>	20	А
Power Dissipation at T <sub>A</sub> = +25°C (Note 6) Linear Derating Factor		P <sub>D</sub>	2 16	W mW/°C
Power Dissipation at T <sub>A</sub> = +25°C (Note 7) Linear Derating Factor		P <sub>D</sub>	3.9 31	W mW/°C
Linear Derating Factor		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Junction to Ambient (Note 6)	$R_{\theta JA}$	62.5	°C/W
Junction to Ambient (Note 7)	$R_{\theta JA}$	32	°C/W

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	-	=	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	0.5	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	=	=	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1	_	-	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		-	-	0.08	Ω	$V_{GS} = 10V, I_D = 4.8A$	
Static Drain-Source On-State Resistance (Note 9)	R <sub>DS(on)</sub>	-		0.15	Ω	$V_{GS} = 4.5V, I_D = 4.2A$	
Forward Transconductance (Notes 9 &11)	g <sub>fs</sub>	-	6.6	_	S	V <sub>DS</sub> =15V,I <sub>D</sub> =4.8A	
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	C <sub>iss</sub>	-	459	_	pF		
Output Capacitance	Coss	=	44.2	=	pF	$V_{DS} = 40V$ , $V_{GS} = 0V$ ,	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	24.1	-	pF	f = 1MHz	
Turn-On Delay Time (Note 10)	t <sub>d(on)</sub>	-	2.6	-	ns		
Turn-On Rise Time (Note 10)	t <sub>r</sub>	ı	2.1	ı	ns	$V_{DD} = 30V, I_{D}=1.5A$	
Turn-Off Delay Time (Note 10)	t <sub>d(off)</sub>	ı	12.3	ı	ns	$R_G \approx 6.0\Omega, V_{GS} = 10V$	
Turn-Off Fall Time (Note 10)	t <sub>f</sub>	-	4.6		ns		
Gate Charge (Note 10)	$Q_{G}$	-	4.0	-	nC	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 5V I <sub>D</sub> = 1.4A	
Total Gate Charge (Note 10)	Q <sub>G</sub>	-	5.8	-	nC		
Gate-Source Charge (Note 10)	Q <sub>GS</sub>	-	1.4	-	nC	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V	
Gate Drain Charge (Note 10)	$Q_{GD}$	-	1.9	-	nC	I <sub>D</sub> = 1.4A	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (Note 9)	V <sub>SD</sub>	-	0.88	1.2	V	T <sub>J</sub> =+25°C, I <sub>S</sub> = 4A, V <sub>GS</sub> =0V	
Reverse Recovery Time (Note 11)	trr	=	19.2	=	ns	T <sub>J</sub> =+25°C, I <sub>S</sub> = 1.4A,	
Reverse Recovery Charge (Note 11)	Qrr	=	30.3	_	nC	di/dt=100A/µs	

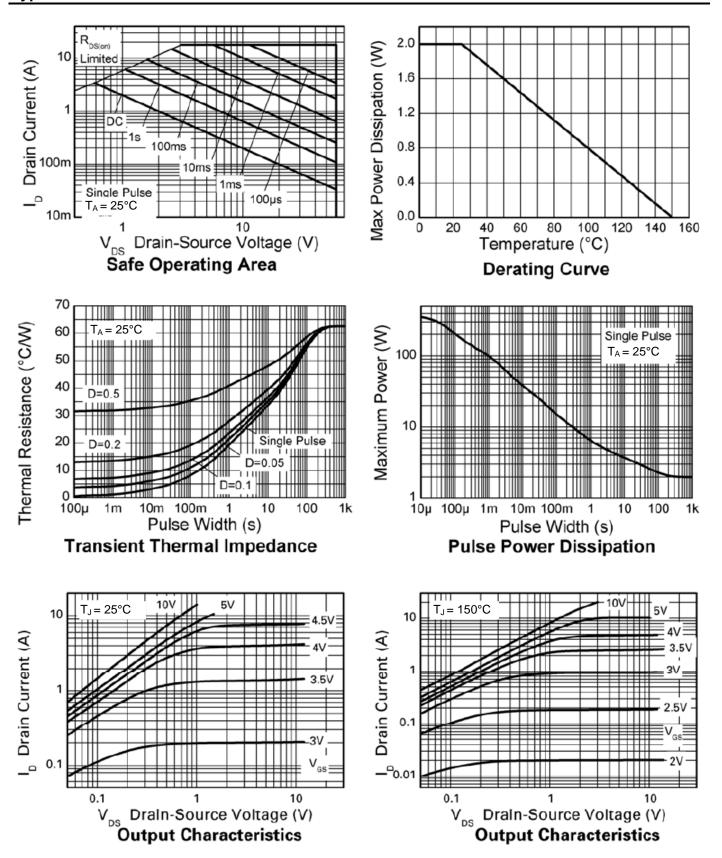
<sup>6.</sup> For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.
7. For a device surface mounted on FR-4 PCB measured at t <= 10s.
8. Repetitive rating - 25mm x 25mm FR-4 PCB, D=0.02, pulse width 300\_s - pulse width limited by maximum junction temperature.

<sup>9.</sup> Measured under pulsed conditions. Pulse width <= 300s; duty cycle <=2%.

Switching characteristics are independent of operating junction temperature.
 For design aid only, not subject to production testing.

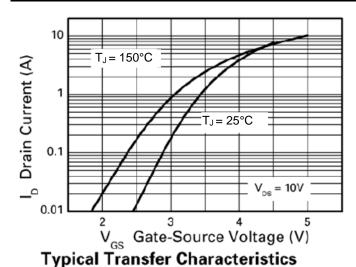


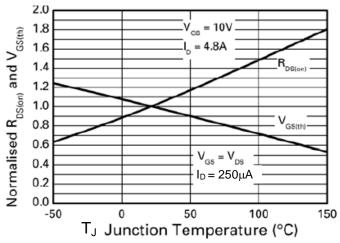
### **Typical Characteristics**



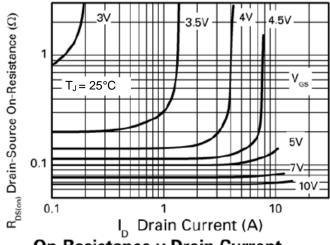


### Typical Characteristics (Cont.)

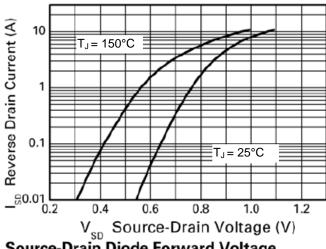




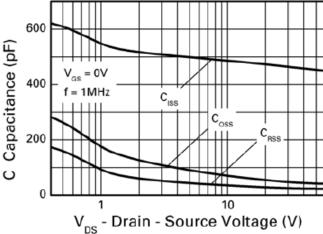
Normalised Curves v Temperature



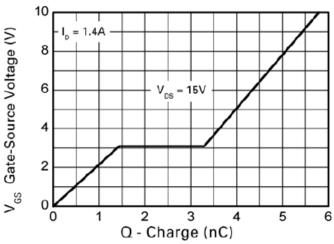




Source-Drain Diode Forward Voltage



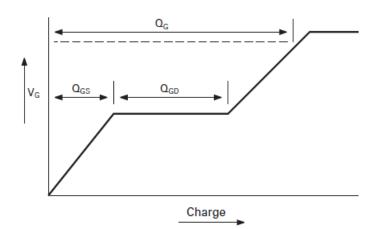
Capacitance v Drain-Source Voltage



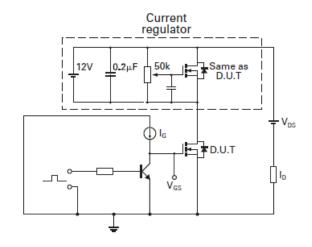
Gate-Source Voltage v Gate Charge



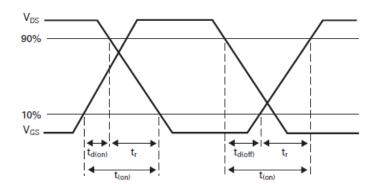
### **Test Circuits**



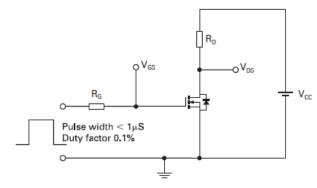
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

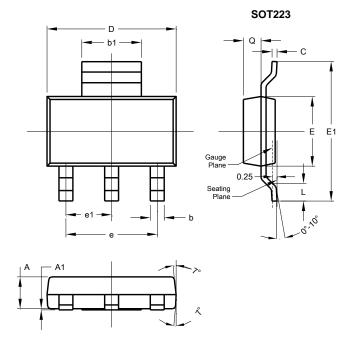


Switching time test circuit



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

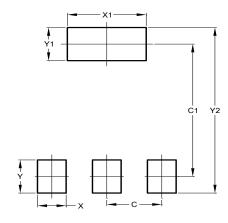


SOT223				
Dim	Min	Max	Тур	
Α	1.55	1.65	1.60	
A1	0.010	0.15	0.05	
b	0.60	0.80	0.70	
b1	2.90	3.10	3.00	
С	0.20	0.30	0.25	
D	6.45	6.55	6.50	
Е	3.45	3.55	3.50	
E1	6.90	7.10	7.00	
е	-	-	4.60	
e1	-	-	2.30	
L	0.85	1.05	0.95	
Q	0.84	0.94	0.89	
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT223**



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8.00



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