EMB2 FHA

General purpose (Dual digital transistor)

AEC-Q101 Qualified

DTr2

| Parameter | DTr1 and DTr2 | |
|----------------------|---------------|--|
| V _{CC} | -50V | |
| I _{C(MAX.)} | -100mA | |
| R ₁ | 47kΩ | |
| R ₂ | 47kΩ | |

Two DTA144E chips in a EMT6 package.
Mounting possible with EMT3 automatic

3)Transistor elements are independent,

4)Mounting cost and area can be cut in half.

● Outline SOT-563 SC-107C

Inner circuit

EMT6

(1) DTr1 GND(Emitter) (6) (5) (4) (3) DTr2 OUT(Collector) (4) DTr2 GND(Emitter) (5) DTr2 IN(Base) (6) DTr1 OUT(Collector) (1) (2) (3)

Features

mounting machines.

eliminating interference.

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|-------------------|-----------------|----------------|-------------------|--------------------|---------------------------------|---------|
| EMB2 FHA | SOT-563 (EMT6) | 1616 | T2R | 180 | 8 | 8000 | B2 |

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

| Parameter | Symbol | Values | Unit |
|------------------------------|--------------------------------|-------------|----------|
| Supply voltage | V _{cc} | -50 | V |
| Input voltage | V _{IN} | -40 to 10 | V |
| Output current | Ι _ο | -30 | mA |
| Collector current | I _{C(MAX)} *1 | -100 | mA |
| Power dissipation | P _D ^{*2*3} | 150 | mW/TOTAL |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

• Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

| Parameter | Symbol | Conditions | Values | | | Unit | |
|----------------------|---------------------|---|--------|------|------|------|--|
| | Symbol Conditions | | Min. | Тур. | Max. | Unit | |
| Inputivaltage | V _{I(off)} | V _{CC} = -5V, I _O = -100µA | - | - | -0.5 | V | |
| Input voltage | V _{I(on)} | V _O = -0.3V, I _O = -2mA | -3.0 | - | - | | |
| Output voltage | V _{O(on)} | I _O = -10mA, I _I = -0.5mA | - | -100 | -300 | mV | |
| Input current | I _I | V ₁ = -5V | - | - | -180 | μA | |
| Output current | I _{O(off)} | $V_{CC} = -50V, V_{I} = 0V$ | - | - | -500 | nA | |
| DC current gain | G | V _O = -5V, I _O = -5mA | 68 | - | - | - | |
| Input resistance | R ₁ | - | 32.9 | 47 | 61.1 | kΩ | |
| Resistance ratio | R_2/R_1 | - | 0.8 | 1.0 | 1.2 | - | |
| Transition frequency | f _T *1 | V _{CE} = -10V, I _E = 5mA, f = 100MHz | - | 250 | - | MHz | |

*1 Characteristics of built-in transistor.

*2 Each terminal mounted on a reference land.

*3 120mW per element must not be exceeded.



•Electrical characteristic curves (T_a = 25°C) <For DTr1 and DTr2 in common>

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

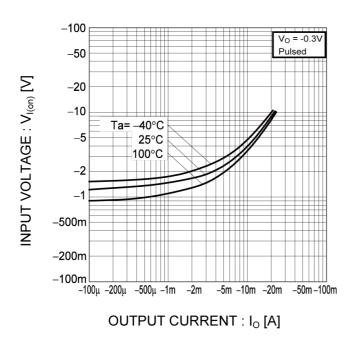


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

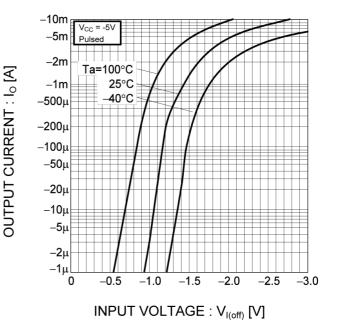


Fig.3 Output Current vs. Output Voltage

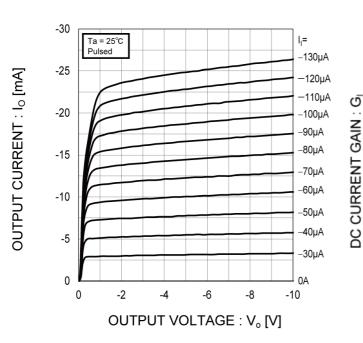
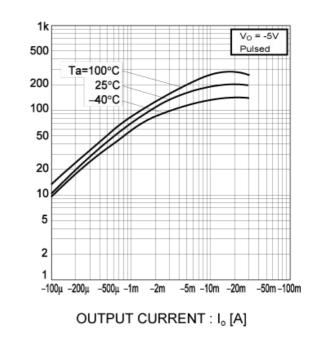


Fig.4 DC Current Gain vs. Output Current





• Electrical characteristic curves ($T_a = 25^{\circ}C$)

<For DTr1 and DTr2 in common>

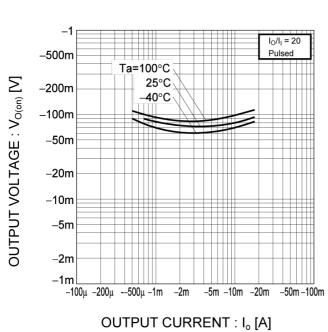
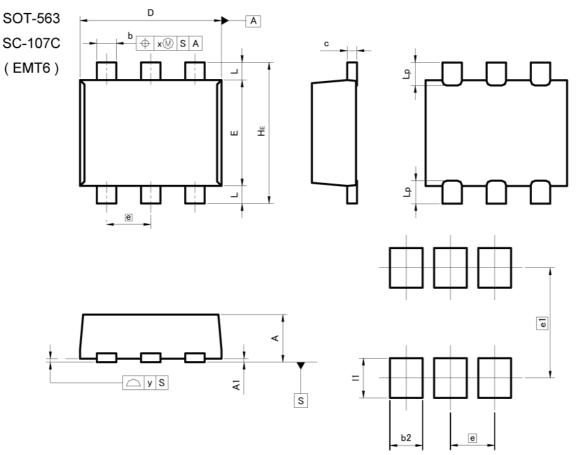


Fig.5 Output Voltage vs. Output Current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | | |
|-----|------------|-------|--------|-------|--|--|
| DIM | MIN | MAX | MIN | MAX | | |
| A | 0.45 | 0.55 | 0.018 | 0.022 | | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | | |
| b | 0.17 | 0.27 | 0.007 | 0.011 | | |
| с | 0.08 | 0.18 | 0.003 | 0.007 | | |
| D | 1.50 | 1.70 | 0.059 | 0.067 | | |
| E | 1.10 | 1.30 | 0.043 | 0.051 | | |
| е | 0. | 50 | 0.020 | | | |
| HE | 1.50 | 1.70 | 0.059 | 0.067 | | |
| L | 0.10 | 0.30 | 0.004 | 0.012 | | |
| Lp | - | 0.35 | - | 0.014 | | |
| x | - | 0.10 | - | 0.004 | | |
| У | | 0.10 | - | 0.004 | | |
| | | | | | | |
| DIM | MILIMETERS | | INCHES | | | |
| DIM | MIN | MAX | MIN | MAX | | |
| b2 | - | 0.37 | - | 0.015 | | |
| e1 | 1. | 25 | 0.049 | | | |
| 11 | - | 0.45 | — | 0.018 | | |

Dimension in mm/inches



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| JAPAN | USA | EU | CHINA |
|---------|--------|------------|---------|
| CLASSII | CLASSⅢ | CLASS II b | CLASSII |
| CLASSⅣ | CLASSI | CLASSⅢ | CLASSII |

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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