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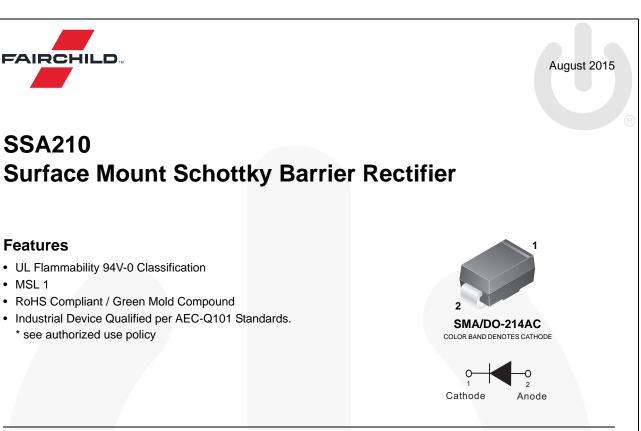


# **ON Semiconductor**®

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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="mailto:www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to <a href="mailto:Fairchild\_questions@onsemi.com">Fairchild\_questions@onsemi.com</a>.

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### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
SSA210	SSA210	DO-214AC (SMA)	Tape and Reel

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Recurrent Peak Reverse Voltage	100	V	
V <sub>RMS</sub>	RMS Voltage	70	V	
V <sub>DC</sub>	DC Blocking Voltage	100	V	
I <sub>F(AV)</sub>	Average Forward Current	2	Α	
I <sub>FSM</sub>	Peak Forward Surge Current: 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	50	A	
Τ <sub>J</sub>	Operating Junction Temperature Range	-55 to +175	°C	
T <sub>STG</sub>	Storage Temperature Range	-55 to +175	°C	

## Thermal Characteristics<sup>(1)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
ΨJL	Typical Thermal Characteristics, Junction-to-Lead <sup>(2)</sup>	30	°C/W
$R_{\thetaJA}$	Typical Thermal Resistance, Junction-to-Ambient	180	°C/W

Note:

1. Per JESD51-3 recommended thermal test board. Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm.

2. Thermocouple soldered at cathode lead.

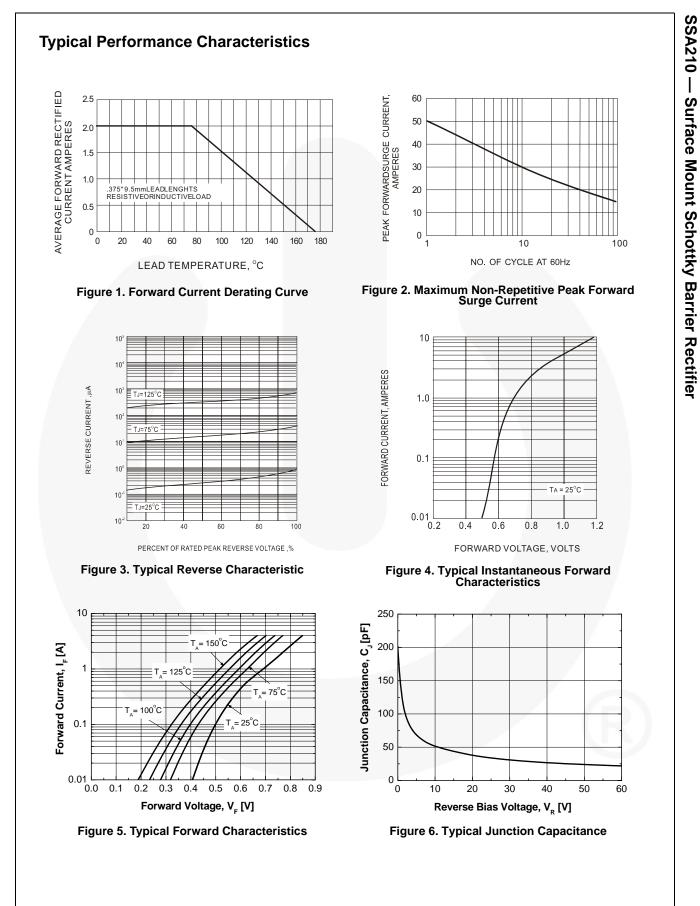
## **Electrical Characteristics**

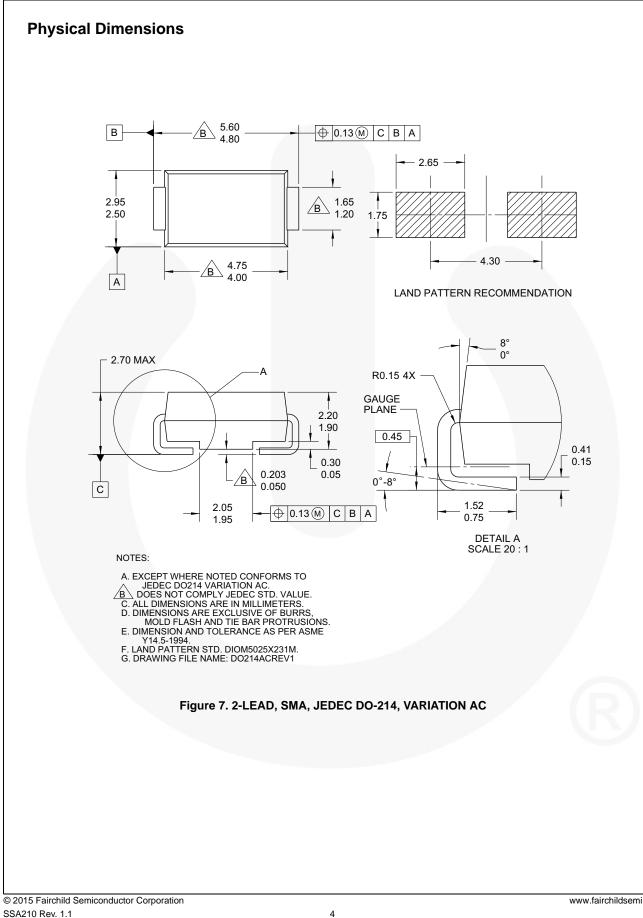
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward Voltage <sup>(3)</sup>	I <sub>F</sub> = 2.0 A			0.8	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 100 V			0.05	mA
		V <sub>R</sub> = 100 V, T <sub>A</sub> = 100°C			20	
T <sub>rr</sub>	Reverse Recovery Time	$I_{\rm F} = 0.5 \text{ A}, I_{\rm R} = 1 \text{ A}, I_{\rm rr} = 0.25 \text{ A}$		8.02		ns

Note:

3. Pulse test with Pulse width =  $300 \ \mu s$ , 1% duty cycle.





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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