

V <sub>DSS</sub>	40V
R <sub>DS(on)</sub> (Max.)	38mΩ
I <sub>D</sub>	±6.0A
P <sub>D</sub>	2.0W

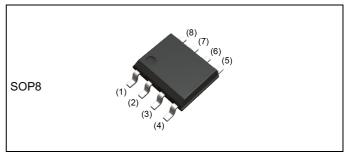
## Features

- 1) Low on resistance
- 2) Small Surface Mount Package
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

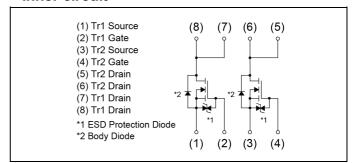
## Application

Switching

## Outline



## •Inner circuit



## Packaging specifications

	Packing	Embossed Tape				
	Reel size (mm)	330				
Туре	Tape width (mm)	12				
	Basic ordering unit (pcs)	2500				
	Taping code	TB				
	Marking	SH8K26				

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	40	V
Continuous drain current	I <sub>D</sub>	±6.0	Α
Pulsed drain current	I <sub>DP</sub> *1	±12	Α
Gate - Source voltage	V <sub>GSS</sub>	±12	V
Avalanche current, single pulse	I <sub>AS</sub> *2	12	Α
Avalanche energy, single pulse	E <sub>AS</sub> *2	1.08	mJ
Davis diagination (total)	P <sub>D</sub> *3	2.0	10/
Power dissipation (total)	P <sub>D</sub> *4	1.4	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## Thermal resistance

Darameter	Cymhol	Values			l leit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registence, junction, ambient (total)	R <sub>thJA</sub> *3	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R <sub>thJA</sub> *4	-	-	89.2	C/VV

## ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Danamatan	0	0	Values			1.1:4	
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	27.3	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub> V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V		-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 12V, V_{DS} = 0V$		-	±10	μA	
Gate threshold voltage $V_{GS(th)}$ $V_{DS} = 10V, I_D = 1mA$		$V_{DS} = 10V, I_{D} = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-4.6	-	mV/°C	
Static drain - source	D *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6.0A	-	27	38	0	
on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6.0A	-	35	50	mΩ	
Gate resistance	$R_G$	f = 1MHz, open drain	-	7.8	-	Ω	
Forward Transfer $ Y_{fs} ^{*5}$ $V_{DS} = 10V$ , $I_D = 6.0A$		V <sub>DS</sub> = 10V, I <sub>D</sub> = 6.0A	2.0	-	-	S	

<sup>\*1</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*2</sup> L  $\simeq$  10 $\mu$ H, V  $_{DD}$  = 20V, R  $_{G}$  = 25 $\Omega$ , Starting T  $_{j}$  = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*3</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*4</sup> Mounted on a Cu board (40×40×0.8mm)

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Doromotor	Cymah al	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Uill	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	280	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	105	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	30	-		
Turn - on delay time	t <sub>d(on)</sub> *5	V <sub>DD</sub> ≈ 20V,V <sub>GS</sub> = 10V	-	8	-		
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = 3.0A	-	15	-		
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L = 6.5\Omega$	-	20	-	ns	
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	3	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol Conditions		Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$		-	2.9	-	
Gate - Source charge	Q <sub>gs</sub> *5	$V_{DD} \approx 20V, I_{D} = 6.0A$ $V_{GS} = 5.0V$	-	1.4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	1.00	-	0.6	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

## <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.6	^	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	12	Α	
Forward voltage	V <sub>SD</sub> *5	V <sub>GS</sub> = 0V, I <sub>S</sub> = 6.0A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

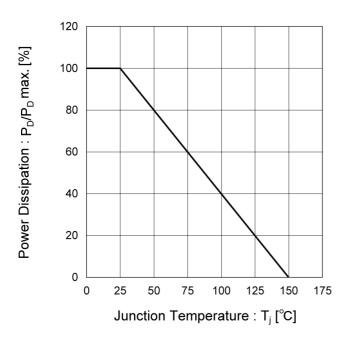
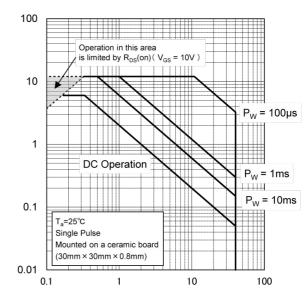


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

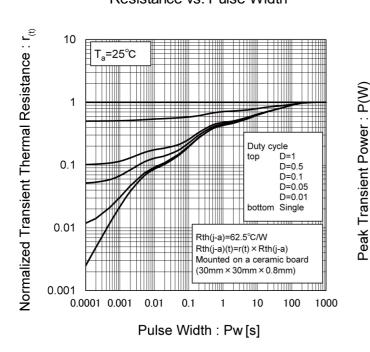
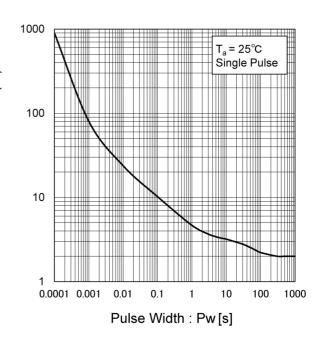


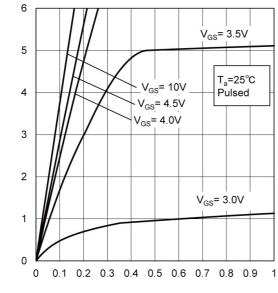
Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I<sub>D</sub> [A]

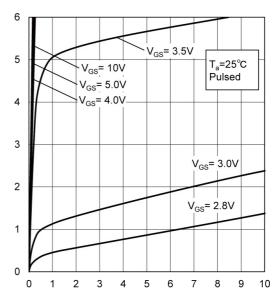
## • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

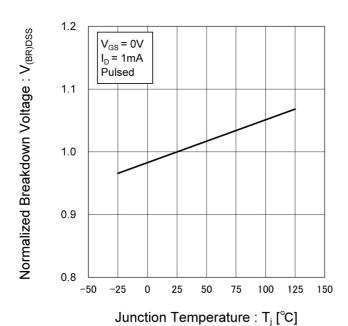


Fig.8 Typical Transfer Characteristics

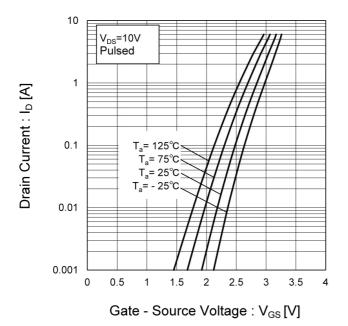


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

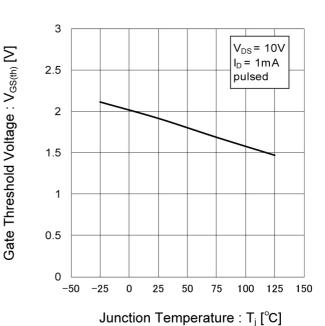
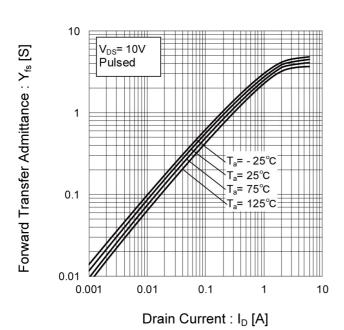


Fig.10 Forward Transfer Admittance vs.
Drain Current



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Fig.11 Drain Current Derating Curve

120 100 **Drain Current Dissipation** 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

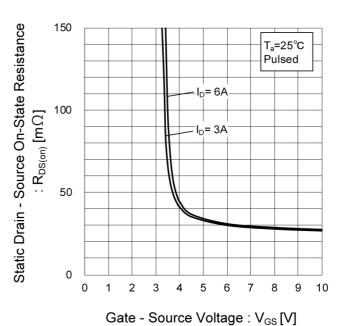


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

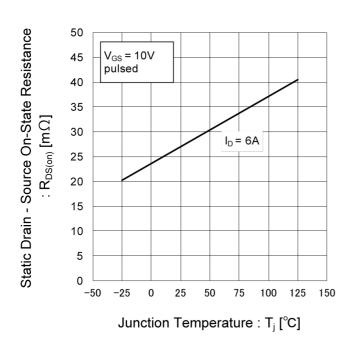


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

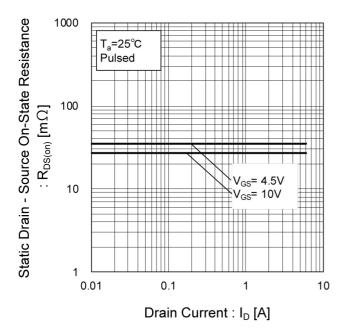


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

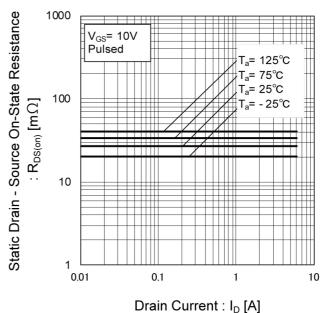


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

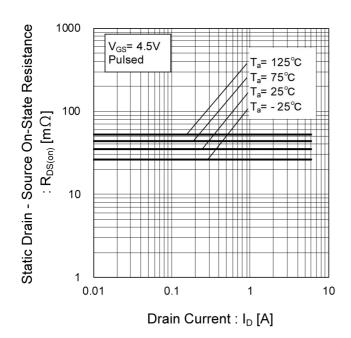


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

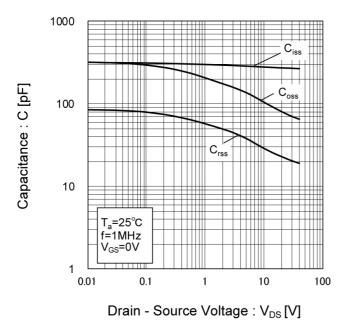


Fig.18 Switching Characteristics

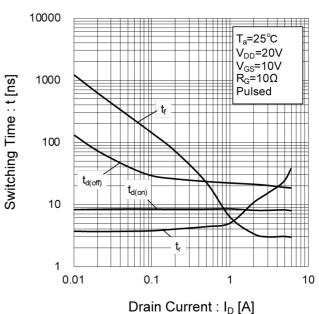


Fig.19 Dynamic Input Characteristics

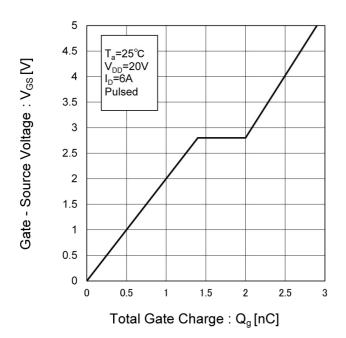
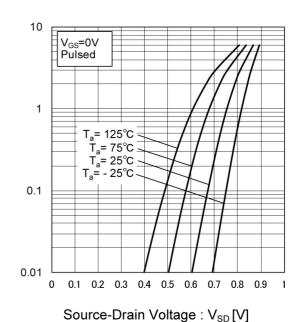


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

## • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

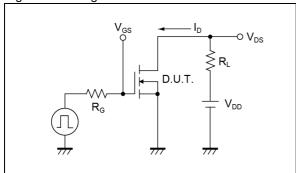


Fig.2-1 Gate Charge Measurement Circuit

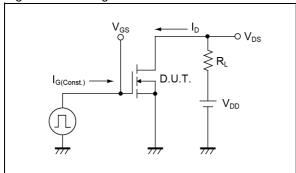


Fig.3-1 Avalanche Measurement Circuit

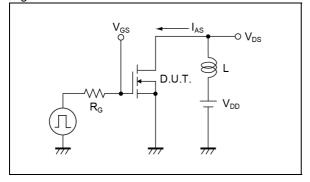


Fig.1-2 Switching Waveforms

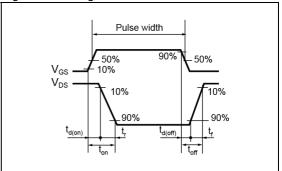


Fig.2-2 Gate Charge Waveform

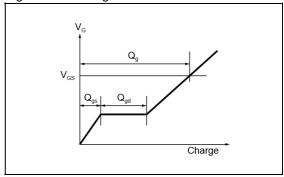
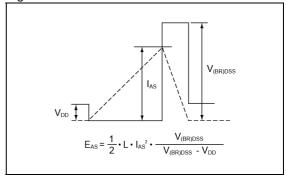
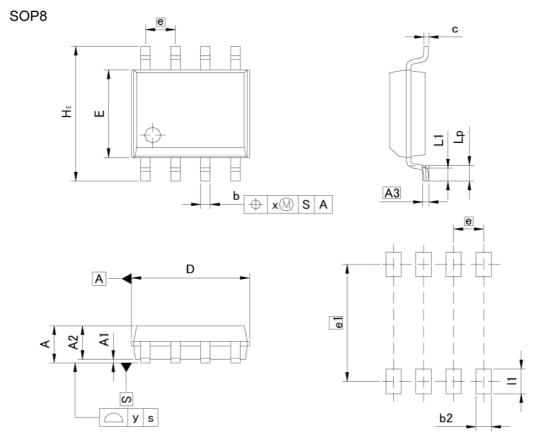


Fig.3-2 Avalanche Waveform



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.75	-	0.069	
A1	0.	15	0.0	06	
A2	1.40	1.60	0.055	0.063	
A3	0.2	25	0.0	10	
b	0.30	0.50	0.012	0.020	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
E	3.75	4.05	0.148	0.159	
е	1.2	27	0.0	50	
HE	5.70	6.30	0.224	0.248	
L1	0.40	0.60	0.016	0.024	
Lp	0.65	0.85	0.026	0.033	
х	0.	15	0.0	06	
у	0.	10	0.0	04	
DIM	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.65		0.026	
e1	5.15		0.2	03	
l1		1.15	<del>-</del> 2	0.045	

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	ОГУООШ	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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