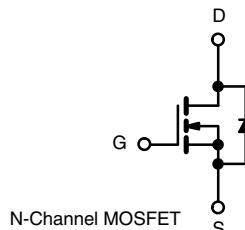
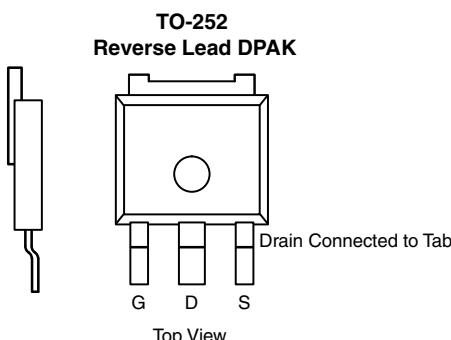


## Automotive N-Channel 40 V (D-S) 175 °C MOSFET

<b>PRODUCT SUMMARY</b>	
$V_{DS}$ (V)	40
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 10$ V	0.0038
$I_D$ (A)	50
Configuration	Single

### FEATURES

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- AEC-Q101 Qualified<sup>d</sup>
- 100 %  $R_g$  and UIS Tested
- Material categorization:  
For definitions of compliance please see  
[www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### ORDERING INFORMATION

Package	TO-252 Reverse Lead DPAK
Lead (Pb)-free and Halogen-free	SQR50N04-3m8-GE3

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$I_D$	50	A
		50	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	50	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	200	
Single Pulse Avalanche Current	$I_{AS}$	62	mJ
Single Pulse Avalanche Energy	$E_{AS}$	192	
Maximum Power Dissipation <sup>b</sup>	$P_D$	136	W
		45	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	50	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	1.1	

#### Notes

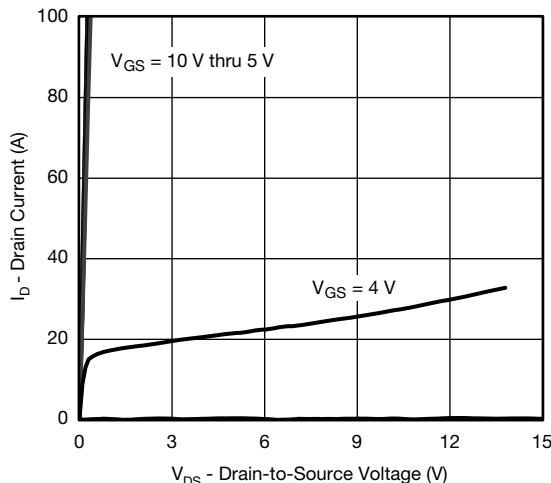
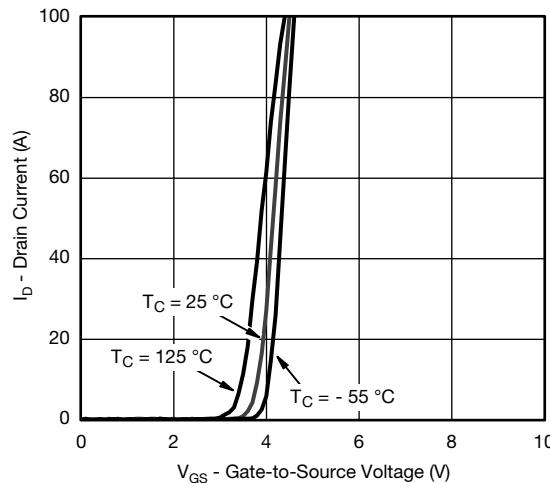
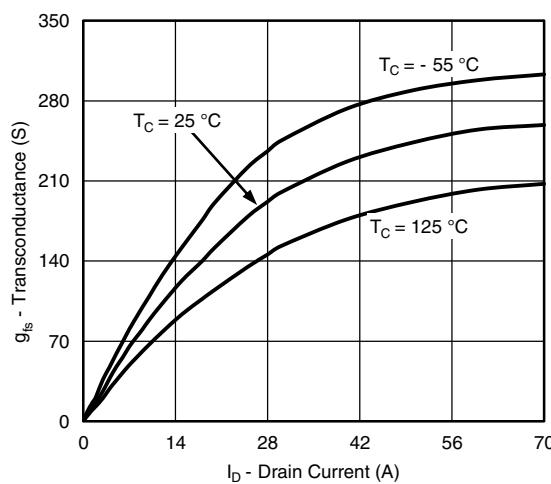
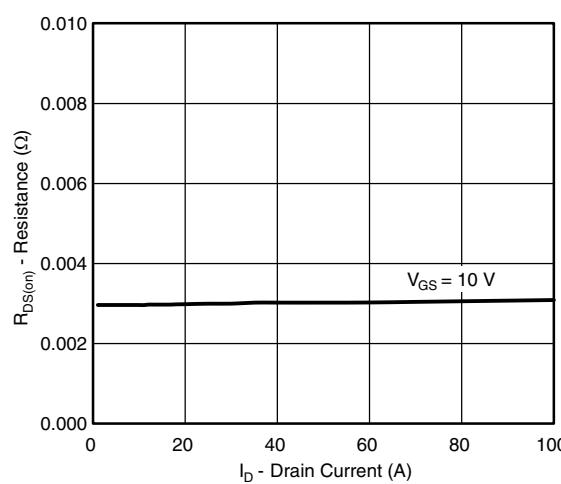
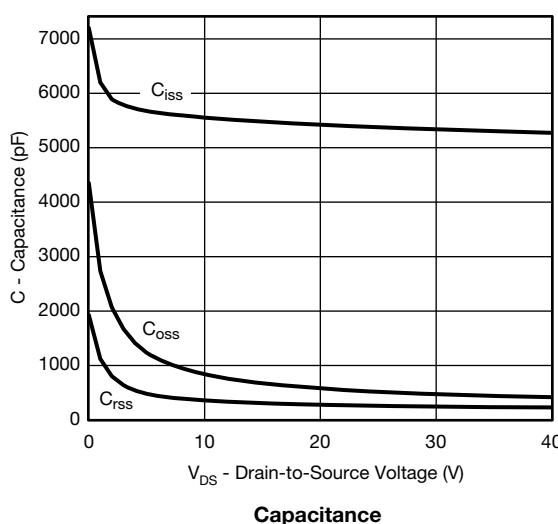
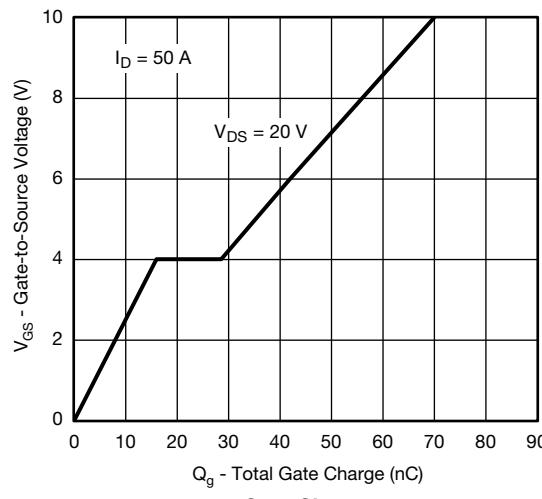
- Package limited.
- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

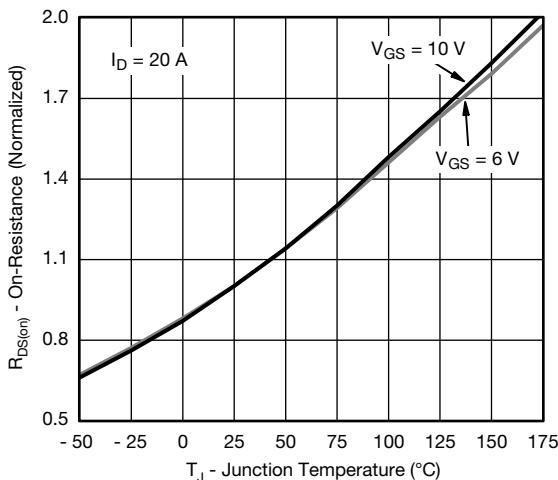
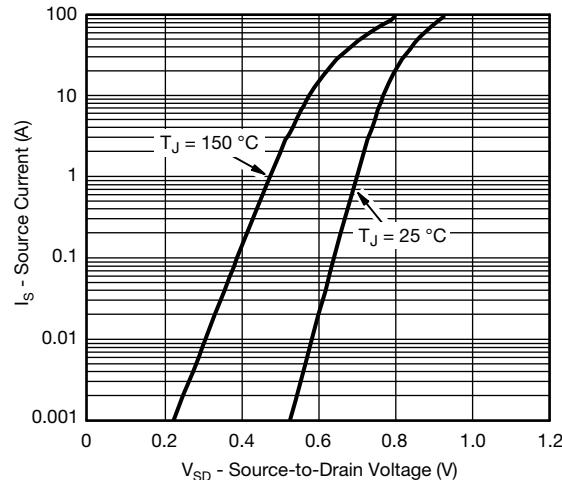
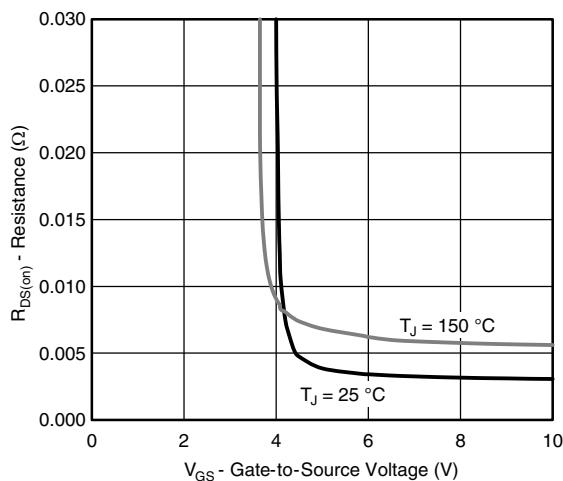
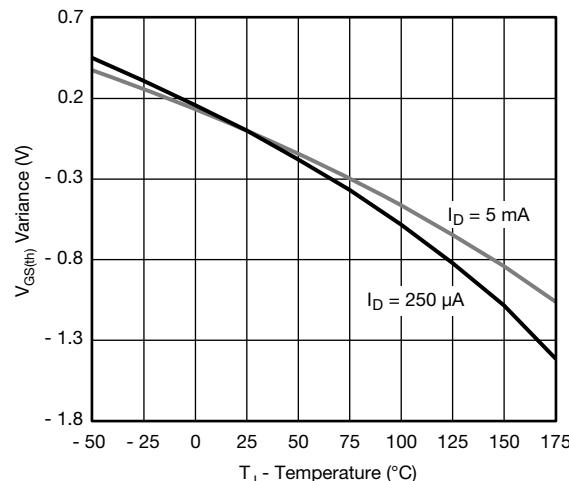
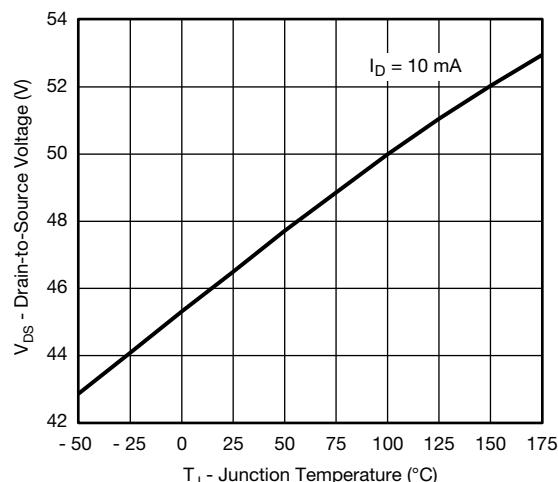
<b>SPECIFICATIONS</b> ( $T_C = 25^\circ\text{C}$ , unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
<b>Static</b>								
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$		40	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$		2.5	3.0	3.5		
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 20 \text{ V}$		-	-	$\pm 100$	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$	-	-	1	$\mu\text{A}$	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$ , $T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$ , $T_J = 175^\circ\text{C}$	-	-	150		
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	50	-	-	A	
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$	$I_D = 20 \text{ A}$	-	0.0030	0.0038	$\Omega$	
		$V_{GS} = 10 \text{ V}$	$I_D = 20 \text{ A}$ , $T_J = 125^\circ\text{C}$	-	-	0.0064		
		$V_{GS} = 10 \text{ V}$	$I_D = 20 \text{ A}$ , $T_J = 175^\circ\text{C}$	-	-	0.0076		
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}$ , $I_D = 15 \text{ A}$		-	120	-	S	
<b>Dynamic<sup>b</sup></b>								
Input Capacitance	$C_{iss}$	$V_{GS} = 0 \text{ V}$	$V_{DS} = 25 \text{ V}$ , $f = 1 \text{ MHz}$	-	5360	6700	pF	
Output Capacitance	$C_{oss}$			-	500	627		
Reverse Transfer Capacitance	$C_{rss}$			-	250	310		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{GS} = 10 \text{ V}$	$V_{DS} = 20 \text{ V}$ , $I_D = 50 \text{ A}$	-	70	105	nC	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			-	16	-		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			-	13	-		
Gate Resistance	$R_g$	$f = 1 \text{ MHz}$		0.9	1.9	2.9	$\Omega$	
Turn-On Delay Time <sup>c</sup>	$t_{d(\text{on})}$	$V_{DD} = 20 \text{ V}$ , $R_L = 0.4 \Omega$ $I_D \approx 50 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 1 \Omega$		-	11	16	ns	
Rise Time <sup>c</sup>	$t_r$			-	5	8		
Turn-Off Delay Time <sup>c</sup>	$t_{d(\text{off})}$			-	34	51		
Fall Time <sup>c</sup>	$t_f$			-	9	14		
<b>Source-Drain Diode Ratings and Characteristics<sup>b</sup></b>								
Pulsed Current <sup>a</sup>	$I_{SM}$			-	-	200	A	
Forward Voltage	$V_{SD}$	$I_F = 30 \text{ A}$ , $V_{GS} = 0 \text{ V}$		-	0.9	1.5	V	

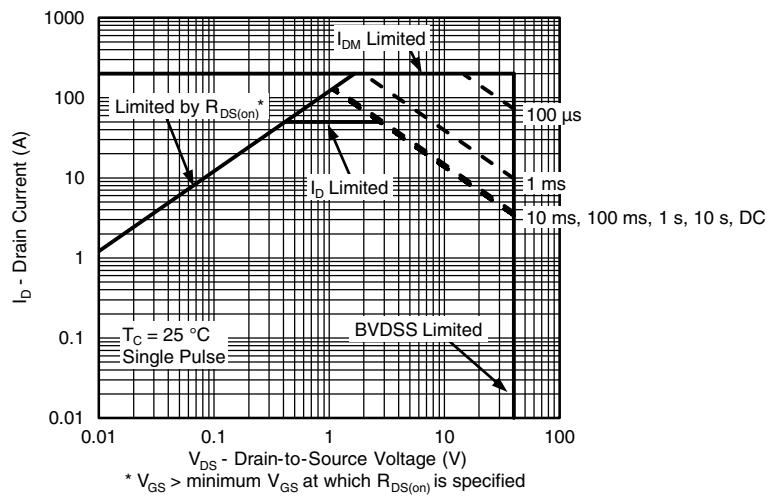
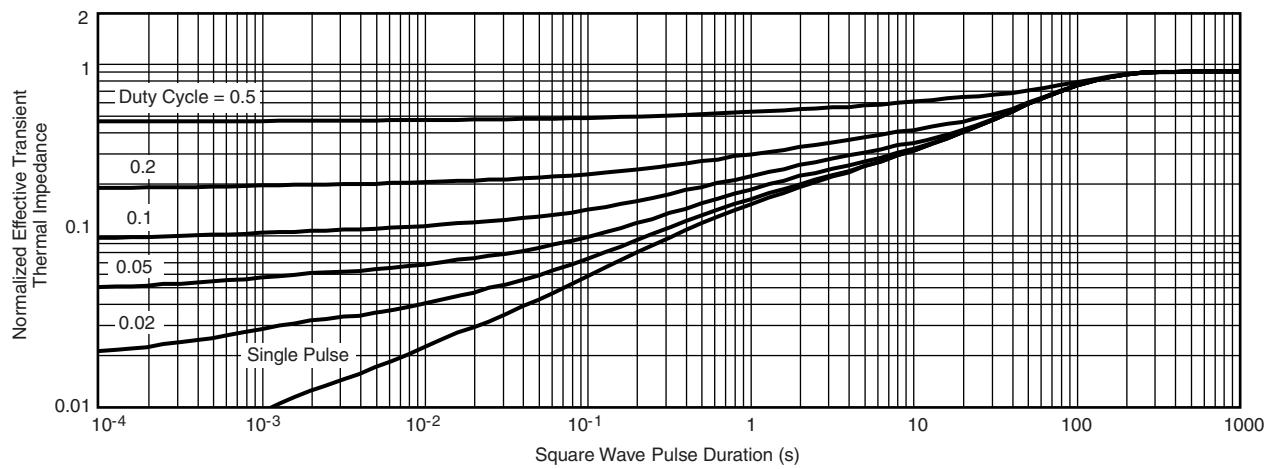
**Notes**

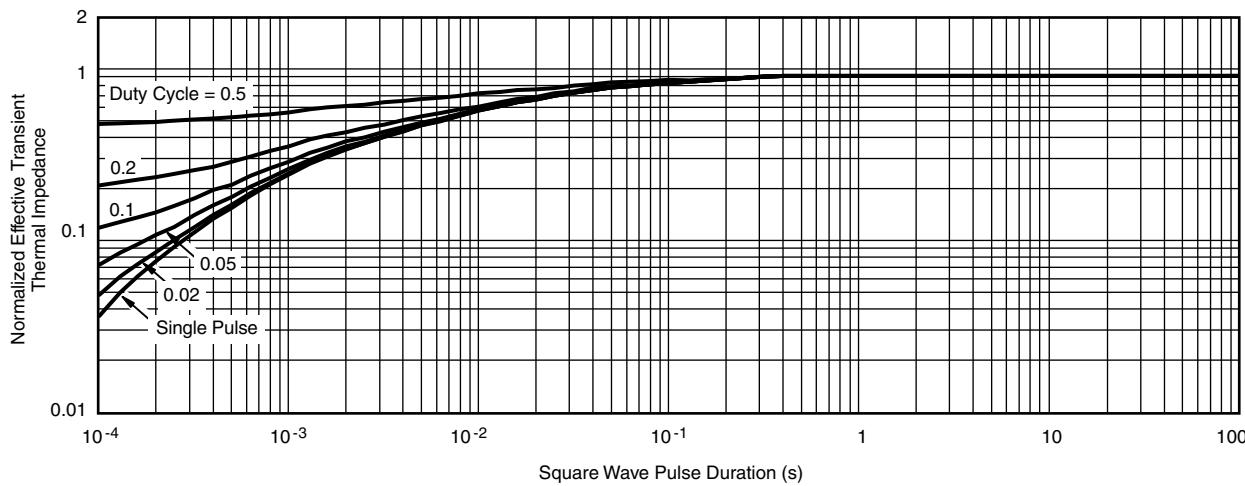
- a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

**Output Characteristics**

**Transfer Characteristics**

**Transconductance**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**TYPICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

**On-Resistance vs. Junction Temperature**

**Source Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Drain Source Breakdown vs. Junction Temperature**

**THERMAL RATINGS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

**Safe Operating Area**

**Normalized Thermal Transient Impedance, Junction-to-Ambient**

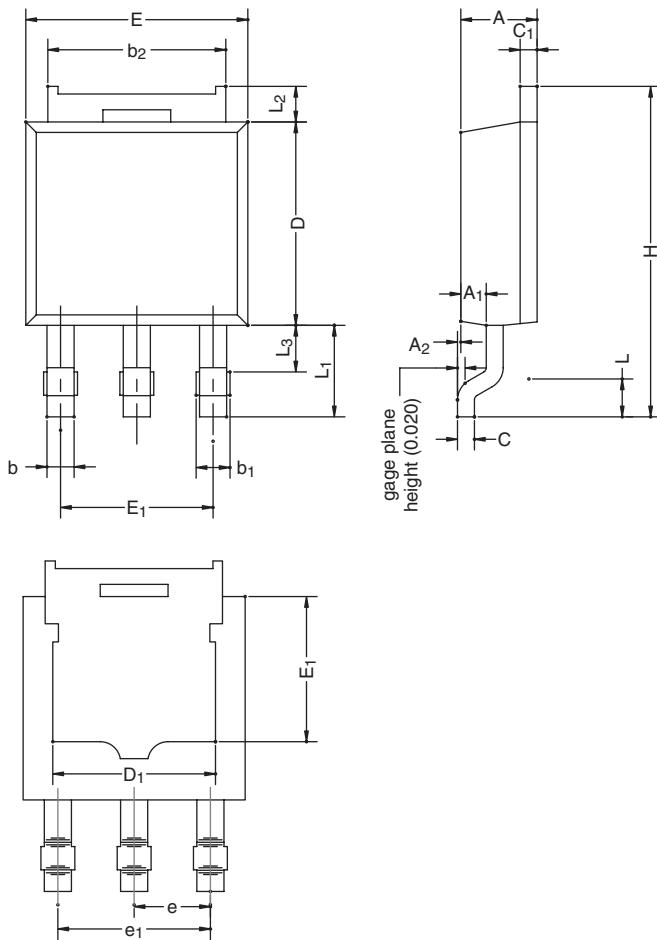
**THERMAL RATINGS ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)**

**Normalized Thermal Transient Impedance, Junction-to-Case**
**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25^\circ\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Case ( $25^\circ\text{C}$ )

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?62783](http://www.vishay.com/ppg?62783).

### TO-252 REVERSE LEAD CASE OUTLINE



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.23	2.33	0.088	0.092
A <sub>1</sub>	0.64	0.89	0.025	0.035
A <sub>2</sub>	0.03	0.23	0.001	0.009
b	0.71	0.88	0.028	0.035
b <sub>1</sub>	0.76	1.14	0.030	0.045
b <sub>2</sub>	5.23	5.44	0.206	0.214
C	0.46	0.58	0.018	0.023
C <sub>1</sub>	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
D <sub>1</sub>	4.49	5.00	0.177	0.197
E	6.48	6.73	0.255	0.265
E <sub>1</sub>	4.32	-	0.170	-
e	2.28 BSC		0.090 BSC	
e <sub>1</sub>	4.57 BSC		0.180 BSC	
H	9.65	10.41	0.380	0.410
L	1.40	1.78	0.055	0.070
L <sub>1</sub>	2.74 BSC		0.108 BSC	
L <sub>2</sub>	0.89	1.27	0.035	0.050
L <sub>3</sub>	1.15	1.52	0.040	0.060

ECN: T-08706-Rev. B, 29-Sep-08  
DWG: 5894

#### Note

Dimension L<sub>3</sub> for reference only.



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**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

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- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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Факс: 8 (812) 320-02-42

Электронная почта: [org@eplast1.ru](mailto:org@eplast1.ru)

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