



LMV321, LMV358, LMV324

Low cost, low power, input/output rail-to-rail operational amplifiers

Datasheet — production data

Features

- Operating range from $V_{CC} = 2.7$ to 6 V
- Rail-to-rail input and output
- Extended V_{icm} ($V_{DD} - 0.2$ V to $V_{CC} + 0.2$ V)
- Low supply current (145 μ A)
- Gain bandwidth product (1 MHz)
- ESD tolerance (2 kV)
- Latch-up immunity
- Available in SOT23-5 micropackage, SO-8, TSSOP-8, SO-14, and TSSOP-14 package

Applications

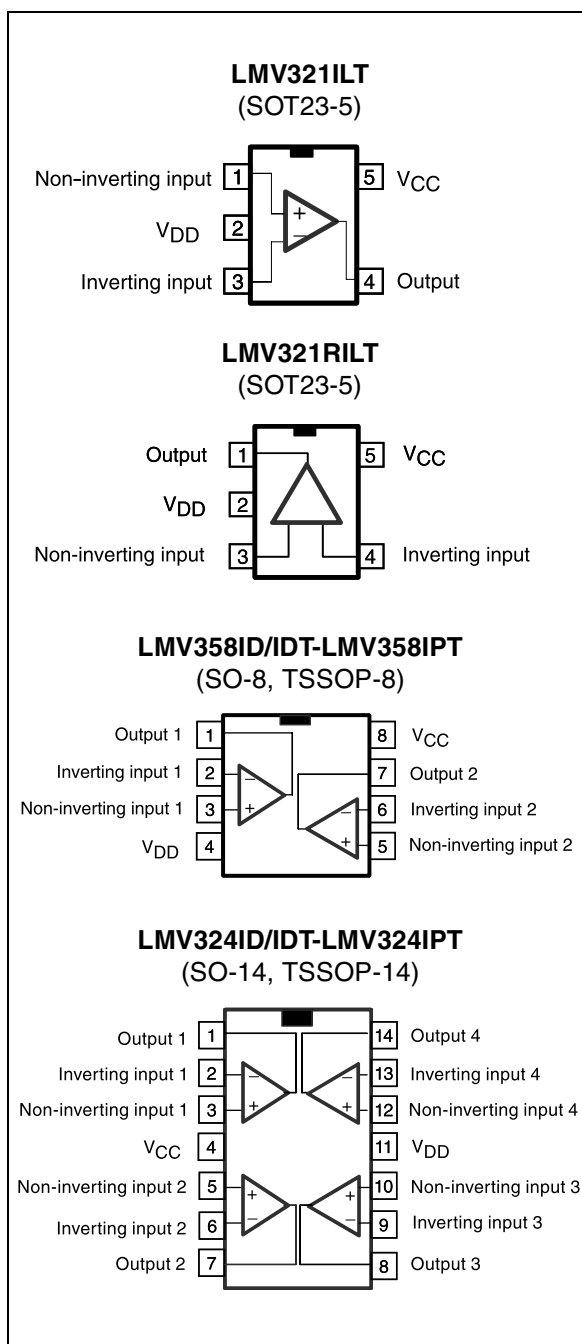
- Two-cell battery powered systems
- Battery powered electronic equipment
- Cordless phones
- Personal medical care (glucose meters)
- Laptops
- PDAs

Description

The LMV321/358/324 family (single, dual and quad) answers the need for low cost, general purpose operational amplifiers. They operate with voltages as low as 2.7 V and feature both input and output rail-to-rail, 145 μ A consumption current and 1 MHz gain bandwidth product (GBP).

With such a low consumption and a sufficient GBP for many applications, these op amps are very well suited for any kind of battery supplied and portable equipment application.

The LMV321 device is housed in the space saving 5-pin SOT23-5 package, which simplifies board design (overall dimensions are 2.8 mm x 2.9 mm). The SOT23-5 has two pinning configurations to answer all application requirements.



1 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage ⁽¹⁾	7	V
V_{id}	Differential input voltage ⁽²⁾	± 1	V
V_{in}	Input voltage	$V_{DD}-0.3$ to $V_{CC}+0.3$	V
T_{oper}	Operating free air temperature range	-40 to + 125	°C
T_{stg}	Storage temperature	-65 to +150	°C
T_j	Maximum junction temperature	150	°C
R_{thja}	Thermal resistance junction-to-ambient ⁽³⁾		°C/W
	SOT23-5	250	
	SO-8	125	
	SO-14	103	
	TSSOP8	120	
	TSSOP14	100	
R_{thjc}	Thermal resistance junction-to-case ⁽³⁾		°C/W
	SOT23-5	81	
	SO-8	40	
	SO-14	31	
	TSSOP8	37	
	TSSOP14	32	
ESD	HBM: human body model ⁽⁴⁾	2	kV
	MM: machine model ⁽⁵⁾	200	V
	CDM: charged device model ⁽⁶⁾	1.5	kV
	Lead temperature (soldering, 10 sec.)	250	°C
	Output short-circuit duration	See ⁽⁷⁾	

1. All voltage values, except differential voltage are with respect to network terminal.
2. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal. If $V_{id} > \pm 1$ V, the maximum input current must not exceed ± 1 mA. In this case ($V_{id} > \pm 1$ V), an input series resistor must be added to limit input current.
3. Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous short-circuits on all amplifiers. All values are typical.
4. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 k Ω resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
5. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
6. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins. No value specified for CDM on SOT23-5L package. The value is given for SO and TSSOP packages.
7. Short-circuits from the output to V_{CC} can cause excessive heating. The maximum output current is approximately 48 mA, independent of the magnitude of V_{CC} . Destructive dissipation can result from simultaneous short-circuits on all amplifiers.

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	2.7 to 6	V
V_{icm}	Common mode input voltage range ⁽¹⁾	$V_{DD} - 0.2$ to $V_{CC} + 0.2$	V
V_{icm}	Common mode input voltage range ⁽²⁾	V_{DD} to V_{CC}	V
T_{oper}	Operating free air temperature range	-40 to + 125	°C

1. At 25 °C, for $2.7 \leq V_{CC} \leq 6$ V, V_{icm} is extended to $V_{DD} - 0.2$ V, $V_{CC} + 0.2$ V.

2. In full temperature range, both rails can be reached when V_{CC} does not exceed 5.5 V.

2 Electrical characteristics

Table 3. Electrical characteristics at $V_{CC} = +2.7\text{ V}$, $V_{DD} = 0\text{ V}$, C_L and R_L connected to $V_{CC}/2$, $T_{amb} = 25\text{ °C}$ (unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{io}	Input offset voltage	$V_{icm} = V_{out} = V_{CC}/2$ $T_{min} \leq T_{amb} \leq T_{max}$		0.1	3 6	mV
ΔV_{io}	Input offset voltage drift			2		$\mu\text{V}/^\circ\text{C}$
I_{io}	Input offset current	$V_{icm} = V_{out} = V_{CC}/2^{(1)}$ $T_{min} \leq T_{amb} \leq T_{max}$		1	9 25	nA
I_{ib}	Input bias current	$V_{icm} = V_{out} = V_{CC}/2^{(1)}$ $T_{min} \leq T_{amb} \leq T_{max}$		10	50 85	nA
CMR	Common mode rejection ratio	$0 \leq V_{icm} \leq V_{CC}$	55	85		dB
SVR	Supply voltage rejection ratio	$V_{icm} = V_{CC}/2$	70	80		dB
A_{vd}	Large signal voltage gain	$V_{out} = 0.5\text{ V to }2.2\text{ V}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$	80 70	100 88		dB
V_{OH}	High level output voltage	$V_{id} = 100\text{ mV}$ $T_{min} \leq T_{amb} \leq T_{max}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$	2.6 2.55	2.65 2.6		V
V_{OL}	Low level output voltage	$V_{id} = -100\text{ mV}$ $T_{min} \leq T_{amb} \leq T_{max}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$		15 50	90 100	mV
I_o	Output current	Output source current $V_{id} = 100\text{ mV}$, $V_O = V_{DD}$ Output sink current $V_{id} = -100\text{ mV}$, $V_O = V_{CC}$	5 5	46 46		mA
I_{CC}	Supply current (per amplifier)	$V_{out} = V_{CC}/2$ $A_{VCL} = 1$, no load $T_{min} \leq T_{amb} \leq T_{max}$		145	200 230	μA
GBP	Gain bandwidth product	$R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$, $f = 100\text{ kHz}$		1		MHz
SR	Slew rate	$R_L = 600\text{ }\Omega$, $C_L = 100\text{ pF}$, $A_V = 1$		0.35		V/ μs
ϕ_m	Phase margin	$R_L = 600\text{ }\Omega$, $C_L = 100\text{ pF}$		44		Degrees
en	Input voltage noise			40		nV/ $\sqrt{\text{Hz}}$
THD	Total harmonic distortion			0.01		%

1. Maximum values include unavoidable inaccuracies of the industrial tests.

Table 4. Electrical characteristics at $V_{CC} = +5\text{ V}$, $V_{DD} = 0\text{ V}$, C_L and R_L connected to $V_{CC}/2$, $T_{amb} = 25\text{ }^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{io}	Input offset voltage	$V_{icm} = V_{out} = V_{CC}/2$ $T_{min} \leq T_{amb} \leq T_{max}$		0.1	3 6	mV
ΔV_{io}	Input offset voltage drift			2		$\mu\text{V}/^{\circ}\text{C}$
I_{io}	Input offset current	$V_{icm} = V_{out} = V_{CC}/2^{(1)}$ $T_{min} \leq T_{amb} \leq T_{max}$		1	9 25	nA
I_{ib}	Input bias current	$V_{icm} = V_{out} = V_{CC}/2^{(1)}$ $T_{min} \leq T_{amb} \leq T_{max}$		16	63 95	nA
CMR	Common mode rejection ratio	$0 \leq V_{icm} \leq V_{CC}$	65	95		dB
SVR	Supply voltage rejection ratio	$V_{icm} = V_{CC}/2$	70	90		dB
A_{vd}	Large signal voltage gain	$V_{out} = 0.5\text{ V to }4.5\text{ V}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$	85 77	97 93		dB
V_{OH}	High level output voltage	$V_{id} = 100\text{ mV}$ $T_{min} \leq T_{amb} \leq T_{max}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$	4.85 4.8	4.95 4.91		V
V_{OL}	Low level output voltage	$V_{id} = -100\text{ mV}$ $T_{min} \leq T_{amb} \leq T_{max}$ $R_L = 10\text{ k}\Omega$ $R_L = 2\text{ k}\Omega$		40 80	180 200	mV
I_o	Output current	Output source current $V_{id} = 100\text{ mV}$, $V_O = V_{DD}$ Output sink current $V_{id} = -100\text{ mV}$, $V_O = V_{CC}$	7 7	48 48		mA
I_{CC}	Supply current (per amplifier)	$V_{out} = V_{CC}/2$ $A_{VCL} = 1$, no load $T_{min} \leq T_{amb} \leq T_{max}$		162	220 250	μA
GBP	Gain bandwidth product	$R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$, $f = 100\text{ kHz}$		1.3		MHz
SR	Slew rate	$R_L = 600\text{ }\Omega$, $C_L = 100\text{ pF}$, $A_V = 1$		0.45		V/ μs
ϕ_m	Phase margin	$R_L = 600\text{ }\Omega$, $C_L = 100\text{ pF}$		48		Degrees
en	Input voltage noise			40		nV/ $\sqrt{\text{Hz}}$
THD	Total harmonic distortion			0.01		%

1. Maximum values include unavoidable inaccuracies of the industrial tests.

Figure 1. Supply current/amplifier vs. supply voltage

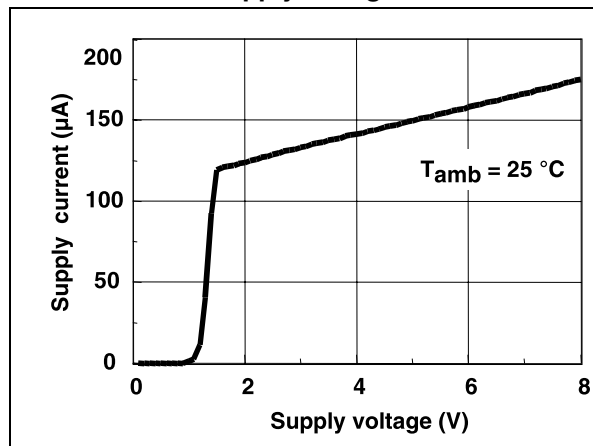
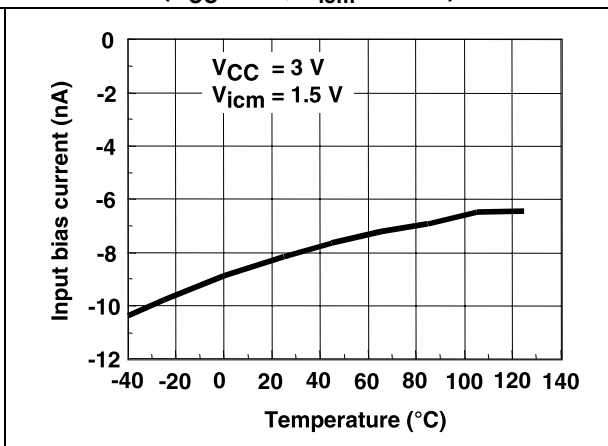
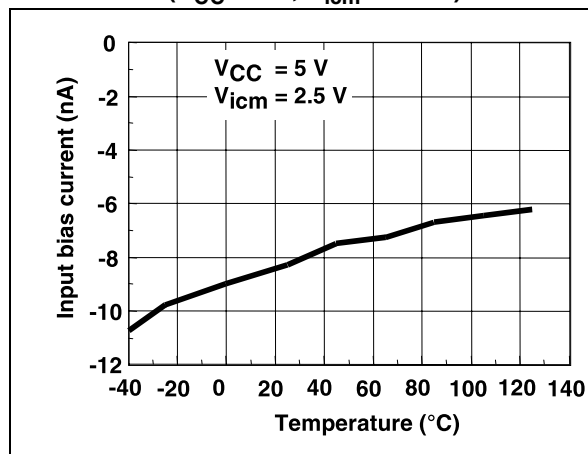
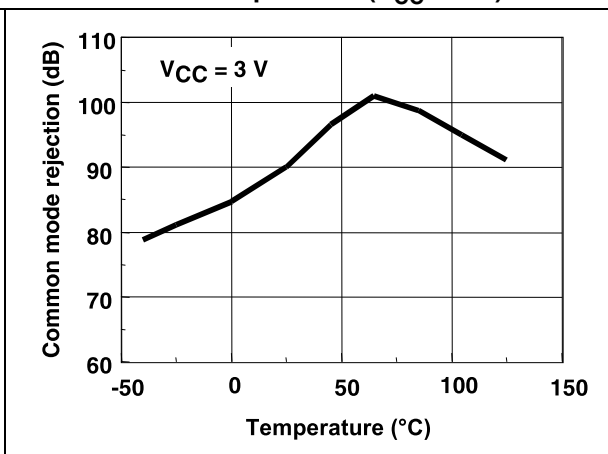
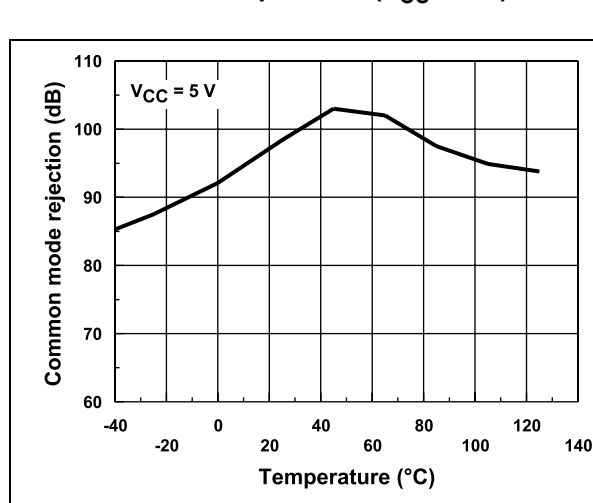
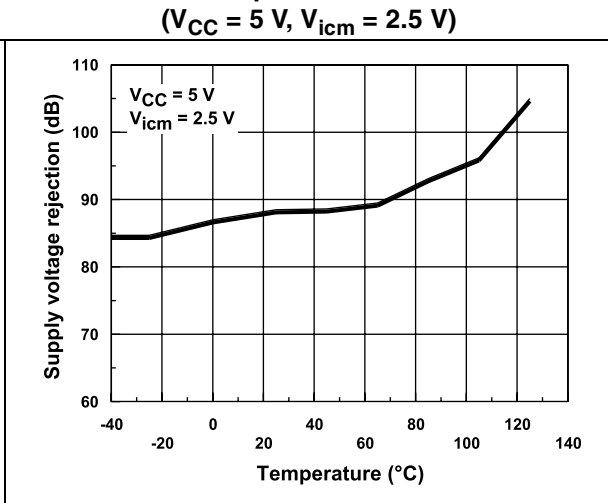
Figure 2. Input bias current vs. temperature ($V_{\text{CC}} = 3\text{ V}$, $V_{\text{icm}} = 1.5\text{ V}$)Figure 3. Input bias current vs. temperature ($V_{\text{CC}} = 5\text{ V}$, $V_{\text{icm}} = 2.5\text{ V}$)Figure 4. Common mode rejection vs. temperature ($V_{\text{CC}} = 3\text{ V}$)Figure 5. Common mode rejection vs. temperature ($V_{\text{CC}} = 5\text{ V}$)Figure 6. Supply voltage rejection vs. temperature ($V_{\text{CC}} = 5\text{ V}$, $V_{\text{icm}} = 2.5\text{ V}$)

Figure 7. Open loop gain vs. temperature
($V_{CC} = 3\text{ V}$, $R_L = 10/2\text{ k}\Omega$)

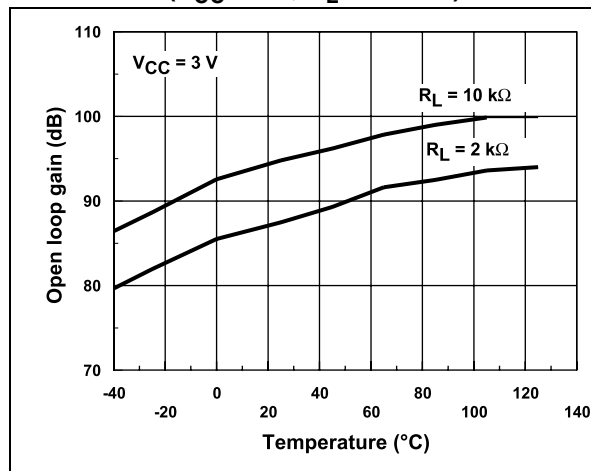


Figure 8. Open loop gain vs. temperature
($V_{CC} = 5\text{ V}$, $R_L = 10/2\text{ k}\Omega$)

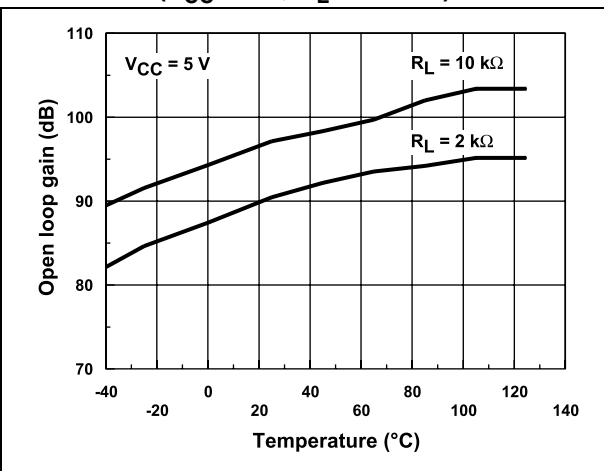


Figure 9. Supply voltage rejection vs. temperature
($V_{CC} = 3\text{ V}$, $V_{icm} = 1.5\text{ V}$)

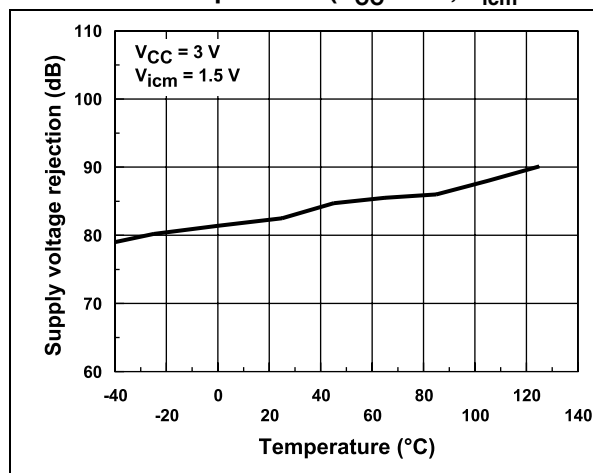


Figure 10. Output current vs. output voltage
($V_{CC} = 3\text{ V}$, $V_{id} = 0.1\text{ V}$, $V_{icm} = 1.5\text{ V}$)

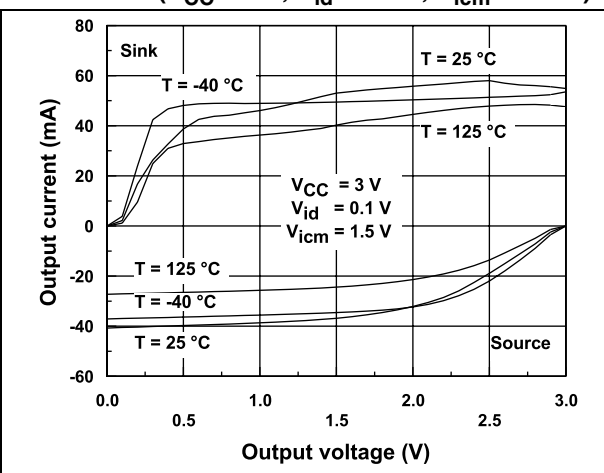


Figure 11. Output current vs. output voltage
($V_{CC} = 5\text{ V}$, $V_{id} = 0.1\text{ V}$, $V_{icm} = 2.5\text{ V}$)

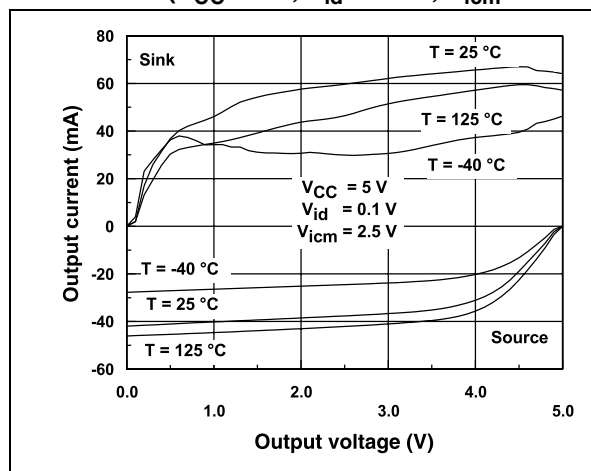
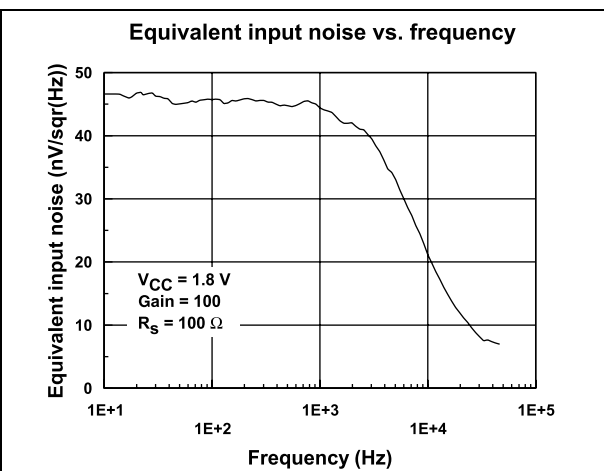


Figure 12. Noise versus frequency



3 **Package information**

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

3.1 SOT23-5 package information

Figure 13. SOT23-5 package outline

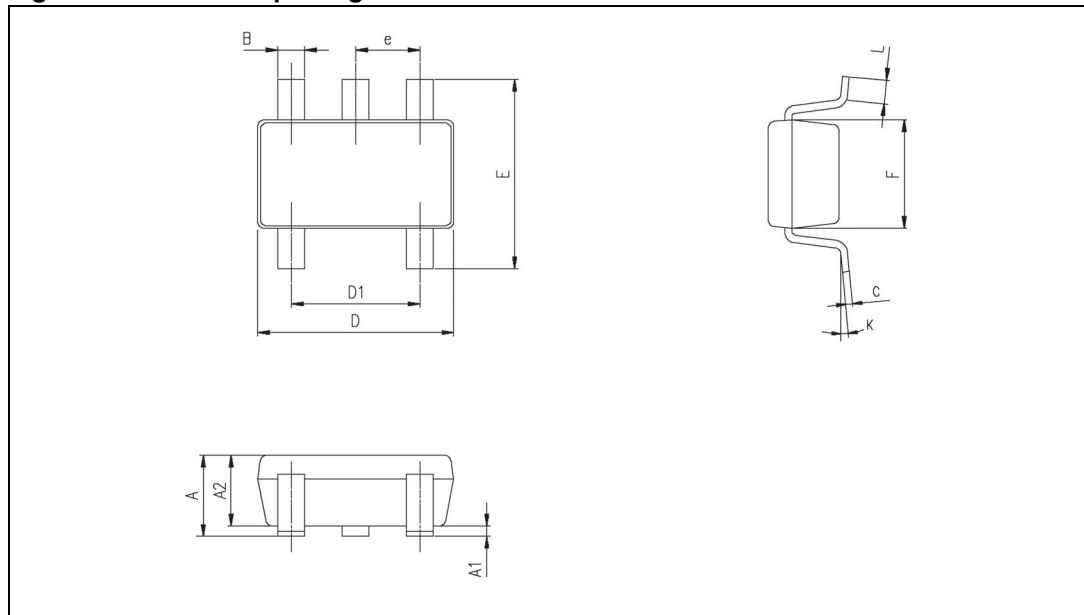


Table 5. SOT23-5 package mechanical data

Symbol	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.90	1.20	1.45	0.035	0.047	0.057
A1			0.15			0.006
A2	0.90	1.05	1.30	0.035	0.041	0.051
B	0.35	0.40	0.50	0.013	0.015	0.019
C	0.09	0.15	0.20	0.003	0.006	0.008
D	2.80	2.90	3.00	0.110	0.114	0.118
D1		1.90			0.075	
e		0.95			0.037	
E	2.60	2.80	3.00	0.102	0.110	0.118
F	1.50	1.60	1.75	0.059	0.063	0.069
L	0.10	0.35	0.60	0.004	0.013	0.023
K	0°		10°			

3.2 SO-8 package information

Figure 14. SO-8 package outline

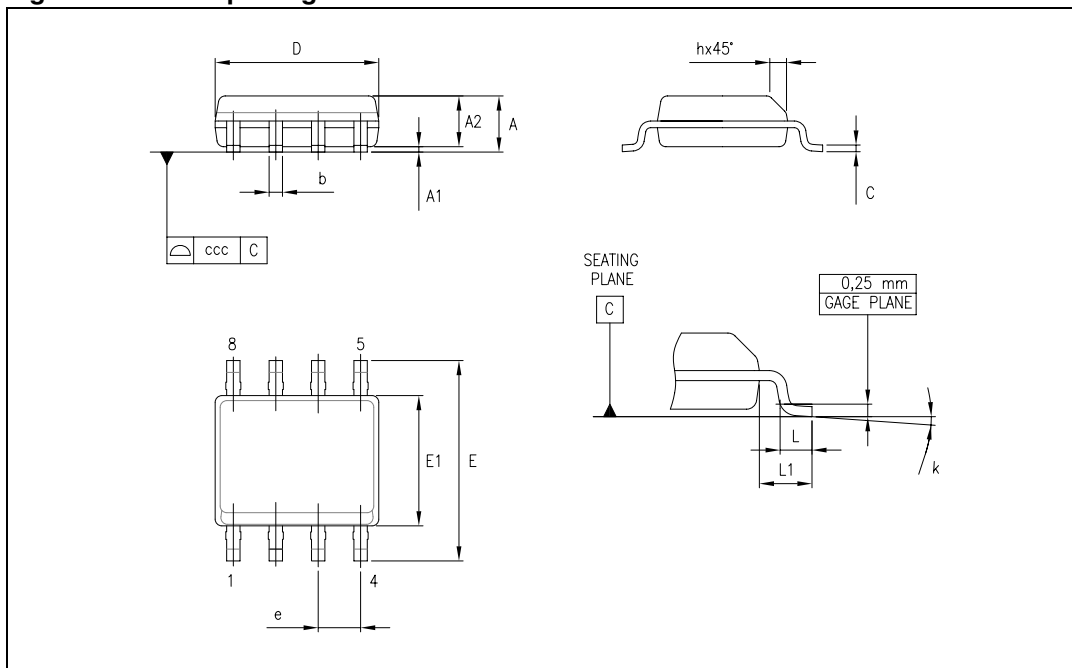


Table 6. SO-8 package mechanical data

Symbol	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.75			0.069
A1	0.10		0.25	0.004		0.010
A2	1.25			0.049		
b	0.28		0.48	0.011		0.019
c	0.17		0.23	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e		1.27			0.050	
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
L1		1.04			0.040	
k	0		8°	1°		8°
ccc			0.10			0.004

3.3 TSSOP8 package information

Figure 15. TSSOP8 package outline

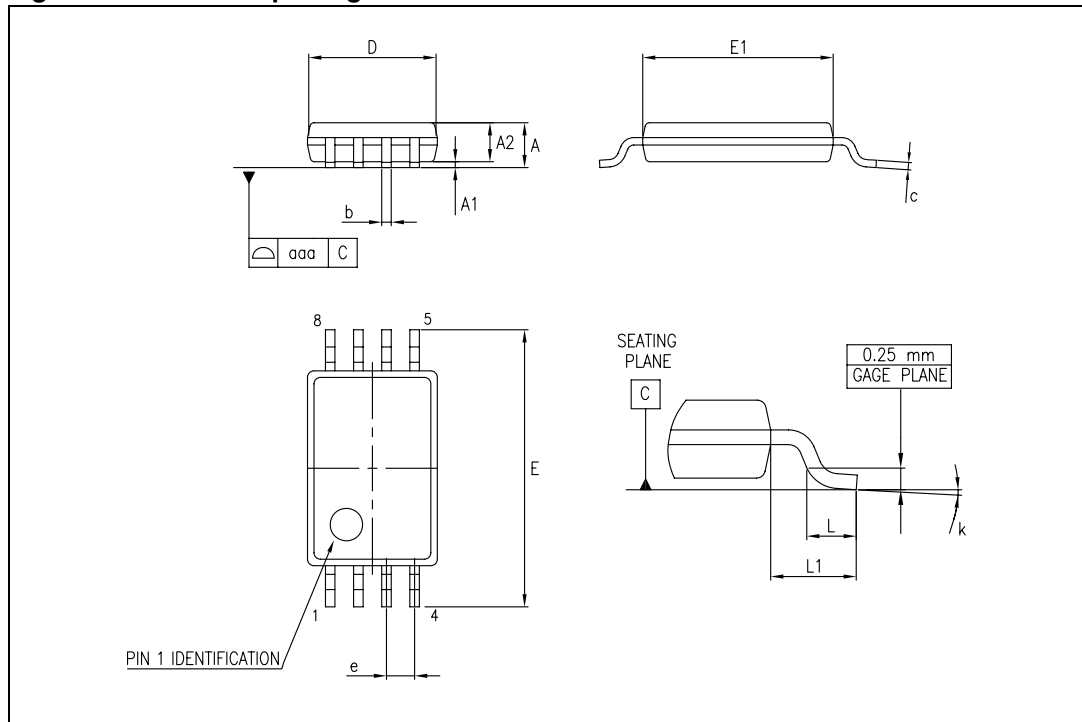


Table 7. TSSOP8 package mechanical data

Symbol	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.20			0.047
A1	0.05		0.15	0.002		0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.008
D	2.90	3.00	3.10	0.114	0.118	0.122
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.177
e		0.65			0.0256	
k	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1			0.039	
aaa			0.10			0.004

3.4 SO-14 package information

Figure 16. SO-14 package outline

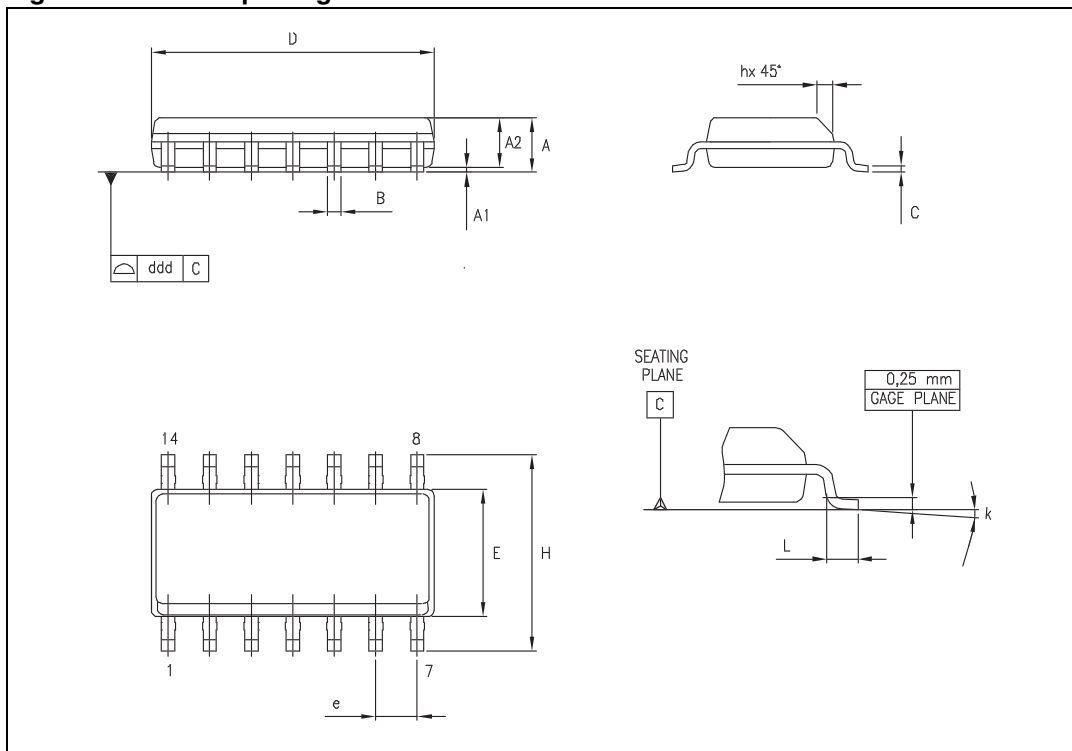


Table 8. SO-14 package mechanical data

Symbol	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.35		1.75	0.05		0.068
A1	0.10		0.25	0.004		0.009
A2	1.10		1.65	0.04		0.06
B	0.33		0.51	0.01		0.02
C	0.19		0.25	0.007		0.009
D	8.55		8.75	0.33		0.34
E	3.80		4.0	0.15		0.15
e		1.27			0.05	
H	5.80		6.20	0.22		0.24
h	0.25		0.50	0.009		0.02
L	0.40		1.27	0.015		0.05
k	8° (max.)					
ddd			0.10			0.004

3.5 TSSOP14 package information

Figure 17. TSSOP14 package outline

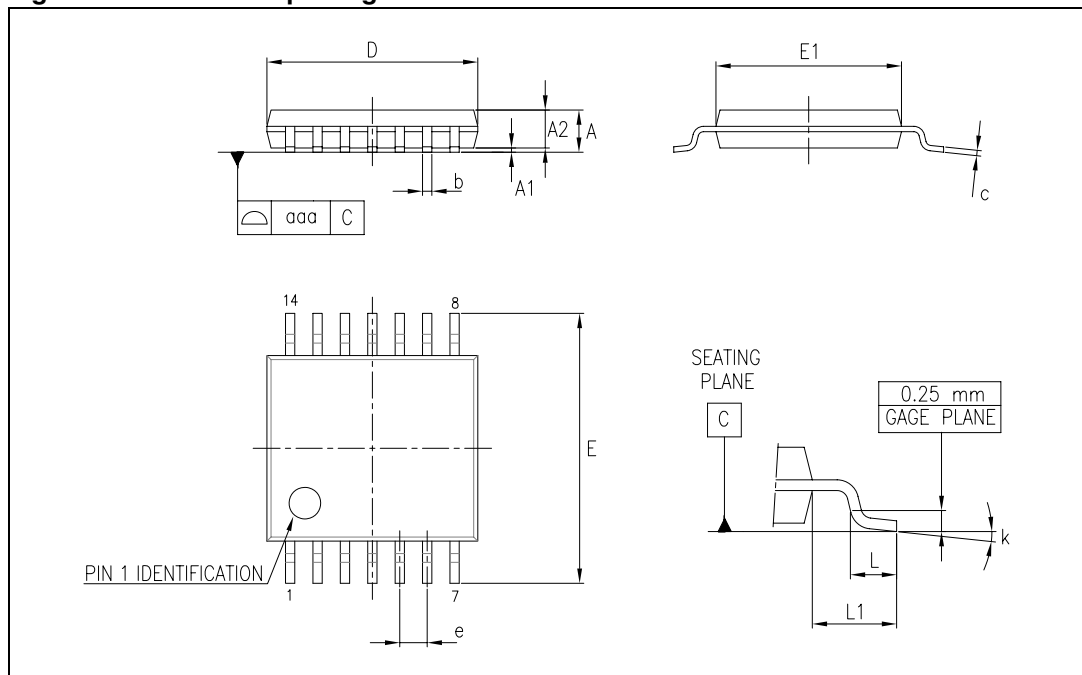


Table 9. TSSOP14 package mechanical data

Symbol	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.20			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0089
D	4.90	5.00	5.10	0.193	0.197	0.201
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.176
e		0.65			0.0256	
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1.00			0.039	
k	0°		8°	0°		8°
aaa			0.10			0.004

4 Ordering information

Table 10. Order codes

Order code	Temperature range	Package	Packaging	Marking
LMV321ILT	-40 °C, +125 °C	SOT23-5	Tape and reel	K177
LMV321RILT				K176
LMV321IYLT ⁽¹⁾		SOT23-5 (automotive grade)	Tape and reel	K180
LMV321RIYLT ⁽¹⁾				K185
LMV358ID LMV358IDT		SO-8	Tube or tape and reel	LMV358
LMV358IYDT ⁽¹⁾		SO-8 (automotive grade)	Tube or tape and reel	LMV358IY
LMV358IPT		TSSOP8	Tape and reel	MV358
LMV358IYPT ⁽²⁾		TSSOP8 (automotive grade)	Tape and reel	K181Y
LMV324ID LMV324IDT		SO-14	Tube or tape and reel	LMV324
LMV324IYDT ⁽¹⁾		SO-14 (automotive grade)	Tube or tape and reel	V324Y
LMV324IPT		TSSOP14	Tape and reel	MV324
LMV324IYPT ⁽¹⁾		TSSOP14 (automotive grade)	Tape and reel	V324IY

1. Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q 002 or equivalent.
2. Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q 002 or equivalent are ongoing.

5 Revision history

Table 11. Document revision history

Date	Revision	Changes
1-Dec-2005	1	First release - Products in full production.
25-May-2007	2	Added automotive grade part numbers to order codes table. Moved order codes table to Section 4 on page 14 .
20-Feb-2008	3	Added Figure 12: Noise versus frequency on page 7 . Updated presentation of package information. Corrected footnote for automotive grade part numbers in order codes table.
18-Jan-2010	4	Updated document format. Updated packages in Chapter 3: Package information . Modified Note 1 and added Note 2 under Table 10: Order codes .
05-Nov-2012	5	Updated Features (added SO-8, TSSOP-8, SO-14, and TSSOP-14 package). Updated titles of Figure 2 to Figure 11 (added conditions). Updated LVM321RIYLT order code in Table 10 (status qualified), removed LVM358IYD and LVM324IYD order code from Table 10 . Minor corrections throughout document.

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Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.