

Si85XX UNIDIRECTIONAL AC CURRENT SENSOR TARGET BOARD USER'S GUIDE

1. Introduction

The Si85xx products are unidirectional ac current sensors available with full-scale current sense input ranges of 5, 10, and 20 Amps. Si85xx products are ideal upgrades for older current sensing technologies offering size, performance, and cost advantages over current transformers, Hall Effect devices, DCR circuits, and other approaches. The Si85xx are extremely low loss, adding less than 1.3 mΩ of series resistance and less than 2 nH series inductance in the sensing path at 25 °C. Current-sensing terminals are isolated from the other package pins, providing 1000 V_{RMS} of isolation with the 4x4x1 mm QFN package and 5000 V_{RMS} of isolation with the wide-body, 20-pin SOIC package.

2. Kit Contents

The Si85xx Unidirectional AC Current Sensor Development Kit contains one of two versions of the 10 Amp, Si8512-based evaluation board or one version of the 20 Amp Si8513 based evaluation board. See "8. Ordering Guide" on page 10 for part number details for the 1 kV (QFN) or 5 kV (SOIC).

3. Hardware Overview

The Si85xx Target Boards shown in Figures 1, 2, and 3 provide a means of evaluating the ac current sensor in an existing application. The boards are meant to be soldered directly into a user's existing power supply and come with either the Si8512 installed (10 Amp full-scale input range) or with the Si8513 installed (20 Amp full-scale input range). For more information, refer to the Si85xx data sheet.

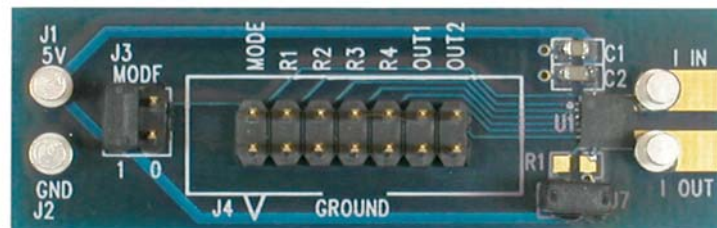


Figure 1. Si85xx-TB Overview (1 kV, 10 Amp Target Board)

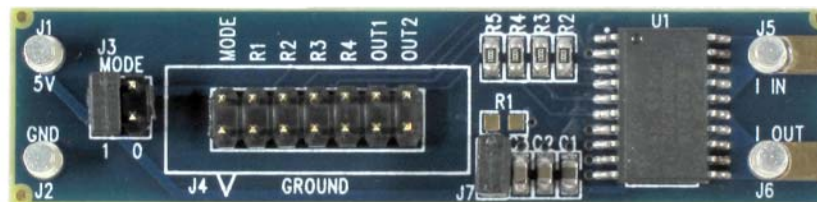


Figure 2. Si85xx5kV-TB Overview (5 kV, 10 Amp Target Board)

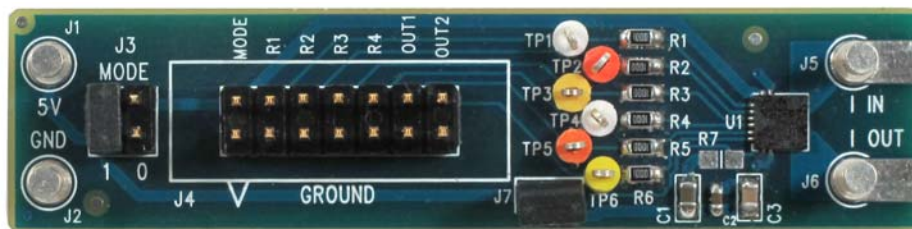


Figure 3. Si85xx20AQFN-TB Overview (1 kV, 20 Amp Target Board)

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4. Target Board Hardwired to Single-Phase POL

Figure 4 illustrates the Si85xx-TB (1 kV, 10 Amp target board) soldered directly into a single-phase POL. The single-phase POL is designed to operate at a 400 kHz switch rate and provides better than 90 percent efficiency. It also provides 10 amps of peak current to the load and protects the supply by shutting down in the event of an overcurrent condition.

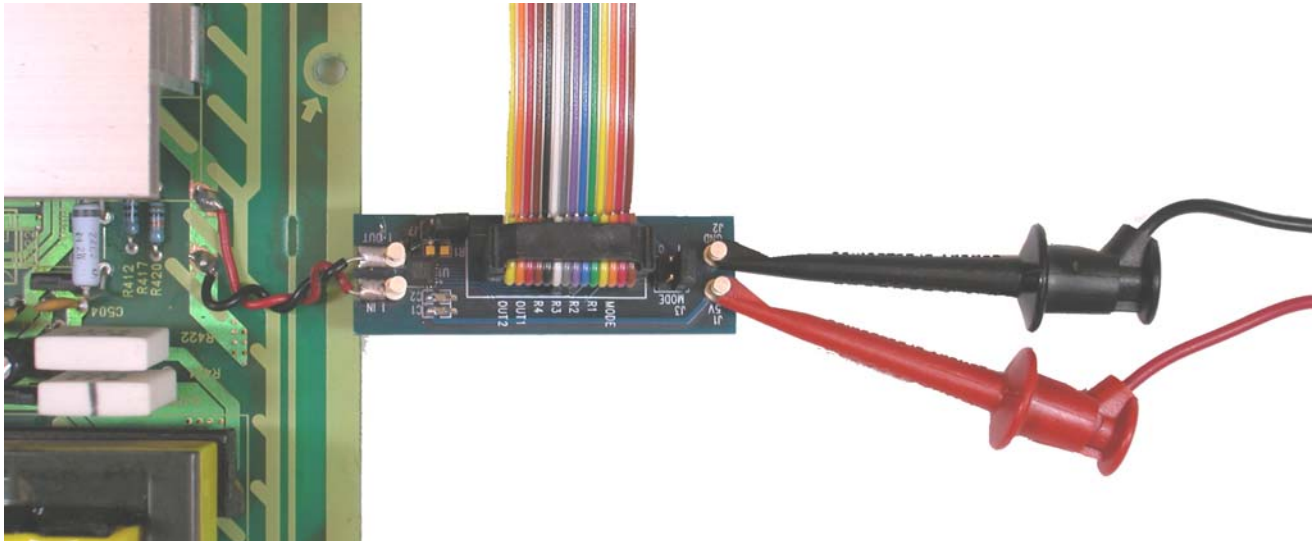


Figure 4. Si85xx-EVB Hardwired to Single-Phase POL (1 kV Target Board)

Figure 5 shows the Si8512 accurately monitoring the POL's switching current. As shown in the scope plot, a 1.6 V peak output voltage accurately monitors the 8 Amp peak, 50 percent duty cycle, 400 kHz current signal of the POL. A 20 mΩ sense resistor in series with the Si85xx is also monitored via a differential probe to provide a baseline measurement of the POL.

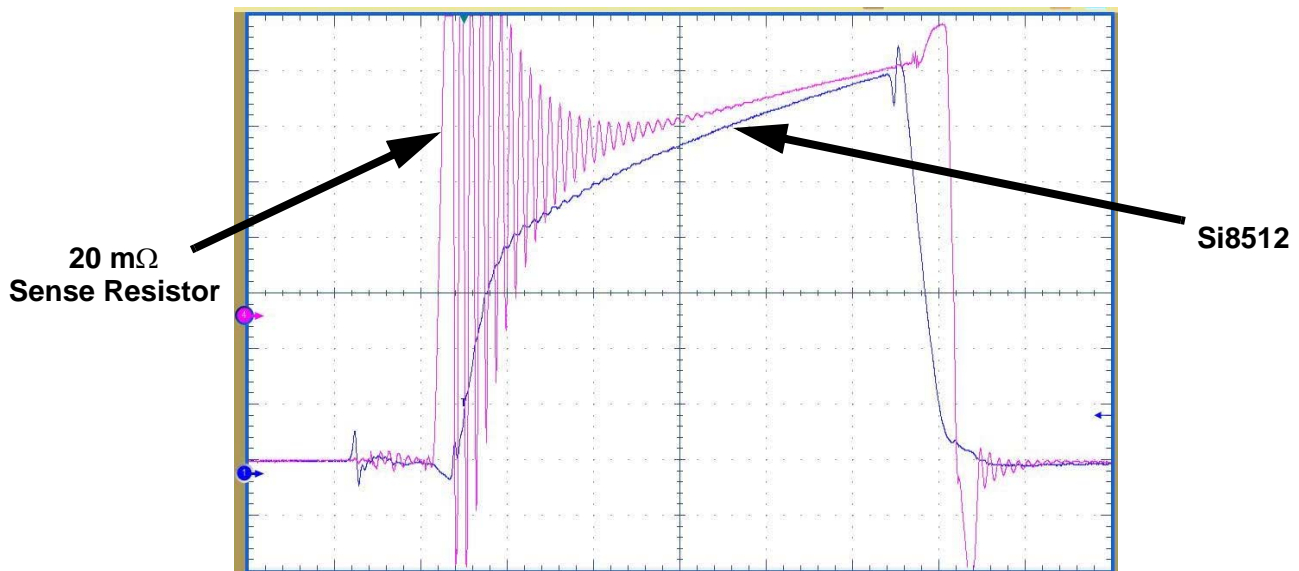


Figure 5. Si85xx-TB OUT1 Oscilloscope Plot

To accommodate this test, the Si85xx-TB was powered with a 5 V (100 mA) supply connected from J1 to J2. R1 was soldered from J4 to the respective phase control line of the POL. R2 and R4 were jumped to GND on J4. R3 was tied to 5 V, and the Mode pin was jumped to V_{DD} via J7.

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5. Si85xx Evaluation Board

The boards come with either the Si8512 installed (10 Amp full-scale input range) or with the Si8513 installed (20 Amp full-scale input range). Refer to Figures 6, 7, and 8 for the locations of the various I/O connectors and major components. Note that 100 Ω resistors were added in series to the R1 to R4 control lines on the 5 kV, 10 Amp target board and to the 20 Amp QFN target board to reduce inductive ringing from excessively long wires; these are optional for the end-system design.

- J1 5 V Supply Input power connection 5 V, 100 mA
- J2 GND, Input Supply power return connection, 0 V
- J3 HDR-2X2, Mode Control input
- J4 HDR-2X7, R1, R2, R3, R4 inputs, OUT1, OUT2 outputs, and seven GND return pins
- J5 IIN Positive Current Sense Input
- J6 IOU Negative/return Current Sense Input
- J7 HEADER, TRST timing select jumper, default tied to VDD

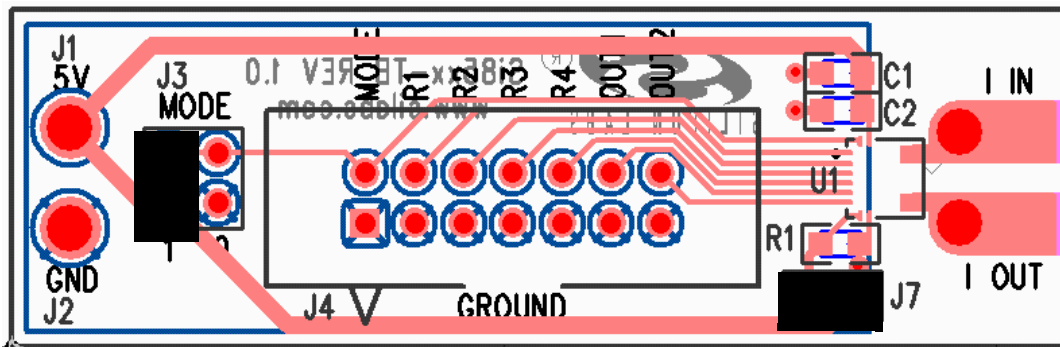


Figure 6. Final Setup (1 kV, 10 Amp Target Board)

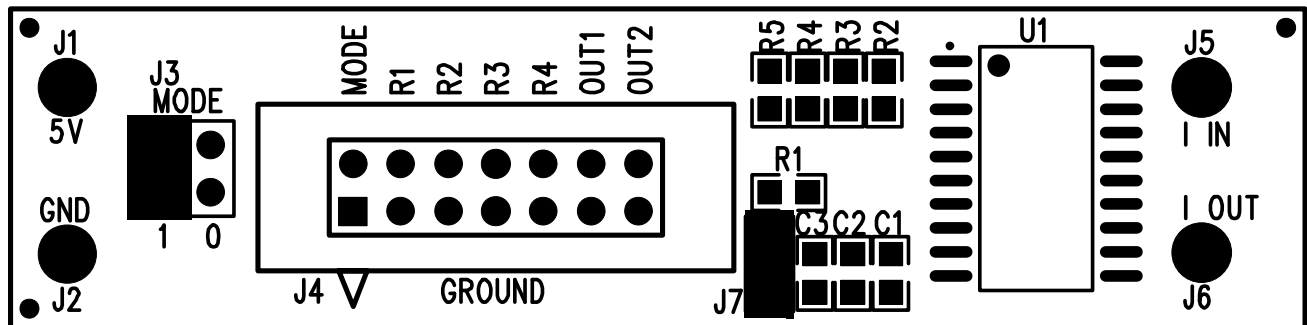


Figure 7. Final Setup (5 kV, 10 Amp Target Board)

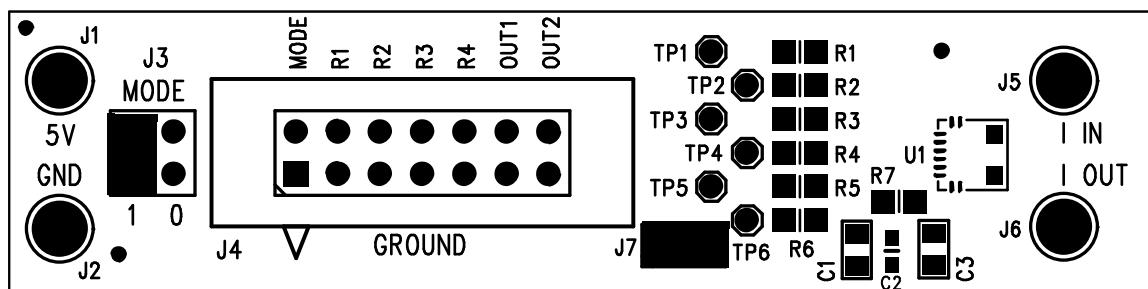


Figure 8. Final Setup (1 kV, 20 Amp Target Board)

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5.1. VIN (J1, J2)

The user-provided power supply should be connected to connectors J1 and J2, where J2 is the reference. The power source must provide up to 5 V with at least 100 mA output current.

Table 1. J1, J2 Pin Descriptions

| Jumper # | Description |
|----------|-------------|
| J1 | 5 V, 100 mA |
| J2 | GND |

5.2. Mode Pin (J3)

Connector J3 is the Mode Control header. The user should tie this pin to either VDD or GND depending on the power supply topology being tested.

Table 2. J3 Pin Descriptions

| Jumper # | Description |
|----------|-------------|
| J3 | VDD or GND |

5.3. R1, R2, R3, R4 Inputs, OUT1, OUT2 Outputs (J4)

Connector J4 is used to provide the control inputs (R1, R2, R3, R4, MODE) to the Si85xx. It also provides the output voltage of the sensed input current on OUT1 and OUT2.

Table 3. J4 Pin Descriptions

| Jumper # | Description |
|----------|---------------------------------|
| J4 | R1, R2, R3, R4, OUT1, OUT2, GND |

5.4. IIN (J5)

Connector J5 is used to provide the positive current sense input to the Si85xx device.

Table 4. J5 Pin Descriptions

| Jumper # | Description |
|----------|-------------|
| J5 | IIN |

5.5. IOUT (J6)

Connector J6 is used to provide the negative/return current sense input to the Si85xx device.

Table 5. J6 Pin Descriptions

| Jumper # | Description |
|----------|-------------|
| J6 | IOUT |

5.6. TRST RESET (J7)

Connector J7 is used to set the timing for the Si85xx integrator. J7 defaults to Reset option 1 where TRST is tied VDD providing approximately 200 ns of reset time. Reset option 2 is selected by connecting a timing resistor (R1 in Figures 9 and 10 and R7 in Figure 11) from the TRST input to ground.

Table 6. J7 Pin Descriptions

| Jumper # | Description |
|----------|-----------------------|
| J7 | TRST—VDD or R1 to GND |

6. Schematics

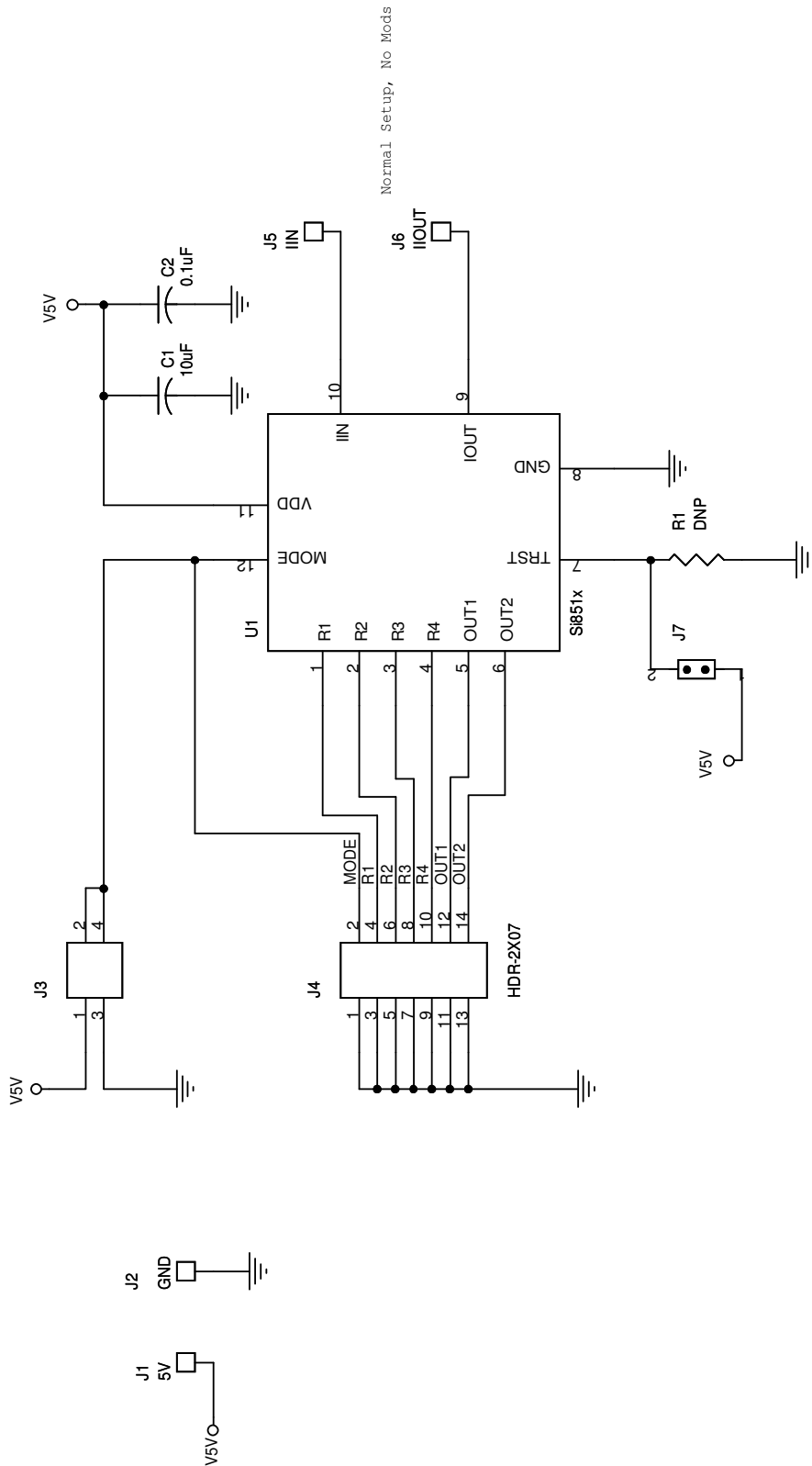


Figure 9. Si85xx-TB Target Board Schematic (1 kV, 10 Amp Target Board)

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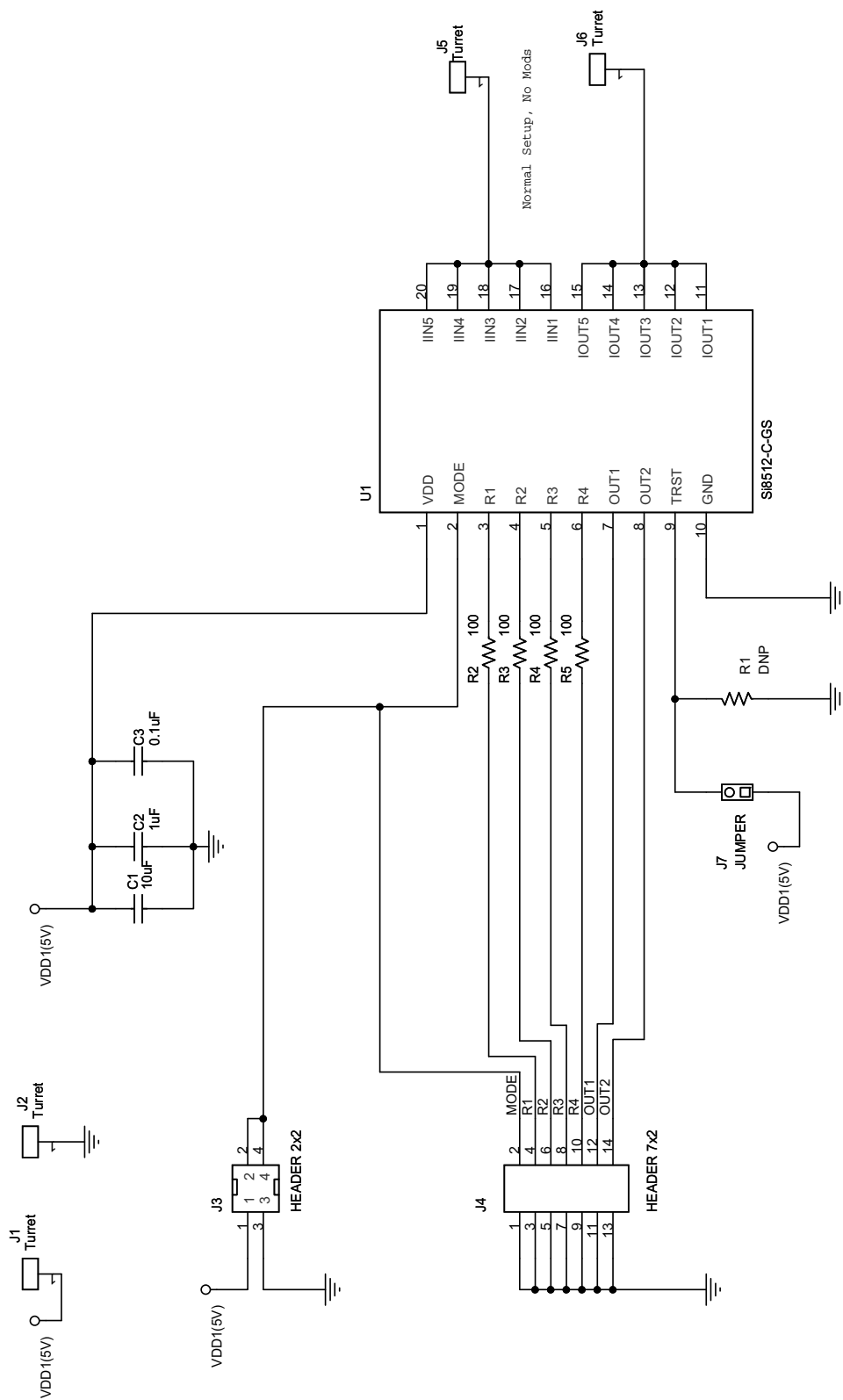


Figure 10. Si85xx5kV-TB Target Board Schematic (5 kV, 10 Amp Target Board)

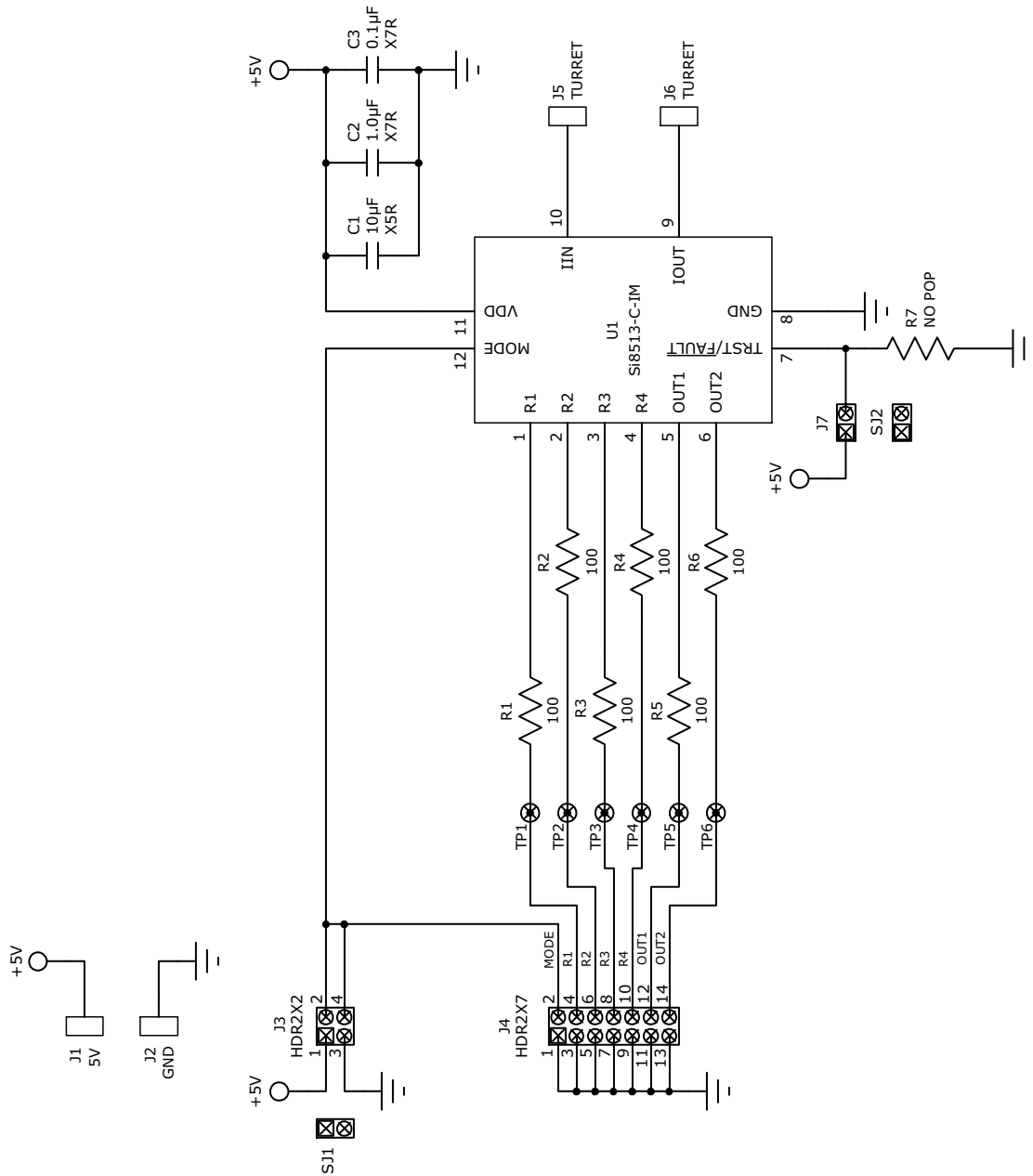


Figure 11. Si85xx20AQFN-TB Target Board Schematic (1 kV, 20 Amp Target Board)

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7. Bill of Materials

Table 7. Si85xx-TB Bill of Materials (1 kV, 10 Amp Target Board)

| Item | Qty | Reference | Description | Mfr Part Number | Manufacturer |
|------|-----|-----------|-------------|-------------------------|--------------|
| 1 | 1 | C1 | 10 uF | PCC2182CT-ND | Digi-Key |
| 2 | 1 | C2 | 0.1 uF | PCC1812CT-ND | Digi-Key |
| 3 | 1 | J1 | 5 V | 2551-2-00-44-00-00-07-0 | Bisco |
| 4 | 1 | J2 | GND | 2551-2-00-44-00-00-07-0 | Bisco |
| 5 | 1 | J3 | HDR-2X2 | S2012-02-ND | Digi-Key |
| 6 | 1 | J4 | HDR-2X07 | S2012-07-ND | Digi-Key |
| 7 | 1 | J5 | IIN | 2551-2-00-44-00-00-07-0 | Bisco |
| 8 | 1 | J6 | IIOUT | 2551-2-00-44-00-00-07-0 | Bisco |
| 9 | 1 | J7 | HEADER | S1012-02-ND | Digi-Key |
| 10 | 1 | R1 | DNP | P150CCT-ND | Digi-Key |
| 11 | 1 | U1 | Si851x | Si8500-IS | Digi-Key |

Table 8. Si85xx5kV-TB Bill of Materials (5 kV, 10 Amp Target Board)

| Item | Qty | Reference | Description | Mfr Part Number | Manufacturer |
|------|-----|-------------------|-------------|-------------------------|--------------|
| 1 | 1 | C1 | 10 µF | C0805X5R6R3-106M | Venkel |
| 2 | 1 | C2 | 1 µF | C0805X7R160-105M | Venkel |
| 3 | 1 | C3 | 0.1 µF | C0805X7R160-104K | Venkel |
| 4 | 4 | J1, J2, J5, J6 | Turret | 2551-2-00-44-00-00-07-0 | Mill-Max |
| 5 | 1 | J3 | HEADER 2x2 | TSW-102-07-T-D | Samtec |
| 6 | 1 | J4 | HEADER 7x2 | TSW-107-07-T-D | Samtec |
| 7 | 1 | J7 | JUMPER | TSW-102-07-T-S | Samtec |
| 8 | 1 | R1 | DNP | CR0805-8W-1002F | Venkel |
| 9 | 4 | R2, R3, R4, R5 | 100 | CR0805-10W-1000F | Venkel |
| 10 | 1 | U1 | Si8512-C-GS | Si8512-C-GS | SiLabs |

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Table 9. Si85xx20AQFN-TB Bill of Materials (1 kV, 20 Amp Target Board)

| Item | Qty | Reference | Description | Mfr Part Number | Manufacturer |
|------|-----|-----------|-----------------|----------------------|--------------|
| 1 | 4 | J1-2 J5-6 | Turret | 2551-200440000070-ND | Digikey |
| 2 | 1 | C3 | 0.1 μ F | 399-1167-1-ND | Digikey |
| 3 | 1 | C2 | 1 μ F | 445-1604-1-ND | Digikey |
| 4 | 1 | C1 | 10 μ F | C0805C106M9PACTU-ND | Digikey |
| 5 | 1 | J7 | Stake header | S1011E-02-ND | Digikey |
| 6 | 1 | J3 | Stake header | S2011E-02-ND | Digikey |
| 7 | 1 | J4 | Stake header | S2011E-07-ND | Digikey |
| 8 | 2 | SJ1-2 | Shorting jumper | S9000-ND | Digikey |
| 9 | 6 | R1-6 | 100 Ω | RNCF20T9100FR-ND | Digikey |
| 10 | 1 | R7 | No POP | No POP | Digikey |
| 11 | 1 | U1 | Si8513 | Si8513-C-IM | Silicon Labs |
| 12 | 6 | TP1-6 | No POP | No POP | Digikey |

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8. Ordering Guide

Table 10. Ordering Guide

| Ordering Part # | Description/Contents | Isolation Rating |
|------------------------|---|-------------------------|
| Si85xx-EVB | Evaluation board with Si8512 (10 Amp) in QFN package | 1 kV _{RMS} |
| Si85xx5kV-EVB | Evaluation board with Si8512 (10 Amp) in SOIC package | 5 kV _{RMS} |
| Si85XX20AQFN-EVB | Evaluation board with Si8513 (20 Amp) in QFN package | 1 kV _{RMS} |

DOCUMENT CHANGE LIST

Revision 0.1 to Revision 0.2

- Added Si85xx5kV-TB target board descriptive details throughout.
 - Added Figure 2 on page 1.
 - Added Figure 7 on page 3.
 - Updated Figure 9 title.
 - Added Figure 10 on page 6.
 - Updated Table 7, "Si85xx-TB Bill of Materials (1 kV, 10 Amp Target Board)," on page 8.
 - Added Table 8, "Si85xx5kV-TB Bill of Materials (5 kV, 10 Amp Target Board)," on page 8.
 - Added "8. Ordering Guide" on page 10.

Revision 0.2 to Revision 0.3

- Added Si85xx20AQFN-TB target board descriptive details throughout.
 - Added Figure 3 on page 1.
 - Added Figure 8 on page 3.
 - Added Figure 10 on page 6.
 - Added Table 9, "Si85xx20AQFN-TB Bill of Materials (1 kV, 20 Amp Target Board)," on page 9.
 - Updated "8. Ordering Guide" on page 10.

Revision 0.3 to Revision 0.4

- Updated "8. Ordering Guide" on page 10.

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