

SIOV metal oxide varistors

Leaded varistors, AdvanceD-MP, S14 series

Series/Type: B722*
Date: April 2011

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Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire

Features

- Wide operating voltage range 130 ... 680 V_{RMS}
- All types duty cycle @ 6 kV/ 3 kA = >10 pulses, according to IEC 60950-1 Annex Q; IEC 61051-2
- All types I_{nom} @ 3 kA = >15 impulses according to UL 1449, 3rd Edition surge current generator (8/20 μs), Type 2 listed
- Multiple pulse handling capability

Approvals

- UL
- CSA
- VDE
- IEC

Delivery mode

- Bulk (standard), taped versions on reel or in Ammo pack upon request.
- For further details refer chapter "Taping, packaging and lead configuration" for leaded varistors.

General technical data

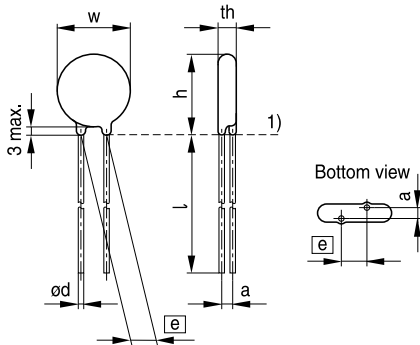
| | | | |
|-----------------------|----------------|--------------|-------------------|
| Climatic category | to IEC 60068-1 | 40/85/56 | |
| Operating temperature | to IEC 61051 | -40 ... + 85 | °C |
| Storage temperature | | -40 ... +125 | °C |
| Electric strength | to IEC 61051 | ≥ 2.5 | kV _{RMS} |
| Insulation resistance | to IEC 61051 | ≥ 100 | MΩ |
| Response time | | < 25 | ns |


Electrical specifications and ordering codes
Maximum ratings ($T_A = 85\text{ }^\circ\text{C}$)

| Ordering code | Type (untaped) SIOV- | V_{RMS} | V_{DC} | i_{max} (8/20 μs) | W_{max} (2 ms) | P_{max} |
|-----------------|----------------------------|-----------|----------|------------------------------------|---------------------|-----------|
| | | V | V | A | J | W |
| B72214P2131K101 | S14K130E2K1 | 130 | 170 | 6000 | 60.0 | 0.60 |
| B72214P2141K101 | S14K140E2K1 | 140 | 180 | 6000 | 65.0 | 0.60 |
| B72214P2151K101 | S14K150E2K1 | 150 | 200 | 6000 | 70.0 | 0.60 |
| B72214P2171K101 | S14K175E2K1 | 175 | 225 | 6000 | 80.0 | 0.60 |
| B72214P2211K101 | S14K210E2K1 | 210 | 270 | 6000 | 95.0 | 0.60 |
| B72214P2231K101 | S14K230E2K1 | 230 | 300 | 6000 | 105.0 | 0.60 |
| B72214P2251K101 | S14K250E2K1 | 250 | 270 | 6000 | 115.0 | 0.60 |
| B72214P2271K101 | S14K275E2K1 | 275 | 350 | 6000 | 130.0 | 0.60 |
| B72214P2301K101 | S14K300E2K1 | 300 | 385 | 6000 | 140.0 | 0.60 |
| B72214P2321K101 | S14K320E2K1 | 320 | 420 | 6000 | 150.0 | 0.60 |
| B72214P2351K101 | S14K350E2K1 | 350 | 460 | 6000 | 165.0 | 0.60 |
| B72214P2381K101 | S14K385E2K1 | 385 | 505 | 6000 | 180.0 | 0.60 |
| B72214P2421K101 | S14K420E2K1 | 420 | 560 | 6000 | 190.0 | 0.60 |
| B72214P2461K101 | S14K460E2K1 | 460 | 615 | 6000 | 200.0 | 0.60 |
| B72214P2511K101 | S14K510E2K1 | 510 | 670 | 6000 | 200.0 | 0.60 |
| B72214P2551K101 | S14K550E2K1 | 550 | 745 | 6000 | 220.0 | 0.60 |
| B72214P2621K101 | S14K620E2K1 | 625 | 825 | 6000 | 240.0 | 0.60 |
| B72214P2681K101 | S14K680E2K1 | 680 | 895 | 6000 | 260.0 | 0.60 |

Characteristics ($T_A = 25\text{ }^\circ\text{C}$)

| Ordering code | Type (untaped) SIOV- | V_v (1 mA) | ΔV_v (1 mA) | $v_{c,max}$ (i_c) | i_c | C_{typ} (1 kHz) |
|-----------------|----------------------------|-----------------|------------------------|--------------------------|-------|----------------------|
| | | V | % | V | A | pF |
| B72214P2131K101 | S14K130E2K1 | 205 | ± 10 | 340 | 50.0 | 1100 |
| B72214P2141K101 | S14K140E2K1 | 220 | ± 10 | 360 | 50.0 | 1000 |
| B72214P2151K101 | S14K150E2K1 | 240 | ± 10 | 395 | 50.0 | 900 |
| B72214P2171K101 | S14K175E2K1 | 270 | ± 10 | 455 | 50.0 | 800 |
| B72214P2211K101 | S14K210E2K1 | 330 | ± 10 | 545 | 50.0 | 690 |
| B72214P2231K101 | S14K230E2K1 | 360 | ± 10 | 595 | 50.0 | 630 |
| B72214P2251K101 | S14K250E2K1 | 390 | ± 10 | 650 | 50.0 | 580 |
| B72214P2271K101 | S14K275E2K1 | 430 | ± 10 | 710 | 50.0 | 530 |
| B72214P2301K101 | S14K300E2K1 | 470 | ± 10 | 775 | 50.0 | 485 |
| B72214P2321K101 | S14K320E2K1 | 510 | ± 10 | 840 | 50.0 | 445 |
| B72214P2351K101 | S14K350E2K1 | 560 | ± 10 | 910 | 50.0 | 410 |
| B72214P2381K101 | S14K385E2K1 | 620 | ± 10 | 1025 | 50.0 | 390 |
| B72214P2421K101 | S14K420E2K1 | 680 | ± 10 | 1120 | 50.0 | 355 |
| B72214P2461K101 | S14K460E2K1 | 750 | ± 10 | 1240 | 50.0 | 320 |
| B72214P2511K101 | S14K510E2K1 | 820 | ± 10 | 1355 | 50.0 | 300 |
| B72214P2551K101 | S14K550E2K1 | 910 | ± 10 | 1500 | 50.0 | 265 |
| B72214P2621K101 | S14K620E2K1 | 1000 | ± 10 | 1650 | 50.0 | 240 |
| B72214P2681K101 | S14K680E2K1 | 1100 | ± 10 | 1815 | 50.0 | 220 |


Leaded varistors
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Dimensional drawings


1) Seating plane to IEC 60717

VAR0408-C-E

Weight

| Nominal diameter mm | V_{RMS} V | Weight g |
|------------------------|----------------|-------------|
| 14 | 130 ... 680 | 1.8 ... 5.4 |

The weight of varistors in between these voltage classes can be interpolated.

Dimensions

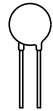
| Ordering code | [e] ±1 mm | a ±1 mm | w_{max} mm | th_{max} mm | h_{max} mm | l_{min} mm | d ±0.05 mm |
|-----------------|--------------|------------|-----------------|------------------|-----------------|-----------------|---------------|
| B72214P2131K101 | 7.5 | 2.0 | 16.0 | 4.7 | 20.0 | 25.0 | 0.8 |
| B72214P2141K101 | 7.5 | 2.1 | 16.0 | 4.8 | 20.0 | 25.0 | 0.8 |
| B72214P2151K101 | 7.5 | 2.2 | 16.0 | 4.9 | 20.0 | 25.0 | 0.8 |
| B72214P2171K101 | 7.5 | 2.4 | 16.0 | 5.1 | 20.0 | 25.0 | 0.8 |
| B72214P2211K101 | 7.5 | 2.7 | 16.0 | 5.4 | 20.0 | 25.0 | 0.8 |
| B72214P2231K101 | 7.5 | 2.9 | 16.0 | 5.6 | 20.0 | 25.0 | 0.8 |
| B72214P2251K101 | 7.5 | 3.0 | 16.0 | 5.7 | 20.0 | 25.0 | 0.8 |
| B72214P2271K101 | 7.5 | 3.2 | 16.0 | 5.9 | 20.0 | 25.0 | 0.8 |
| B72214P2301K101 | 7.5 | 3.5 | 16.0 | 6.1 | 20.0 | 25.0 | 0.8 |
| B72214P2321K101 | 7.5 | 3.7 | 16.0 | 6.3 | 20.0 | 25.0 | 0.8 |
| B72214P2351K101 | 7.5 | 4.1 | 16.5 | 6.7 | 20.5 | 25.0 | 0.8 |
| B72214P2381K101 | 7.5 | 4.4 | 16.5 | 7.7 | 20.5 | 25.0 | 0.8 |
| B72214P2421K101 | 7.5 | 4.7 | 16.5 | 8.2 | 20.5 | 25.0 | 0.8 |
| B72214P2461K101 | 7.5 | 5.1 | 16.5 | 8.5 | 20.5 | 25.0 | 0.8 |
| B72214P2511K101 | 7.5 | 5.4 | 17.0 | 8.9 | 21.0 | 25.0 | 0.8 |
| B72214P2551K101 | 7.5 | 5.9 | 17.0 | 9.4 | 21.0 | 25.0 | 0.8 |
| B72214P2621K101 | 7.5 | 6.4 | 17.0 | 9.9 | 21.0 | 25.0 | 0.8 |
| B72214P2681K101 | 7.5 | 7.0 | 17.0 | 10.5 | 21.0 | 25.0 | 0.8 |


Reliability data

| Test | Test methods/conditions | Requirement |
|---|--|--|
| Varistor voltage | The voltage between two terminals with the specified measuring current applied is called V_V (1 mA _{DC} @ 0.2 ... 2 s). | To meet the specified value |
| Clamping voltage | The maximum voltage between two terminals with the specified standard impulse current (8/20 μ s) applied. | To meet the specified value |
| Endurance at upper category temperature | 1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT ± 2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured. | $ \Delta V/V$ (1 mA) $\leq 10\%$ |
| Surge current derating, 8/20 μ s | 10 surge currents (8/20 μ s), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 μ s | $ \Delta V/V$ (1 mA) $\leq 10\%$ (measured in direction of surge current) No visible damage |
| Surge current derating, 2 ms | 10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms | $ \Delta V/V$ (1 mA) $\leq 10\%$ (measured in direction of surge current) No visible damage |
| Electric strength | IEC 61051-1, test 4.9.2 Metal balls method, 2500 V _{RMS} , 60 s The varistor is placed in a container holding 1.6 \pm 0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls. | No breakdown |


Leaded varistors
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| Test | Test methods/conditions | Requirement |
|-----------------------------|--|---|
| Climatic sequence | <p>The specimen shall be subjected to:</p> <p>a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba</p> <p>b) damp heat, 1st cycle: 55 °C, 93% r. H., 24 h, IEC 60068-2-30, test Db</p> <p>c) cold, LCT, 2 h, IEC 60068-2-1, test Aa</p> <p>d) damp heat, additional 5 cycles: 55 °C/25 °C, 93% r. H., 24 h/cycle, IEC 60068-2-30, test Db.</p> <p>Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h.</p> <p>Thereafter, the change of V_V shall be measured. Thereafter, insulation resistance R_{ins} shall be measured at $V = 500$ V.</p> | $ \Delta V/V (1 \text{ mA}) \leq 10\%$ $R_{ins} \geq 100 \text{ M}\Omega$ |
| Rapid change of temperature | IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles | $ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage |
| Damp heat, steady state | IEC 60068-2-78, test Ca The specimen shall be subjected to 40 ± 2 °C, 90 to 95% r. H. for 56 days without load / with 10% of the maximum continuous DC operating voltage V_{DC} . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured. Thereafter, insulation resistance R_{ins} shall be measured at $V = 500$ V (insulated varistors only). | $ \Delta V/V (1 \text{ mA}) \leq 10\%$ $R_{ins} \geq 100 \text{ M}\Omega$ |



| Test | Test methods/conditions | Requirement |
|------------------------------|--|--|
| Solderability | <p>IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s:</p> <p>After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.</p> | <p>The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.</p> |
| Resistance to soldering heat | <p>IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s:</p> <p>Each lead shall be dipped into a solder bath having a temperature of 260 ±5 °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for 10 ±1 s and then be stored at room temperature and normal humidity for 1 to 2 h.</p> <p>The change of V_V shall be measured and the specimen shall be visually examined.</p> | <p>$\Delta V/V (1 \text{ mA}) \leq 5\%$</p> <p>No visible damage</p> |
| Tensile strength | <p>IEC 60068-2-21, test Ua1</p> <p>After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage.</p> <p>Force for wire diameter:</p> <p>0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N</p> | <p>$\Delta V/V (1 \text{ mA}) \leq 5\%$</p> <p>No break of solder joint, no wire break</p> |


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| Test | Test methods/conditions | Requirement |
|-------------|---|---|
| Vibration | IEC 60068-2-6, test Fc, method B4 Frequency range: 10 ... 55 Hz Amplitude: 0.75 mm or 98 m/s ² Duration: 6 h (3 · 2 h) Pulse: sine wave After repeatedly applying a single harmonic vibration according to the table above. The change of V_v shall be measured and the specimen shall be visually examined. | $ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage |
| Bump | IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400 m/s ² Number of bumps: 4000 Pulse: half sine | $ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage |
| Fire hazard | IEC 60695-11-5 (needle flame test) Severity: vertical 10 s | 5 s max. |

Note:

UCT = Upper category temperature

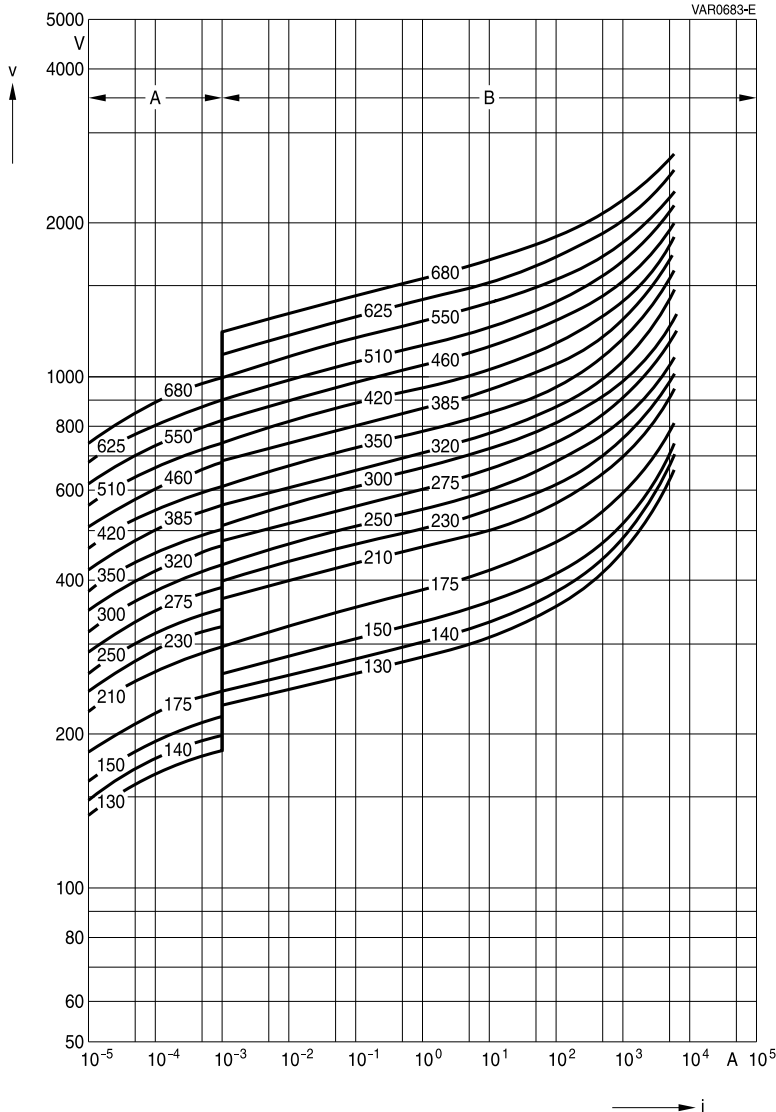
LCT = Lower category temperature

 R_{ins} = Insulation resistance



v/i characteristics

$v = f(i)$ - for explanation of the characteristics refer to "General technical information", 1.6.3
 A = Leakage current, B = Protection level } for worst-case varistor tolerances



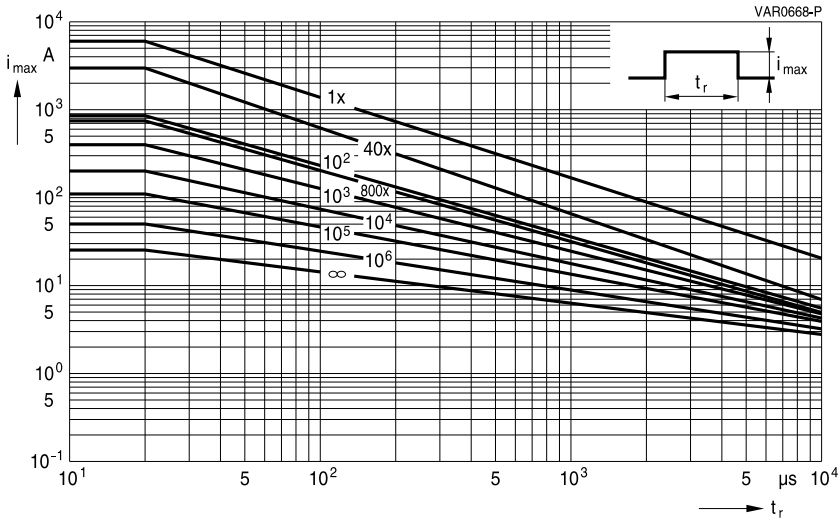
SIOV-S14 ... E2K1



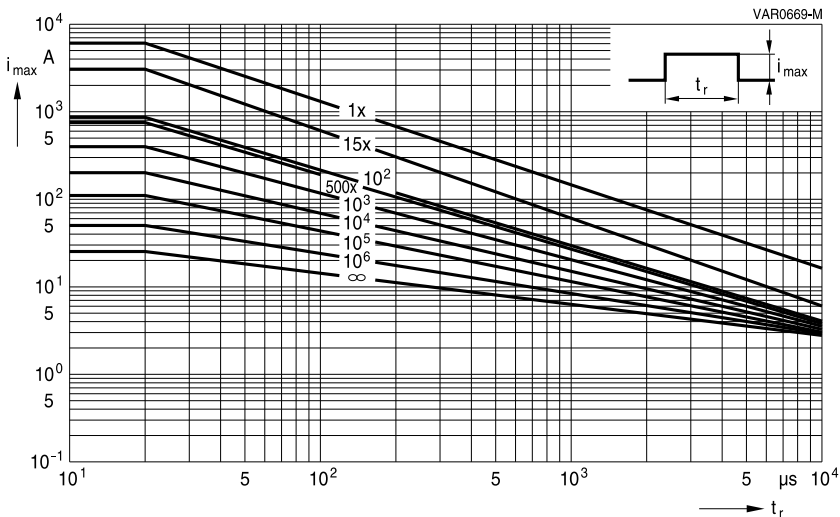
Derating curves

Maximum surge current $i_{max} = f(t_r, \text{pulse train})$

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-S14K130 ... K460E2K1



SIOV-S14K510 ... K680E2K1



Leaded varistors

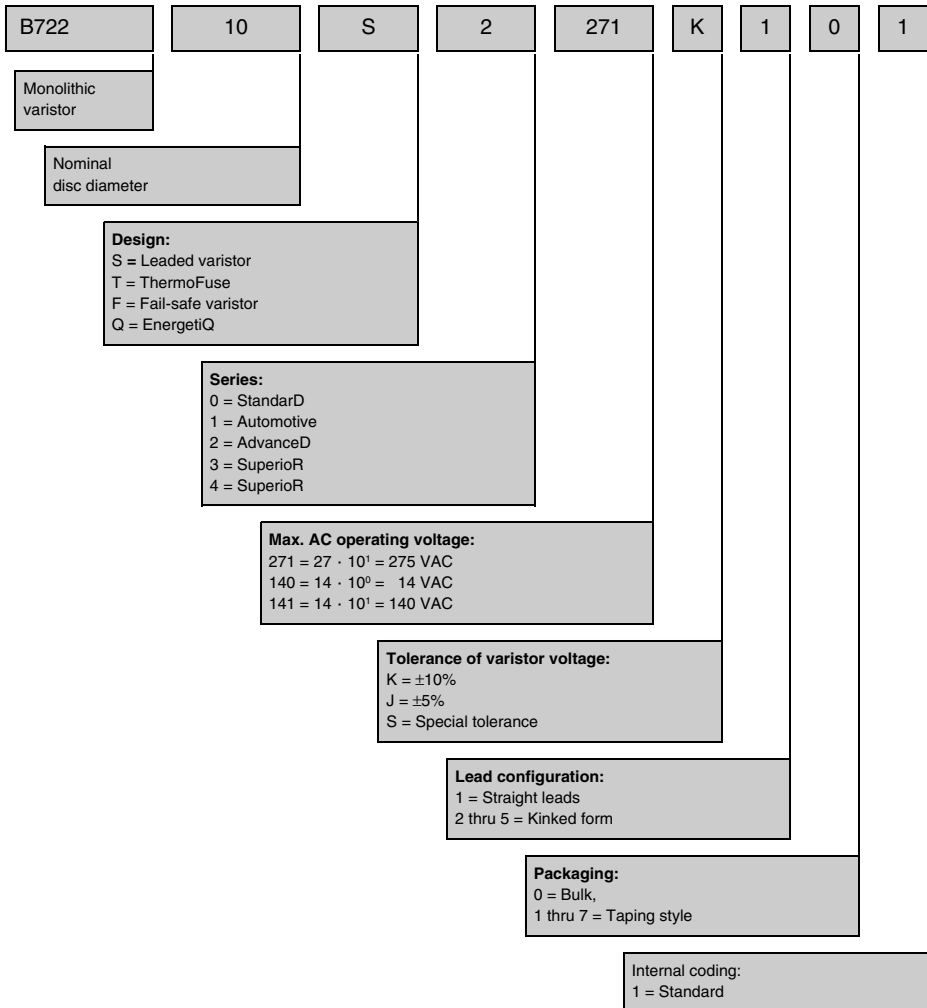
B722*

AdvanceD-MP, S14 series

Taping, packaging and lead configuration

1 EPCOS ordering code system

For leaded varistors





Leaded varistors

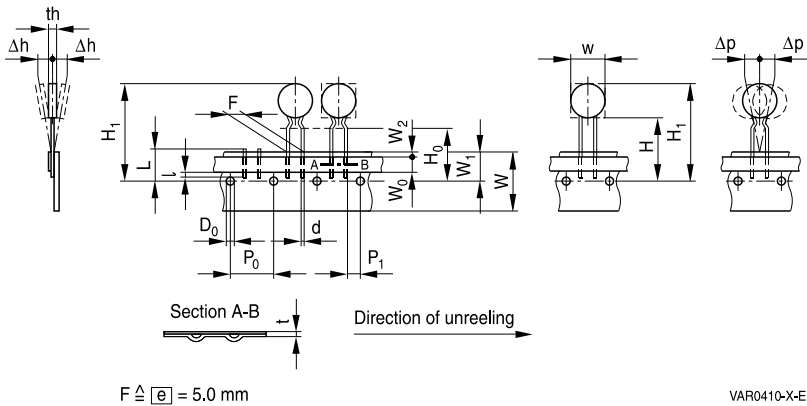
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AdvanceD-MP, S14 series

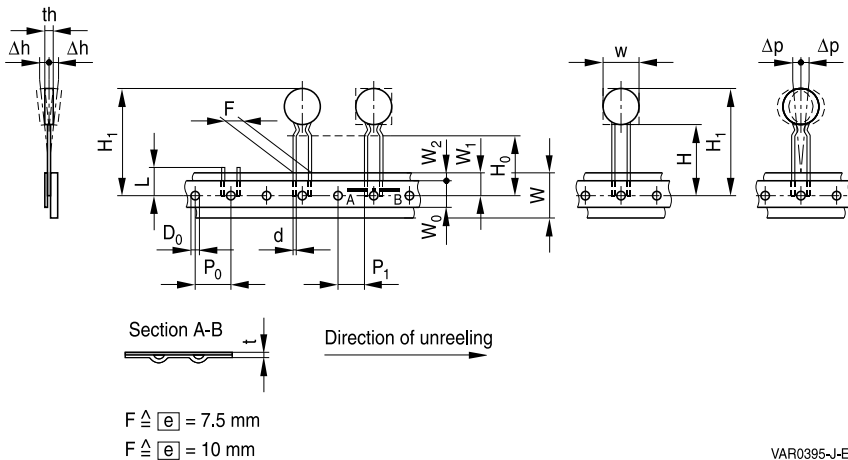
2 Taping and packaging of leaded varistors

Tape packaging for lead spacing \boxed{e} = 5 fully conforms to IEC 60286-2, while for lead spacings \boxed{e} = 7.5 and 10 the taping mode is based on this standard.

2.1 Taping in accordance with IEC 60286-2 for lead spacing 5.0 mm



2.2 Taping based on IEC 60286-2 for lead spacing 7.5 and 10 mm





2.3 Tape dimensions (in mm)

| Symbol | $e = 5.0$ | Tolerance | $e = 7.5$ | Tolerance | $e = 10.0$ | Tolerance | Remarks |
|----------------|----------------|-------------|--------------------|--------------|--------------|--------------|--|
| w | | max. | | max. | | max. | see tables in each series under "Dimensions" |
| th | | max. | | max. | | max. | |
| d | 0.6 | ± 0.05 | 0.8 | ± 0.05 | 1.0 | ± 0.05 | |
| P ₀ | 12.7 | ± 0.3 | 12.7 ¹⁾ | ± 0.3 | 12.7 | ± 0.3 | ± 1 mm/20 sprocket holes |
| P ₁ | 3.85 | ± 0.7 | 8.95 | ± 0.8 | 7.7 | ± 0.8 | |
| F | 5.0 | $+0.6/-0.1$ | 7.5 | ± 0.8 | 10.0 | ± 0.8 | measured at top of component body |
| Δh | 0 | ± 2.0 | depends on s | | depends on s | | |
| Δp | 0 | ± 1.3 | 0 | ± 2.0 | 0 | ± 2.0 | |
| W | 18.0 | ± 0.5 | 18.0 | ± 0.5 | 18.0 | ± 0.5 | Peel-off force ≥ 5 N |
| W ₀ | 5.5 | min. | 11.0 | min. | 11.0 | min. | |
| W ₁ | 9.0 | ± 0.5 | 9.0 | $+0.75/-0.5$ | 9.0 | $+0.75/-0.5$ | |
| W ₂ | 3.0 | max. | 3.0 | max. | 3.0 | max. | |
| H | 18.0 | $+2.0/-0$ | 18.0 | $+2.0/-0$ | 18.0 | $+2.0/-0$ | 2) 3) |
| H ₀ | 16.0 (18.0) | ± 0.5 | 16.0 (18.0) | ± 0.5 | 16.0 | ± 0.5 | |
| H ₁ | 32.2 | max. | 45.0 | max. | 45.0 | max. | |
| D ₀ | 4.0 | ± 0.2 | 4.0 | ± 0.2 | 4.0 | ± 0.2 | without lead |
| t | 0.9 | max. | 0.9 | max. | 0.9 | max. | |
| L | 11.0 | max. | 11.0 | max. | 11.0 | max. | |
| l | 4.0 | max. | | | | | |

1) Taping with P₀ = 15.0 mm upon request

2) Applies only to uncrimped types

3) Applies only to crimped types (H₀ = 18 upon request)



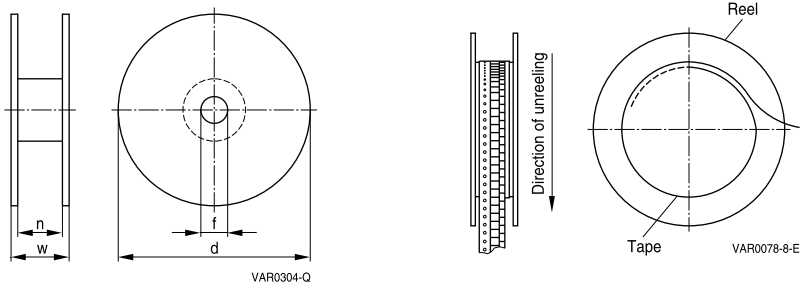
2.4 Taping mode

Example: B72210S0271K1 5 1
|
Digit 14

| Digit 14 | Taping mode | Reel type | Seating plane height H ₀ for crimped types mm | Seating plane height H for uncrimped types mm | Pitch distance P ₀ mm |
|---|-------------|-----------|--|---|--|
| 0 | – | Bulk | – | – | – |
| 1 | G | I | 16 | 18 | 12.7 |
| 2 | G2 | I | 18 | – | 12.7 |
| 3 | G3 | II | 16 | 18 | 12.7 |
| 4 | G4 | II | 18 | – | 12.7 |
| 5 | G5 | III | 16 | 18 | 12.7 |
| 6 | GA | Ammo pack | 16 | 18 | 12.7 |
| 7 | G2A | Ammo pack | 18 | – | 12.7 |
| Internal coding for special taping | | | | | |
| | G6 | III | 18 | – | 12.7 |
| | G10 | II | 16 | 18 | 15.0 |
| | G11 | II | 18 | – | 15.0 |
| | G10A | Ammo pack | 16 | 18 | 15.0 |
| | G11A | Ammo pack | 18 | – | 15.0 |



2.5 Reel dimension

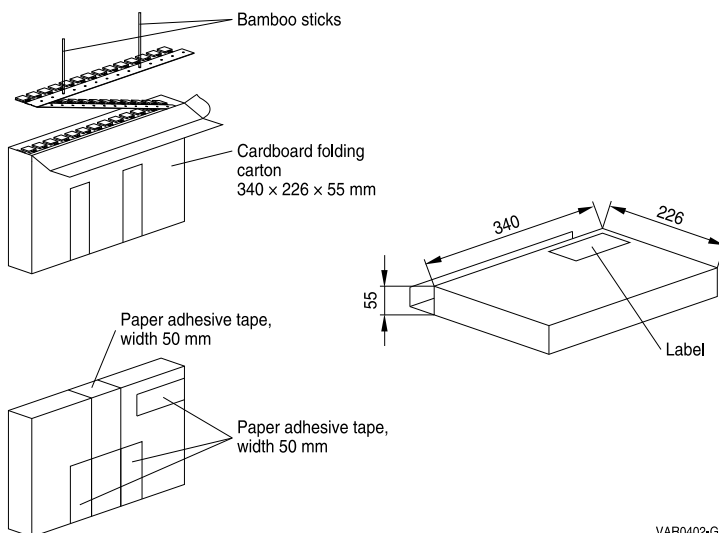


Dimensions (in mm)

| Reel type | d | f | n | w |
|-----------|----------|-------|------------|---------|
| I | 360 max. | 31 ±1 | approx. 45 | 54 max. |
| II | 360 max. | 31 ±1 | approx. 55 | 64 max. |
| III | 500 max. | 23 ±1 | approx. 59 | 72 max. |

If reel type III is not compatible with insertion equipment because of its large diameter, nominal disk diameter 10 mm and 14 mm can be supplied on reel II upon request (taping mode G3).

2.6 Ammo pack dimensions





3 Lead configuration

Straight leads are standard for disk varistors. Other lead configurations as crimp style or customer-specific lead wire length according to 3.1, 3.2, 3.3 and 3.4 are optional. Crimped leads (non-standard) are differently crimped for technical reasons; the individual crimp styles are denoted by consecutive numbers (S, S2 through S5) as shown in the dimensional drawings below.

The crimp styles of the individual types can be seen from the type designation in the ordering tables.

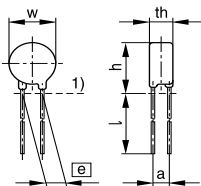
3.1 Crimp style mode

Example: B72210S0271K **5**01
Digit 13

| Digit 13 of ordering code | Crimp style | Figure |
|---------------------------|--------------------------|--------|
| 1 | Standard, straight leads | 1 |
| 2 | S2 | 2 |
| 3 | S3 | 3 |
| 4 | S4 | 4 |
| 5 | S5 | 5 |
| Available upon request | | |
| Internal coding | — | 6 |

3.2 Standard leads and non-standard crimp styles

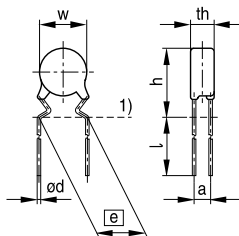
Standard, straight leads



1) Seating plane to IEC 717
VAR0586-W-E

Figure 1

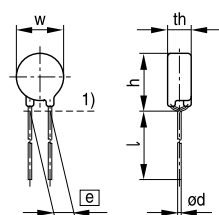
Non-standard, crimp style S2



1) Seating plane to IEC 60717
VAR0411-F-E

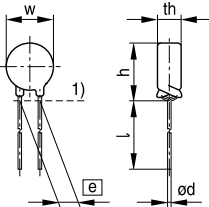
Figure 2

Non-standard, crimp style S3

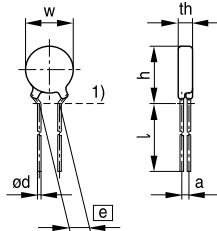


1) Seating plane to IEC 60717
VAR0396-R-E

Figure 3


Non-standard, crimp style S4


1) Seating plane to IEC 60717
VAR0404-W-E

Non-standard, crimp style S5


1) Seating plane to IEC 60717
VAR0412-N-E

Figure 4
Figure 5

3.3 Component height (h_{max}) for crimped versions (non-standard)

Due to technical reasons the component height (h_{max}) increases if a crimp is added. The maximum height of the crimped component can be found in the table below.

| Nominal diameter mm | V_{RMS} V | Crimp style | e mm | h_{max} mm |
|------------------------|-----------------------|-------------|-----------|-----------------|
| 5 | 11 ... 175 | S2 | 5.0 | 10.0 |
| 5 | 210 ... 460 | S3 | 5.0 | 10.0 |
| 7 | 11 ... 175 | S2 | 5.0 | 12.0 |
| 7 | 210 ... 460 | S3 | 5.0 | 12.0 |
| 10 | 11 ... 300 | S5 | 7.5 | 15.5 |
| 10 | 320 ... 460 | S3/S5 | 7.5 | 16.5 |
| 10 | 510 | S3/S5 | 7.5 | 17.5 |
| 10 | Automotive | S5 | 7.5 | 17.0 |
| 10 | Automotive (D1 types) | S5 | 7.5 | 16.0 |
| 10 | 11 ... 175 | S4 | 5.0 | 16.5 |
| 10 | 210 ... 460 | S3 | 5.0 | 16.5 |
| 14 | 11 ... 300 | S5 | 7.5 | 20.0 |
| 14 | 320 ... 460 | S3/S5 | 7.5 | 20.0 |
| 14 | 510 | S3/S5 | 7.5 | 21.5 |
| 14 | Automotive | S5 | 7.5 | 21.0 |
| 14 | Automotive (D1 types) | S5 | 7.5 | 20.0 |
| 20 | 11 ... 320 | S5 | 10.0 | 27.0 |
| 20 | 385 ... 510 | S5 | 10.0 | 27.5 |



3.4 Trimmed leads (non-standard)

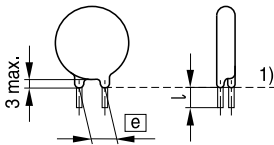
Varistors with cut leads available upon request.

Lead length tolerances:

Straight leads ± 1.0 mm

Crimped leads ± 0.8 mm

Minimum lead length 3.5 mm



1) Seating plane to IEC 60717

VAR0642-U-E

Figure 6



Cautions and warnings

General

1. EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

1. Store SIOVs only in original packaging. Do not open the package before storage.
2. Storage conditions in original packaging:
Storage temperature: $-25\text{ }^{\circ}\text{C} \dots +45\text{ }^{\circ}\text{C}$,
Relative humidity: $<75\%$ annual average,
 $<95\%$ on maximum 30 days a year.
Dew precipitation: is to be avoided.
3. Avoid contamination of an SIOV's during storage, handling and processing.
4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
5. The SIOV type series should be soldered within the time specified:
SIOV-S, -Q, -LS, -B, -SFS 24 months
ETFV 12 months.

Handling

1. SIOVs must not be dropped.
2. Components must not be touched with bare hands. Gloves are recommended.
3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

1. Use rosin-type flux or non-activated flux.
2. Insufficient preheating may cause ceramic cracks.
3. Rapid cooling by dipping in solvent is not recommended.
4. Complete removal of flux is recommended.



Mounting

1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

1. Use SIOVs only within the specified temperature operating range.
2. Use SIOVs only within the specified voltage and current ranges.
3. Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.


Symbols and terms

| Symbol | Term |
|--------------------|--|
| C | Capacitance |
| C_{typ} | Typical capacitance |
| i | Current |
| i_c | Current at which $V_{c, max}$ is measured |
| I_{leak} | Leakage current |
| i_{max} | Maximum surge current (also termed peak current) |
| I_{max} | Maximum discharge current to IEC 61643-1 |
| I_{nom} | Nominal discharge current to IEC 61643-1 |
| LCT | Lower category temperature |
| L_{typ} | Typical inductance |
| P_{max} | Maximum average power dissipation |
| R_{ins} | Insulation resistance |
| R_{min} | Minimum resistance |
| T_A | Ambient temperature |
| t_r | Duration of equivalent rectangular wave |
| UCT | Upper category temperature |
| v | Voltage |
| V_{clamp} | Clamping voltage |
| $V_{c, max}$ | Maximum clamping voltage at specified current i_c |
| V_{DC} | DC operating voltage |
| V_{jump} | Maximum jump start voltage |
| V_{max} | Maximum voltage |
| V_{op} | Operating voltage |
| V_{RMS} | AC operating voltage, root-mean-square value |
| $V_{RMS, op, max}$ | Root-mean-square value of max. DC operating voltage incl. ripple current |
| V_{surge} | Super imposed surge voltage |
| V_V | Varistor voltage |
| ΔV_V | Tolerance of varistor voltage |
| W_{LD} | Maximum load dump |
| W_{max} | Maximum energy absorption |
| e | Lead spacing |

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

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