Qualcom

RF360 Europe GmbH

SAW components

SAW RF filter Short range devices

Version:

Series/type:	B3419
Ordering code:	B39921B3419U410
Date:	September 11, 2017

2.1

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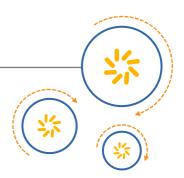
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SAW RF filter	924.15 MHz

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SAW RF filter

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- 1 Application
- Low insertion attenuation
- Low amplitude ripple
- Usable pass band 7.1 MHz

2 Features

- Package size 3.0±0.1 mm × 3.0±0.1 mm
- Package height 1.1±0.125 mm
- Package code DCC6C
- Approximate weight 0.04 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Lead free soldering compatible with J-STD20C
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 1 (MSL1)
- AEC-Q200 qualified component family (Grade 1: -40 °C to +125 °C)

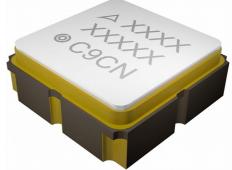


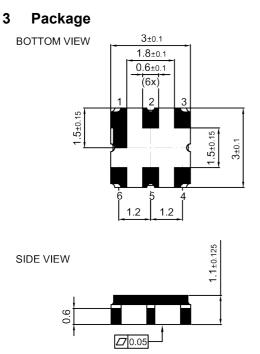
Figure 1: Picture of component with example of product marking.

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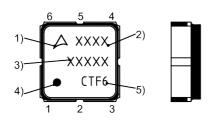


4 Pin configuration

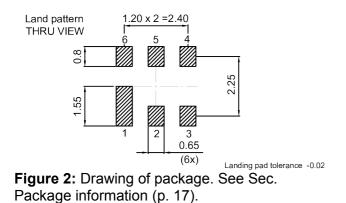
- 2 Input
- 5 Output
- 1, 3, 4, 6 Ground



SIDE VIEW



- 1) Company logo
- 2) Device designation
- 3) Last five digits of the lot number
- 4) Marking for pad number 1
- 5) Example of production location and date code





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5 Matching circuit

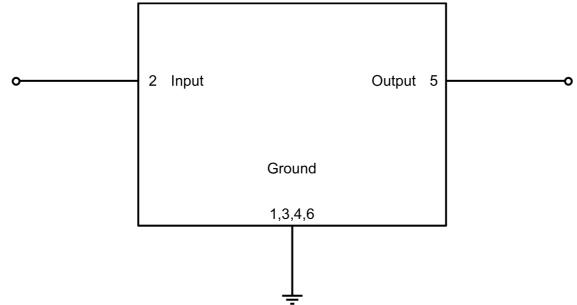


Figure 3: Schematic of matching circuit. No external matching components required.

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6 Characteristics

Temperature range for specification	$T_{_{\rm SPEC}}$	= −30 °C +75 °C
Input terminating impedance	Z _{IN}	= 50 Ω
Output terminating impedance	Z _{OUT}	= 50 Ω

Characteristics				min. for $T_{_{\rm SPEC}}$	typ. @ +25 °C	max. for T _{SPEC}	
Center frequency			f _c	—	924.15	—	MHz
Maximum insertion attenuation			$\alpha_{_{max}}$				
	920.6 927.7	MHz		_	2.0	2.8	dB
Amplitude ripple (p-p)			Δα				
	920.6 927.7	MHz		—	0.5	1.5	dB
Maximum VSWR			$VSWR_{max}$				
@ input port	920.6 927.7	MHz		_	1.5	2.0	
@ output port	920.6 927.7	MHz		—	1.6	2.0	
Minimum attenuation			$\alpha_{_{min}}$				
	10 900	MHz		40	49		dB
	900 910	MHz		15	36	—	dB
	910 915	MHz		3	10	—	dB
	945 950	MHz		35	55	—	dB
	950 960	MHz		50	55	—	dB
	960 1150	MHz		40	48	—	dB

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7 Maximum ratings

Operable temperature	T _{OP} = −45 °C +125 °C	
Storage temperature	<i>T</i> _{STG} ¹⁾ = −45 °C +125 °C	
DC voltage	$ V_{\rm DC} = 6.0 \rm V$	
Input power @ input port: 920.6 927.7 MHz	$P_{\rm IN} = 22 \rm dBm$	Continuous wave for 5000 h @ 50 °C.

¹⁾ Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C.

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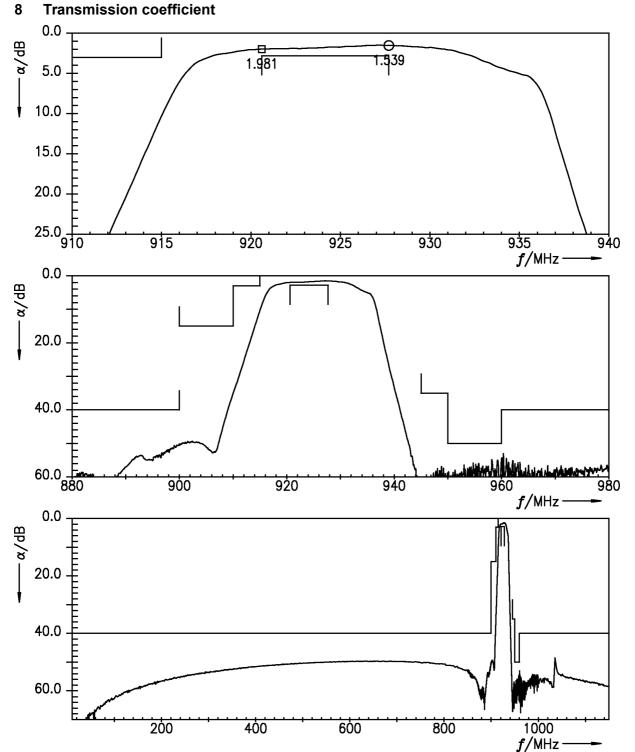


Figure 4: Attenuation.



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□ = 920.6 O = 927.7

□ = 920.6 O = 927.7

Z_{IN}=50 Ω

Z_{OUT}=50 Ω

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9 Reflection coefficients

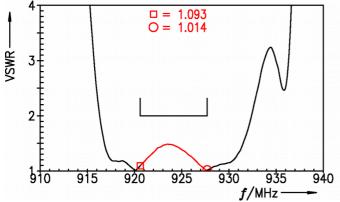
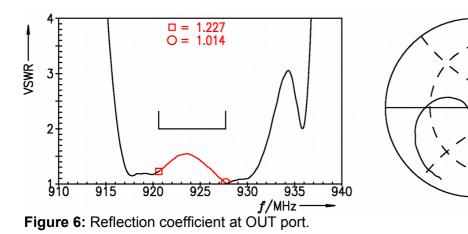


Figure 5: Reflection coefficient at IN port.



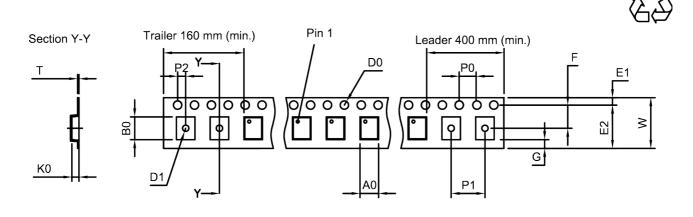
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10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	3.25±0.1 mm
B ₀	3.3±0.1 mm
D ₀	1.5+0.1/-0 mm
D ₁	1.5 mm (min.)
E ₁	1.75±0.1 mm
	1

Table 1: Tape dimensions.

E2	10.25 mm (min.)
F	5.5±0.05 mm
G	0.75 mm (min.)
K ₀	1.5±0.1 mm
P ₀	4.0±0.1 mm

P ₁	4.0±0.1 mm
P ₂	2.0±0.1 mm
Т	0.2±0.05 mm
W	12.0+0.3/-0.1 mm

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10.2 Reel with diameter of 330 mm

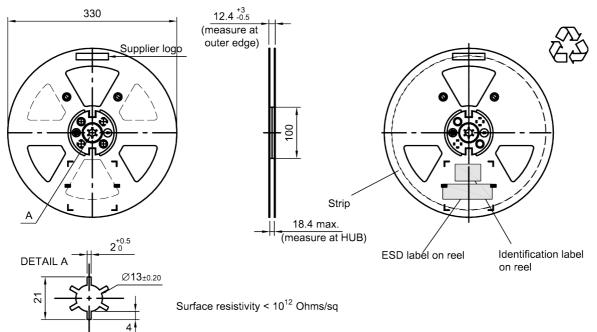


Figure 8: Drawing of reel (first-angle projection) with diameter of 330 mm.

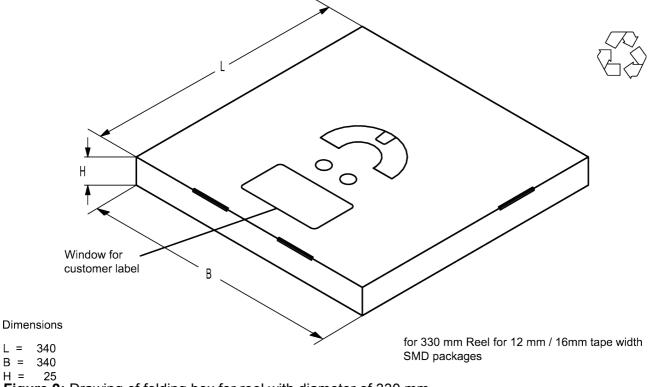


Figure 9: Drawing of folding box for reel with diameter of 330 mm.

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11 Marking

Products are marked with device designation, lot number, as well as production location and date code.

Device designation: The 4-character device designation of the ordering code is used for the marking.

Example for 4-character device designation: B3xxxxB1234xxxx

■ Lot number: The last 5 digits of the lot number are used for the marking.

Example: 12345

Production location and date code: The production location is Wuxi (encoded in the first character 'C'). The production date code is encoded in the last three characters according to Table 2.

1 st digit (day)					2 nd digit (year)			3 rd digit (month)					
Day	Code	Day	Code	Day	Code	Year	Code	Year	Code	Month	Code	Month	Code
1	1	11	А	21	М	2010	А	2022	Р	Jan	1	Jul	7
2	2	12	В	22	N	2011	В	2023	R	Feb	2	Aug	8
3	3	13	С	23	Р	2012	С	2024	S	Mar	3	Sep	9
4	4	14	D	24	R	2013	D	2025	Т	Apr	4	Oct	0
5	5	15	E	25	S	2014	Е	2026	U	May	5	Nov	Ν
6	6	16	F	26	Т	2015	F	2027	V	Jun	6	Dec	D
7	7	17	н	27	U	2016	Н	2028	W				
8	8	18	J	28	V	2017	J	2029	Х				
9	9	19	к	29	W	2018	К	2030	Z				
10	0	20	L	30	Х	2019	L	2031	А				
				31	Z	2020	М	2032	В				
						2021	Ν	and	so on				

 Table 2: Production date code.

Example of how to decode production location and date code:

Code:	CTF6	
Location:	С	→ Wuxi
Day:	Т	$\rightarrow 26^{th}$
Year:	F	→ 2015
Month:	6	\rightarrow June

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12 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s	
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s	
<i>T</i> > 220 °C	30 s to 70 s	
<i>T</i> > 230 °C	min. 10 s	
<i>T</i> > 245 °C	max. 20 s	
<i>T</i> ≥ 255 °C	-	
peak temperature T _{peak}	250 °C +0/-5 °C	
wetting temperature T _{min}	230 °C +5/-0 °C for 10 s ± 1 s	
cooling rate	≤ 3 K/s	
soldering temperature T	measured at solder pads	

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

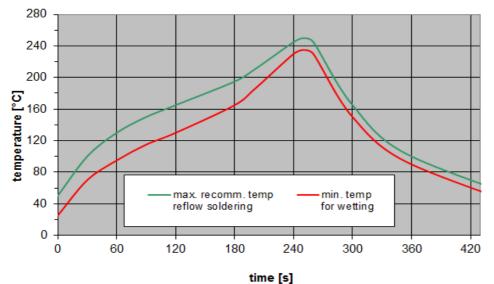


Figure 10: Recommended reflow profile for convection and infrared soldering – lead-free solder.

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13 ESD protection of SAW filters

SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

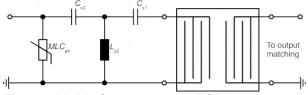


Figure 11: MLC varistor plus ESD matching.

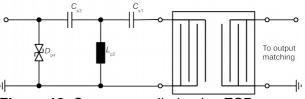
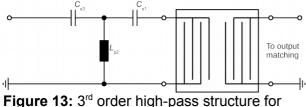


Figure 12: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.



basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: **"ESD protection for SAW filters".** This report can be found under <u>www.rf360jv.com/rke</u>. Click on "Applications Notes".

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14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog <u>http://www.tdk.co.jp/tefe02/coil.htm#aname1</u> and Data Library for circuit simulation <u>http://www.tdk.co.jp/etvcl/index.htm</u>.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.



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15 Cautions and warnings

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15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Projection method

Unless otherwise specified first-angle projection is applied.

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