WP3DP3BT/BD-P22

Phototransistor



DESCRIPTION

· Made with NPN silicon phototransistor chips

FEATURES

- · Mechanically and spectrally matched to the infrared emitting LED lamp
- · Black diffused lens
- Daylight filter
- RoHS compliant

APPLICATIONS

- Infrared applied systems
- Optoelectronic switches
- Photodetector control circuits
- Sensor technology

PACKAGE DIMENSIONS



Notes

- Notes: 1. All dimensions are in millimeters (inches). 2. Tolerance is ±0.25(0.01") unless otherwise noted. 3. Lead spacing is measured where the leads emerge from the package. 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice

\$0.9x2

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Max.Ratings	Units
Collector-to-Emitter Voltage	30	V
Emitter-to-Collector Voltage	5	V
Power Dissipation at (or below) 25°C Free Air Temperature	100	mW
Operating Temperature	-40 to +85	°C
Storage Temperature	-40 to +85	°C
Lead Soldering Temperature(>5mm for 5sec)	260	°C

Note: 1. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Min.	Тур.	Max.	Units	Test Conditions
Collector-to-Emitter Breakdown Voltage	V _{BR CEO}	30	-	-	V	I _c = 100uA Ee = 0mW/cm ²
Emitter-to-Collector Breakdown Voltage	V _{BR ECO}	5	-	-	V	I _E = 100uA Ee = 0mW/cm ²
Collector-to-Emitter Saturation Voltage	V _{CE (SAT)}	-	-	0.8	V	$I_{\rm C}$ = 2mA Ee = 20mW/cm ²
Collector Dark Current	I _{CEO}	-	-	100	nA	V_{CE} = 10V Ee = 0mW/cm ²
Rise Time(10% to 90%)	T _R	-	15	-	μS	V _{CE} = 5V IC = 1mA RL = 1000Ω
Fall Time(90% to 10%)	T _F	-	15	-	μS	
On State Collector Current	I _(ON)	0.1	0.2	-	mA	$V_{CE} = 5V$ Ee = 1mW/cm ² $\lambda = 940$ nm
Range of spectral bandwidth	λ _{0.1}	670	-	1070	nm	-
Wavelength of peak sensitivity	λ _p	-	940	-	nm	-
Angle of half sensitivity	201/2	-	50	-	deg	-

RELATIVE SPECTRAL SENSITIVITY vs. WAVELENGTH



RELATIVE RADIANT SENSITIVITY vs. ANGULAR DISPLACEMENT



TECHNICAL DATA

Collector Current vs. Irradiance





Relative Collector Current vs. Ambient Temperature



TECHNICAL DATA

PHOTOTRANSISTOR



XXXXXXXXXX-XXXX

1 RoHS Compliant

E: XXXX (4L) COO: XX

ITY: XXXXXXXXXXXXXXXX

Outside

Labe

PRECAUTIONS

Storage conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- Product in the original sealed package is recommended to be assembled within 72 hours of opening.
 Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

 The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. Note 1-3: Do not route PCB trace in the contact area between the

leadframe and the PCB to prevent short-circuits.

LEC LED LED Note 1 Note.2 Note.3 45°Max. 45 \bigcirc \cap LED LED LED PCB X X X \times Correct mounting method " X " Incorrect mounting method

2. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure.





- 3. Use stand-offs (Fig. 1) or spacers (Fig. 2) to securely position the LED above the PCB.
- 4. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend (*Fig. 3 , Fig. 4*).
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (*Fig. 5*)

Lead Forming Procedures

- 1. Do not bend the leads more than twice. (Fig. 6)
- 2. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering. (*Fig.* 7)
- 3. The tip of the soldering iron should never touch the lens epoxy.
- 4. Through-hole LEDs are incompatible with reflow soldering.
- If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.

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PRECAUTIONARY NOTES

- 1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- 2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
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- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.