

System Lens Drivers





μ -step System Lens Driver for Digital Still Cameras

BU24038GW

General Description

BU24038GW is a system Lens Driver that uses μ -step driving to make the configuration of the sophisticated, high precision and low noise lens driver system possible. This IC has a built-in driver for both DC motor and voice coil motor and a μ -step controller that decreases CPU power. Therefore, multifunctional lens can be applied.

■Features

- Built-in 9 channels Driver block
 1ch-8ch: Voltage control type H-bridge
 (Adaptable to STM 3systems)
 9ch: Current control type H-bridge
- Built-in 3 channels PI driving circuit
- Built-in 4 channels Waveforming circuit
- Built-in PLL circuit

Applications

■ Digital still cameras

Key Specifications

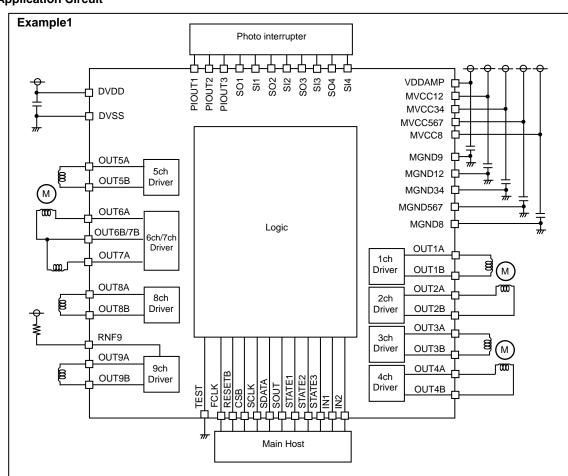
Digital Power Supply Voltage:
 Driver Power Supply Voltage:
 Output Current (1ch-9ch):
 Input Clock Frequency:
 FET ON Resistance (1ch-8ch):
 FET ON Resistance (9ch):
 Operating Temperature Range:
 2.7V to 3.6V
 ±500mA(Max)
 1MHz to 28MHz
 1.5Ω(Typ)
 1.0Ω(Typ)
 -20°C to +85°C

Package

USCP75M3

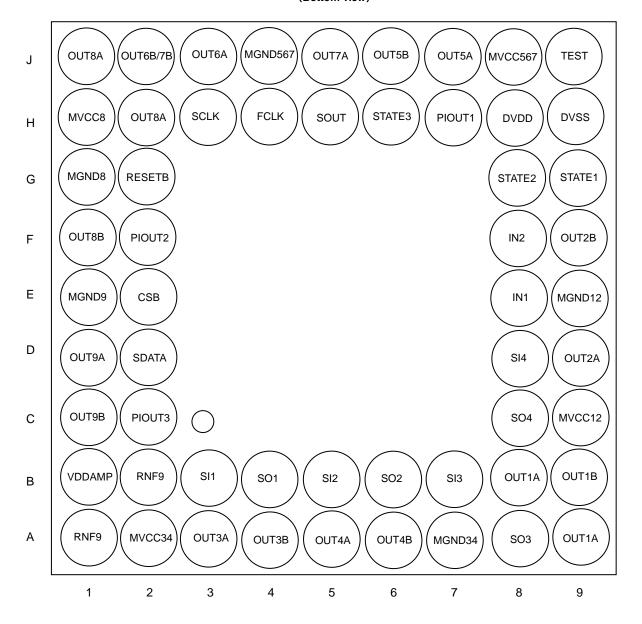
3.80mm x 3.80mm x 0.85mm

● Typical Application Circuit



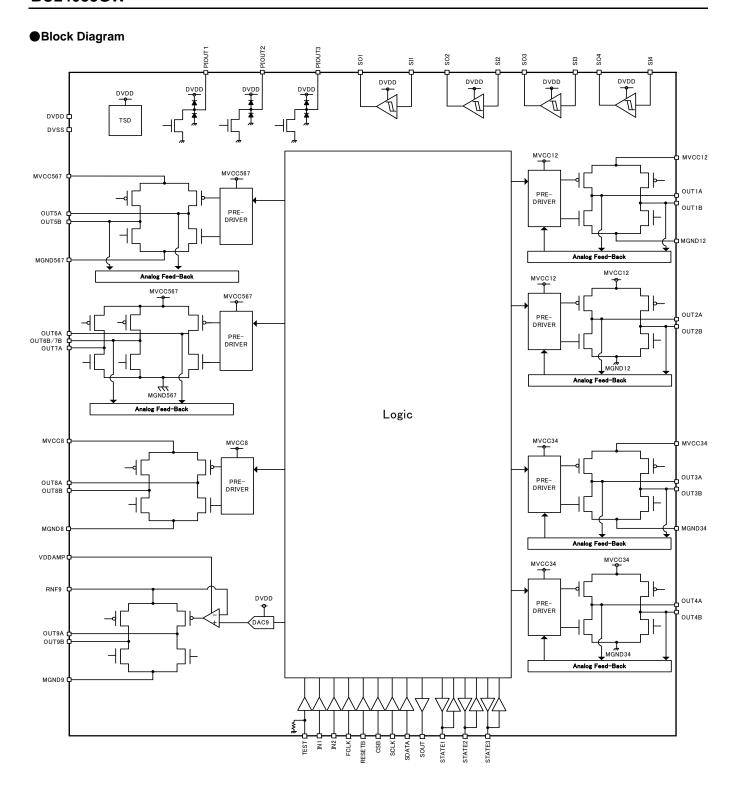
Pin Configuration

(Bottom view)



●Pin Description

●Pin De	escription						
Land Matrix No.	Pin Name	Power Supply	Function	Land Matrix No.	Pin Name	Power Supply	Function
H8	DVDD	-	Digital power supply	A9, B8	OUT1A	MVCC12	1ch Driver A output
H9	DVSS	-	ground	В9	OUT1B	MVCC12	1ch Driver B output
G2	RESETB	DVDD	RESETB logic input	D9	OUT2A	MVCC12	2ch Driver A output
H4	FCLK	DVDD	FCLK logic input	F9	OUT2B	MVCC12	2ch Driver B output
E2	CSB	DVDD	CSB logic input	A2	MVCC34	-	3ch, 4ch Driver power supply
НЗ	SCLK	DVDD	SCLK logic input	A7	MGND34	-	3ch, 4ch Driver ground
D2	SDATA	DVDD	SDATA logic input	А3	OUT3A	MVCC34	3ch Driver A output
H5	SOUT	DVDD	SOUT logic output	A4	OUT3B	MVCC34	3ch Driver B output
E8	IN1	DVDD	IN1 logic input	A5	OUT4A	MVCC34	4ch Driver A output
F8	IN2	DVDD	IN2 logic input	A6	OUT4B	MVCC34	4ch Driver B output
G9	STATE1	DVDD	STATE1 logic input/output	J8	MVCC567	-	5ch, 6ch, 7ch Driver power supply
G8	STATE2	DVDD	STATE2 logic input/output	J4	MGND567	-	5ch, 6ch, 7ch Driver ground
H6	STATE3	DVDD	STATE3 logic input/output	J7	OUT5A	MVCC567	5ch Driver A output
J9	TEST	DVDD	TEST logic input	J6	OUT5B	MVCC567	5ch Driver B output
H7	PIOUT1	DVDD	PI driving output 1	J3	OUT6A	MVCC567	6ch Driver A output
F2	PIOUT2	DVDD	PI driving output 2	J2	OUT6B/7B	MVCC567	6ch, 7ch Driver B output
C2	PIOUT3	DVDD	PI driving output 3	J5	OUT7A	MVCC567	7ch Driver A output
В3	SI1	DVDD	Waveforming input1	H1	MVCC8	-	8ch Driver power supply
B4	SO1	DVDD	Waveforming output1	G1	MGND8	-	8ch Driver ground
B5	SI2	DVDD	Waveforming input2	H2, J1	OUT8A	MVCC8	8ch Driver A output
В6	SO2	DVDD	Waveforming output2	F1	OUT8B	MVCC8	8ch Driver B output
В7	SI3	DVDD	Waveforming input3	B1	VDDAMP	-	9ch Power supply of current driver control
A8	SO3	DVDD	Waveforming output3	A1, B2	RNF9	-	9ch Driver power supply
D8	SI4	DVDD	Waveforming input4	E1	MGND9	-	9ch Driver ground
C8	SO4	DVDD	Waveforming output4	D1	OUT9A	RNF9	9ch Driver A output
C9	MVCC12	-	1ch, 2ch Driver power supply	C1	OUT9B	RNF9	9ch Driver B output
E9	MGND12	-	1ch, 2ch Driver ground				



Description of Blocks

Stepping Motor Driver (1ch-6ch Driver)

Built-in stepping motor driver of PWM driving type.

Maximum 3 stepping motors can be driven independently.

Built-in voltage feedback circuit of D-class type.

3ch/4ch drivers can also drive independently for DC motor or voice coil motor.

(1) Control

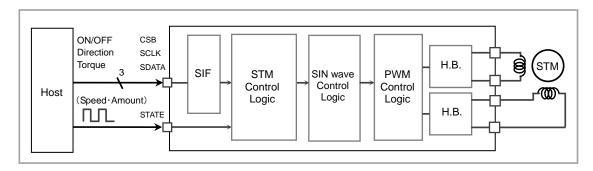
It corresponds to both Clock IN and Autonomous control.

(i)Clock IN Control

Set the resistors for the stepping motor control.

The stepping motor is rotated and synchronized with the input clock in the STATE pin.

It is possible to select the mode of stepping motor control from μ -step, 1-2 phase excitation, 2 phase excitation and the number of edge for electrical angle cycle from 4, 8, 32, 64, 128, 256, 512 or 1024.



(ii) Autonomous Control

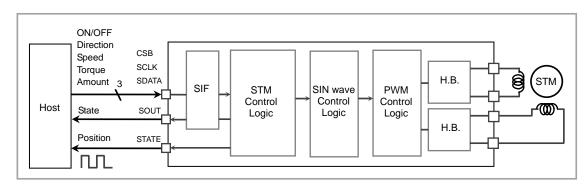
The stepping motor is rotated by setting the resistors for the stepping motor control.

The state of rotation command (executing:1, finished:0), Cache resistor, and motor position are the output from the serial output (SOUT pin). Also, the signal (MO output) which is synchronized with the motor rotation is the output from STATE pin.

It is possible to select the mode of stepping motor control from μ -step (1024 portion), 1-2 phase excitation and 2 phase excitation

Built-in Cache resistors.

Cache resisters enable the setting of subsequent process while the motor is in operation. Through these registers, operations are done continuously.



Description of Blocks

Voltage Driver (7ch, 8ch Driver)

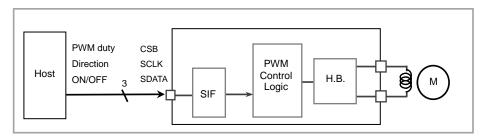
Built-in voltage driver of PWM driving type.

(1) Control

(i)Register Control

■at speed control = OFF

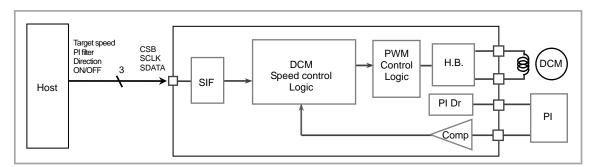
The PWM drive is executed by the PWM duty ratio, the PWM direction and the PWM ON/OFF which are controlled by the register settings.



■8ch Driver (at speed control = ON)

The speed control drive is executed by the target speed value, the direction, the coefficient value of PI filter and the turning ON/OFF which are controlled by the register settings.

The motor speed is adjusted by comparing the target speed with the motor speed detected at the signal of photo-interrupter.



Description of Blocks

Current Driver (9ch Driver)

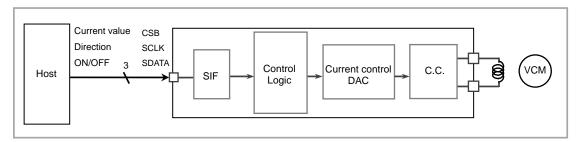
Built-in constant current driver.

The voltage of RNF pin and the external resistor (RRNF) determine the amount of output current. The internal high-precision amplifier (CMOS gate input) is used for the constant current control. If any resistance component exists in the wirings of RNF pin and the external resistor (RRNF), the precision can be reduced. To avoid this, pay utmost attention to the wirings.

(1) Control

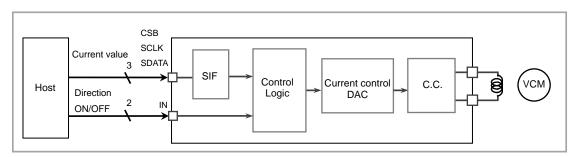
(i)Resistor Control

The constant current drive is executed by the output current value, the current direction and the current ON/OFF which are controlled by the register settings.



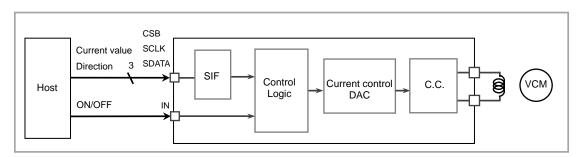
(ii) External Pin Control1

The constant current drive is executed by the output current value which is controlled by the register setting. Constant current direction and turning ON/OFF are controlled by IN pin.



(iii) External Pin Control2

The constant current drive is executed by the output current value and the current direction which are controlled by the register setting. Constant current driving ON/OFF is controlled by IN pin.



● Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limit	Unit	Remark
	DVDD	-0.3 to +4.5	V	
Power Supply Voltage	MVCC	-0.3 to +7.0	V	MVCC12,MVCC34,MVCC567, MVCC8, VDDAMP
Input Voltage	VIN	-0.3∼supply voltage+0.3	V	
Input / Output Current *1	IIN	±500	mA	MVCC12,MVCC34,MVCC567, MVCC8, RNF9
		+50	mA	by PIOUT pin
Storage Temperature Range	TSTG	-55 to +125	°C	
Operating Temperature Range	TOPE	-20 to +85	°C	
Permissible Dissipation *2	PD	1300	mW	

^{*1} Must not exceed PD.

● Recommended Operating Rating (Ta=25°C)

econlinenced Operating (Ta=23 O)								
Parameter	Symbol	Symbol Limit		Remark				
Digital Power Supply Voltage	DVDD	2.7 to 3.6	٧	DVDD≦MVCC				
Driver Power Supply Voltage	MVCC	2.7 to 5.5	V	MVCC12,MVCC34,MVCC567, MVCC8, VDDAMP				
Clock Operating Frequency	FCLK	1 to 28	MHz	Reference clock				

^{*2} To use at a temperature higher than Ta=25 °C, derate 13mW per 1 °C (At mounting 50mm x 58mm x 1.75mm glass epoxy board.)

●Electrical Characteristic

	11 0 0, 18=2		5°C, DVDD=3.0V, MVCC=5.0V, DVSS =MGND= Limit				G
Parameter		Symbol	MIN	TYP	MAX	Unit	Conditions
<current consumption:<="" td=""><td>></td><td></td><td></td><td></td><td></td><td></td><td></td></current>	>						
Quiescence (D	VDD)	ISSD	-	0	10	μΑ	CMD_RS=0
(N	(IVCC)	ISSM	-	0	10	μΑ	
Operation (D\	VDD)	IDDD	-	14	19	mA	RESETB=H, CMD_RS=STB=CLK_EN=1 FCLK=24MHz CLK_DIV setting : 0h, No load
<logic block=""></logic>							
Low-level Input Voltage)	VIL	DVSS	-	0.3DVDD	V	
High-level Input Voltage	е	VIH	0.7DVDD	-	DVDD	V	
Low-level Input Current	t	IIL	0	-	10	μΑ	VIL=DVSS
High-level Input Curren	nt	IIH	0	-	10	μΑ	VIH=DVDD
Low-level Output Voltag	ge	VOL	DVSS	-	0.2DVDD	V	IOL=1.0mA
High-level Output Volta	ige	VOH	0.8DVDD	-	DVDD	V	IOH=1.0mA
<pi circuit="" driving=""></pi>		,					
Output Voltage		PIVO	-	0.15	0.5	V	IIH=30mA
<waveforming circuit=""></waveforming>							
High-level Threshold Vo	oltage	VthH	-	-	1.9	V	DVDD=3.25V
Low-level Threshold Vo	oltage	VthL	0.9	-	-	V	DVDD=3.25V
Hysteresis Width		Vhys	0.2	-	0.6	V	DVDD=3.25V
<voltage 1<="" block="" driver="" td=""><td>1ch-6ch></td><td></td><td></td><td></td><td></td><td></td><td></td></voltage>	1ch-6ch>						
ON-resistance		Ron	-	1.5	2.0	Ω	IO=±100mA (the sum of high and low sides)
OFF-leak Current		IOZ	-10	0	+10	μΑ	Output Hiz setting
Average Voltage Accurate between different Outp		Vdiff	-5	-	+5	%	Vdiff setting : 2Bh
<voltage 7<="" block="" driver="" td=""><td>7ch,8ch></td><td></td><td></td><td></td><td></td><td></td><td></td></voltage>	7ch,8ch>						
ON-resistance		Ron	-	1.5	2.0	Ω	IO=±100mA (the sum of high and low sides)
OFF-leak Current		IOZ	-10	0	+10	μΑ	Output Hiz setting
<current block<="" driver="" td=""><td>9ch></td><td></td><td></td><td></td><td></td><td>•</td><td></td></current>	9ch>					•	
ON-resistance		Ron	-	1.0	1.5	Ω	IO=±100mA (the sum of high and low sides)
OFF-leak Current		IOZ	-10	0	+10	uA	Output Hiz setting
Output Current		Ю	190	200	210	mA	DAC setting : 80h RRNF=1[Ω]

●Typical Performance Curves

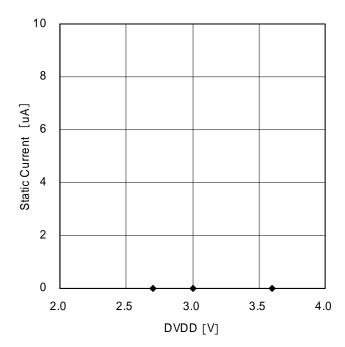


Figure 1. DVDD Static Current Voltage Dependency

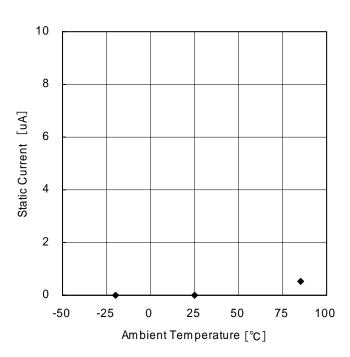


Figure 2. DVDD Static Current Temperature Dependency

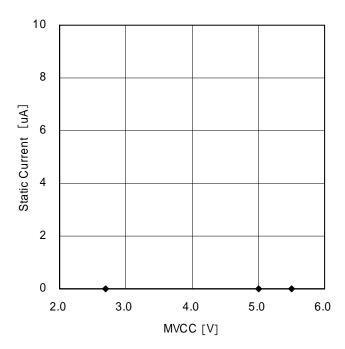


Figure 3. MVCC Static Current Voltage Dependency

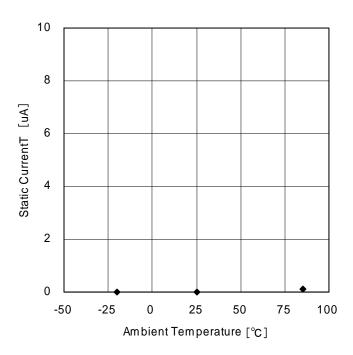


Figure 4. MVCC Static Current Temperature Dependency

●Typical Performance Curves

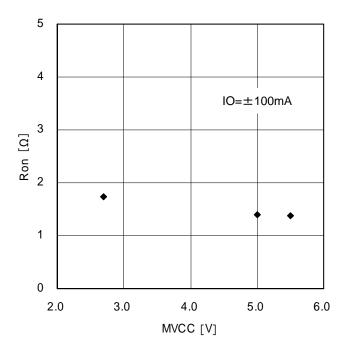


Figure 5. Output ON-Resistance MVCC Dependency (Voltage driver block)

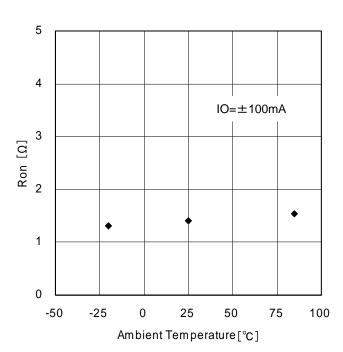


Figure 6. Output ON-Resistance
Temperature Dependency
(Voltage driver block)

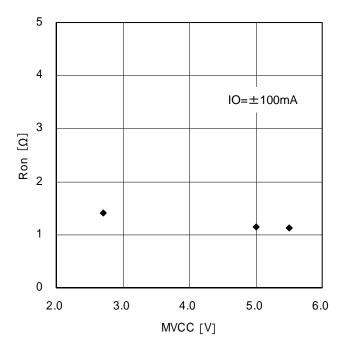


Figure 7. Output ON-Resistance MVCC Dependency (Current driver block)

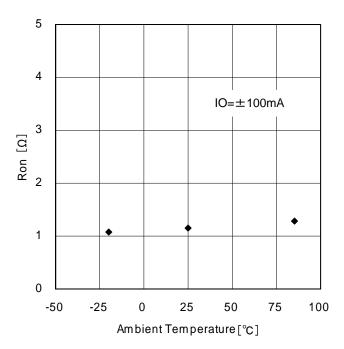


Figure 8. Output ON-Resistance
Temperature Dependency
(Current driver block)

●Typical Performance Curves

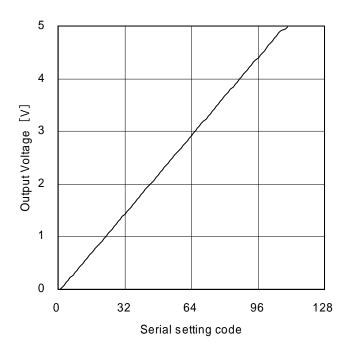


Figure 9. Average Voltage Accuracy between different output pins (Voltage driver block)

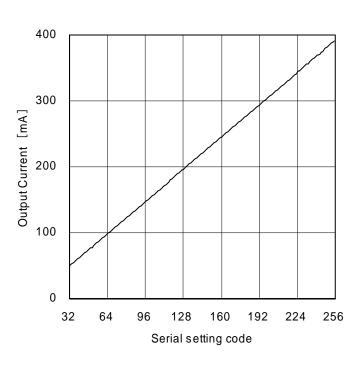


Figure 10. Output Current $\label{eq:current} \mbox{(Current driver block, RRNF = $1.0\,\Omega\,), RL = $5.0\,\Omega\,)}$

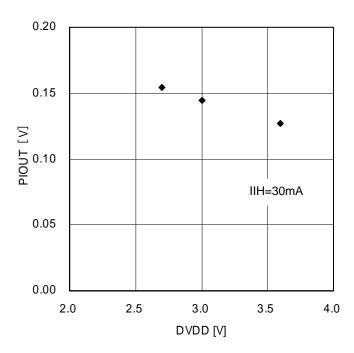


Figure 11. Output Voltage DVDD Dependency (PI driving circuit)

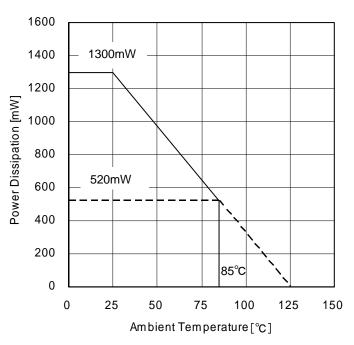
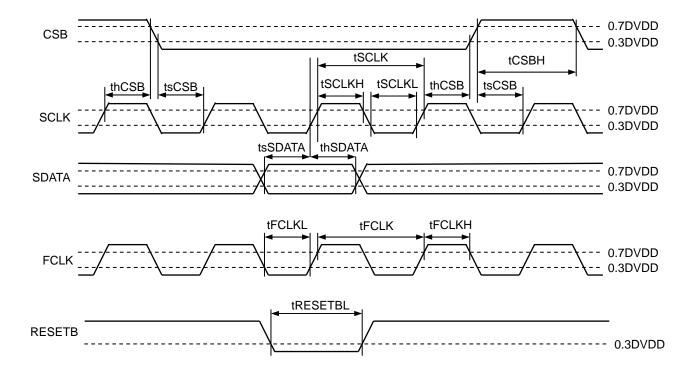


Figure 12. Power Dissipation Curve

Timing Chart

(Unless otherwise specified, Ta=25°C, DVDD=3.0V)

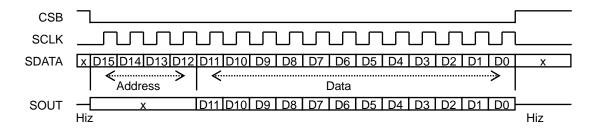
Parameter	Symbol	Specification
SCLK input cycle	tSCLK	More than 100 nsec
SCLK L-level input time	tSCLKL	More than 50 nsec
SCLK H-level input time	tSCLKH	More than 50 nsec
SDATA setup time	tsSDATA	More than 50 nsec
SDATA hold time	thSDATA	More than 50 nsec
CSB H-level input time	tCSBH	More than 380 nsec
CSB setup time	tsCSB	More than 50 nsec
CSB hold time	thCSB	More than 50 nsec
RESETB L-level input time	tRESETBL	More than 350 nsec
FCLK input cycle	tFCLK	More than 36 nsec
FCLK L-level input time	tFCLKL	More than 18 nsec
FCLK H-level input time	tFCLKH	More than 18 nsec



(note1) RESETB and FCLK are asynchronous with SCLK. (note2) Duty of FCLK, SCLK are free.

Serial interface

Control commands are framed by a 16-bit serial input (MSB first) and are sent through CSB, SCLK, and SDATA pins. The 4 higher-order bits specify addresses, while the remaining 12 bits specify data. Data of every bit is sent through SDATA pin, which is retrieved during the rising edge of SCLK. Data becomes valid when CSB is Low and is registered during the rising edge of CSB. Furthermore, the interface will be synchronized with the falling edges of SCLK to output the SOUT data of the 12 bits.



<Register map>

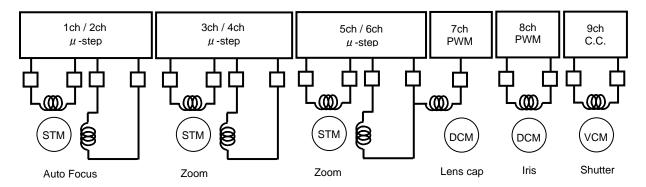
tegis	ster	map)/	1											
Α	ddre	ss[3:	0]						Data	[11:0]					
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0	A_Mo	de[1:0]		A_SEL[2:0] A_different_output_voltage[6:0]								
				0	0	0	0			A_Cyc	cle[5:0]			0	0
0	0	_	4	0	0	1	0	A_Cycle[13:6]							
0		0 1	0 1	0	1	1	0	A_BEXC	0	0	A_BSL	A_AEXC	0	0	A_ASL
				1	1	1	0	0	0	A_PC	S[1:0]	0	0	A_PS	A_Stop
0	0	1	0	A_EN	A_RT					A_Pul	se[9:0]				
0	0	1	1	A_ACT	A_BUSY	B_ACT	B_BUSY	C_ACT	C_BUSY	L	L	L	L	L	L
0	1	0	0	B_Mo	de[1:0]		B_SEL[2:0]			B_differer	nt_output_v	oltage[6:0]		
				0	0	0	0			B_Cyc	de[5:0]			0	0
				0	0	1	0				B_Cyc	le[13:6]		l	
				0	1	1	0	B_BEXC	0	0	B_BSL	B_AEXC	0	0	B_ASL
0	1	0	1	1	0	0	0	0	0	3_CH0	DP[1:0]	0	0	4_CH	OP[1:0]
				1	0	1	3_State_	CTL[1:0]			3_F	PWM_Duty	[6:0]		
				1	1	0	4_State_	CTL[1:0]			4_F	PWM_Duty	[6:0]		
				1	1	1	0	0	0	B_PC	S[1:0]	0	0	B_PS	B_Stop
0	1	1	0	B_EN	B_RT		1	I	I.	B_Pul	se[9:0]	I	I.	I	
0	1	1	1		A_Posit	tion[9:6]			B_Posit	tion[9:6]			C_Posi	tion[9:6]	
1	0	0	0	C_Mo	de[1:0]		C_SEL[2:0]			C_differer	nt_output_v	oltage[6:0]		
				0	0	0	0		l	C_Cyc	cle[5:0]			0	0
		_		0	0	1	0				C_Cyc	:le[13:6]			
1	0	0	1	0	1	1	0	C_BEXC	0	0	C_BSL	C_AEXC	0	0	C_ASL
				1	1	1	0	0	0	C_PC	S[1:0]	0	0	C_PS	C_Stop
1	0	1	0	C_EN	C_RT					C_Pul	se[9:0]				
				0	0	0	0	0	0	Edge	0	0	C_CTL	B_CTL	A_CTL
1	0	1	1	0	0	1	0	0	0	0	67_SEL	0	Е	XT_CTL[2:	:0]
1	1	0	0	0	0	Chopp	ing[1:0]	CacheM	0	0	CLK_EN		CLK_E	DIV[3:0]	
				0	0	0	0	0	0	0	0	0	PI_CTL3	PI_CTL2	PI_CTL1
				0	0	1	0	0	0	0	0	0	0	7_CH	OP[1:0]
				0	1	0	7_State_	CTL[1:0]			7_F	PWM_Duty	[6:0]		
				0	1	1	0		I .		8_TAR	SP[7:0]			
1	1	0	1	0	1	1	1	0		8_PSP[2:0]	0		8_ISP[2:0]]
				1	0	0	0	0	0	0	0	0	0	SPC_L	imit[1:0]
				1	0	1	0	0	0	0	8_SPEN	0	0	8_CH	OP[1:0]
				1	1	0	8_State_	_CTL[1:0]			8_F	PWM_Duty	[6:0]	I.	
				0	0	0	0		II.		9_101	JT[7:0]			
				0	1	0	0	0	0	0	0	0	0	9_State_	_CTL[1:0]
1	1	1	0	1	0	1	0	0	0	0	0	HYS4	HYS3	HYS2	HYS1
				1	1	0	0	0	0	0	STB	0	0	STM_RS	CMD_RS
Add	dress	es of	her		1	1	1	1			I	1	I .		
		se ab							Setting p	orohibited					
				•											

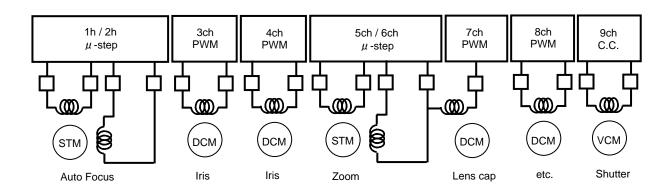
⁽Note 1) The notations A, B, and C in the register map correspond to A-channel, B-channel and C-channel respectively.
(Note 2) The A-channel is defined as 1chl and 2ch driver output, the B-channel as 3ch and 4ch driver output, the C-channel as 5ch and 6ch driver output.

 ⁽Note 3) After reset, the initial condition is saved in all registers.
 (Note 4) The addresses 4'b0011, and 4'b0111 have data (ACT, BUSY, Position [9:6]), which are internal register values and output from SOUT pin.
 (Note 5) For Mode, different output voltage, Cycle, EN, and RT registers, data that are written before the access to the Pulse register becomes valid and determines the rising edge of CSB after the access to Pulse register.
 (The Mode, different output voltage, Cycle, EN, RT, and Pulse registers contain Cache registers. Any registers other than those do not contain Cache

registers.)

Application Example





●I/O Equivalence Circuit

I/O Equivalence	Circuit		
Pin	Equivalent Circuit Diagram	Pin	Equivalent Circuit Diagram
RESETB FCLK CSB SCLK SDATA IN1 IN2	DVDD DVDD	TEST (note1)	DVDD DVDD
SOUT	DVDD DVDD P	STATE1 STATE2 STATE3	DVDD DVDD DVDD
SI1 SI2 SI3 SI4	DVDD DVDD	SO1 SO2 SO3 SO4	DVDD DVDD P
PIOUT1 PIOUT2 PIOUT3	DVDD P	OUT1A OUT1B OUT2A OUT2B	MVCC1
OUT3A OUT3B OUT4A OUT4B	MVCC3	OUT5A OUT5B OUT6A OUT6B/7B OUT7A	MVCC567

(note1) Short TEST pin to DVSS.

●I/O Equivalence Circuit

Pin	Equivalent Circuit Diagram	Pin	Equivalent Circuit Diagram
OUT8A OUT8B	MVCC8	OUT9A OUT9B	RNF9

Operational Notes

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you expect that any voltage or temperature could be exceeding the absolute maximum ratings, take physical safety measures such as fuses to prevent any conditions exceeding the absolute maximum ratings from being applied to the LSI.

2) GND potential

The voltage of the ground pin must be the lowest voltage of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

Thermal design

Use a thermal design that allows for a sufficient margin by taking into account the permissible power dissipation (PD) in actual operating conditions.

4) Short circuit between pins and malfunctions

Ensure that when mounting the IC on the PCB the direction and position are correct. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

5) Operation in strong magnetic field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

6) Power ON sequence

To turn ON the DVDD, be sure to reset at RESETB pin.

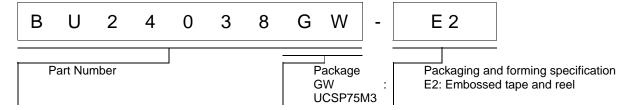
7) Thermal shutdown

The IC incorporates a built-in thermal shutdown circuit, which is designed to turn off the IC when the internal temperature of the IC reaches a specified value. It is not designed to protect the IC from damage or guarantee its operation. Do not continue to operate the IC after this function is activated. Do not use the IC in conditions where this function will always be activated.

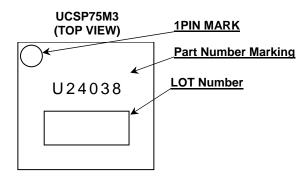
8) PI drive circuit

The output voltage of PIOUT should not exceed the voltage of the power supply voltage DVDD.

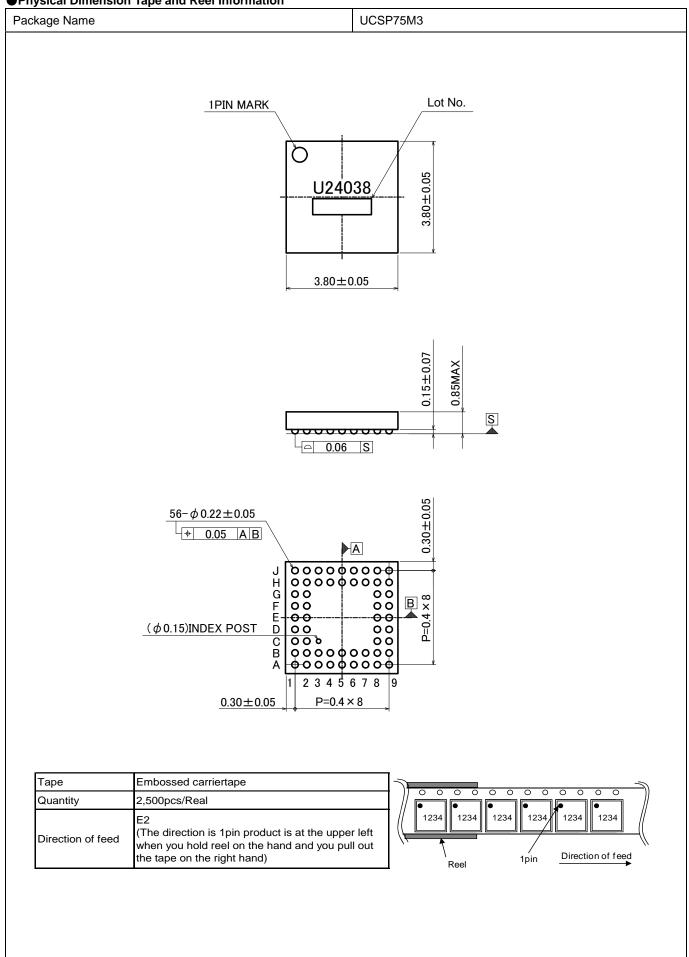
Ordering Information



Marking Diagram



● Physical Dimension Tape and Reel Information



Revision History

Date	Revision	Changes
26.Sep.2012	001	New Release
18.Apr.2013	002	Update some English words, sentences, descriptions, grammar and formatting

Notice

Precaution on using ROHM Products

Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ	
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII	

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

Precaution Regarding Intellectual Property Rights

- 1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data. ROHM shall not be in any way responsible or liable for infringement of any intellectual property rights or other damages arising from use of such information or data.:
- 2. No license, expressly or implied, is granted hereby under any intellectual property rights or other rights of ROHM or any third parties with respect to the information contained in this document.

Other Precaution

- 1. This document may not be reprinted or reproduced, in whole or in part, without prior written consent of ROHM.
- 2. The Products may not be disassembled, converted, modified, reproduced or otherwise changed without prior written consent of ROHM.
- 3. In no event shall you use in any way whatsoever the Products and the related technical information contained in the Products or this document for any military purposes, including but not limited to, the development of mass-destruction weapons.
- The proper names of companies or products described in this document are trademarks or registered trademarks of ROHM, its affiliated companies or third parties.

General Precaution

- 1. Before you use our Products, you are requested to care fully read this document and fully understand its contents. ROHM shall not be in an y way responsible or liable for failure, malfunction or accident arising from the use of a ny ROHM's Products against warning, caution or note contained in this document.
- 2. All information contained in this docume nt is current as of the issuing date and subject to change without any prior notice. Before purchasing or using ROHM's Products, please confirm the latest information with a ROHM sale s representative.
- 3. The information contained in this doc ument is provided on an "as is" basis and ROHM does not warrant that all information contained in this document is accurate an d/or error-free. ROHM shall not be in an y way responsible or liable for any damages, expenses or losses incurred by you or third parties resulting from inaccuracy or errors of or concerning such information.

Notice – WE © 2014 ROHM Co., Ltd. All rights reserved. Rev.001



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: <u>org@eplast1.ru</u>

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.