

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild guestions@onsemi.com.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer





FPF1048 IntelliMAX™ 3 A-Capable, Slew-Rate-Controlled Load Switch with True Reverse Current Blocking

Features

- Input Voltage Operating Range: 1.5 V to 5.5 V
- Typical R_{DS(ON)}:
 - 21 m Ω at V_{IN}=5.5 V
 - 23 m Ω at V_{IN}=4.5 V
 - 41 mΩ at V_{IN}=1.8 V
 - 90 m Ω at $V_{IN}=1.5 \text{ V}$
- Slew Rate/Inrush Control with t_R: 2.7 ms (Typ.)
- 3 A Maximum Continuous Current Capability
- Low Off Switch Current: <1 µA</p>
- True Reverse Current Blocking (TRCB)
- Logic CMOS IO Meets JESD76 Standard for GPIO Interface and Related Power Supply Requirements
- ESD Protected:
 - Human Body Model: >8 kV
 - Charged Device Model: >1.5 kV
 - IEC 61000-4-2 Air Discharge: >15 kV
 - IEC 61000-4-2 Contact Discharge: >8 kV

Applications

- Smart Phones, Tablet PCs
- Storage, DSLR, and Portable Devices

Description

The FPF1048 advanced load management switch targets applications requiring a highly integrated solution. It disconnects loads powered from the DC power rail (<6 V) with stringent off-state current targets and high load capacitances (up to 100 μF). The FPF1048 consists of slew-rate controlled low-impedance MOSFET switch (23 m Ω typical) and integrated analog features. The slew-rate controlled turn-on characteristic prevents inrush current and the resulting excessive voltage droop on power rails.

The FPF1048 has a True Reverse Current Blocking (TRCB) function that obstructs unwanted reverse current from V_{OUT} to V_{IN} during both ON and OFF states. The exceptionally low off-state current drain (<1 μ A maximum) facilitates compliance with standby power requirements. The input voltage range operates from 1.5 V to 5.5 V_{DC} to support a wide range of applications in consumer, optical, medical, storage, portable, and industrial-device power management. Switch control is managed by a logic input (active HIGH) capable of interfacing directly with low-voltage control signal / General-Purpose Input / Output (GPIO) without an external pull-down resistor.

The device is packaged in advanced, fully "green" compliant, 1.0 mm x 1.5 mm, Wafer-Level Chip-Scale Package (WLCSP) with backside lamination.

Ordering Information

Part Number	Top Mark	Switch R _{ON} (Typical) at 4.5V _{IN}	Input Buffer	Output Discharge	ON Pin Activity	t _R	Package
FPF1048BUCX	RA	23 mΩ	CMOS	NA	Active HIGH	2.7 ms	6-Ball, WLCSP with Backside Laminate, 2x3 Array, 0.5 mm Pitch, 300 μm Balls

Application Diagram

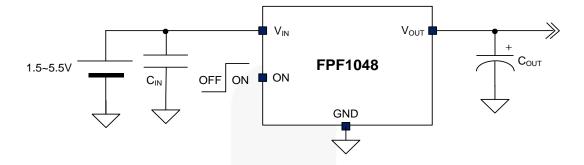


Figure 1. General Application

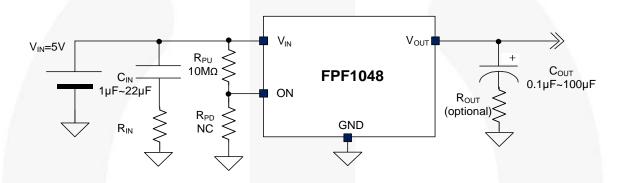


Figure 2. Specific Application with 10 $M\Omega$ Pull-Up Resistor at ON Pin

Notes:

- 1. Turn-on operation with a 10 M Ω pull-up resistor at ON pin is acceptable.
- 2. V_{IN} should be high enough to generate V_{ON} greater than V_{IH} at the ON pin.
- 3. NC means no connection.
- 4. R_{IN} and R_{OUT} can be added to reduce transient peak voltage. 1 Ω ~10 Ω is recommended.

Functional Block Diagram

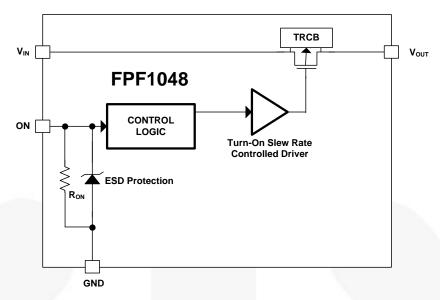


Figure 3. Functional Block Diagram

Pin Configurations

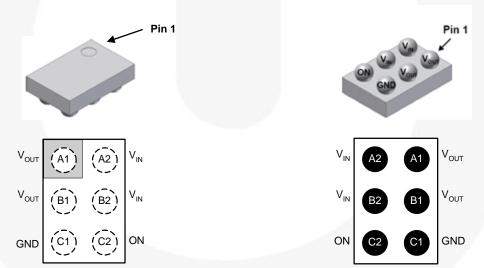


Figure 4. Pin Assignments (Top View)

Figure 5. Pin Assignments (Bottom View)

Pin Description

Pin #	Name	Description		
A1, B1	V _{OUT}	Switch Output		
A2, B2	V_{IN}	Supply Input: Input to the Power Switch		
C1	GND	Ground		
C2	ON	ON/OFF Control, Active High, GPIO Compatible		

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters			Min.	Max.	Unit
V _{IN}	V _{IN} , V _{OUT} , V _O	_N to GND		-0.3	6.0	V
I _{SW}	Maximum Continuous Switch Current				3.0	Α
P _D	Power Dissip	Power Dissipation at T _A =25°C			1.2	W
T _{STG}	Storage Junction Temperature			-65	+150	°C
T _A	Operating Temperature Range			-40	+85	°C
Θ_{JA}	Thermal Resistance, Junction-to-Ambient				85 ⁽⁵⁾ 110 ⁽⁶⁾	°C/W
	Human Body Model, JESD22-A114					
ESD Electrostation Discharge Capability	Charged Devic	e Model, JESD22-C101	1.5		kV	
	Capability	IEC61000-4-2	Air Discharge (V _{IN} , V _{ON} , V _{OUT} to GND)	15.0		K.V
		Contact Discharge (V _{IN,} V _{ON,} V _{OUT} to GND)	8.0			

Notes:

- 5. Measured using 2S2P JEDEC std. PCB.
- 6. Measured using 2S2P JEDEC PCB cold plate method.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameters		Тур.	Max.	Unit
V_{IN}	Input Voltage			5.5	V
T _A	Ambient Operating Temperature			+85	°C
I _{SW}	Continuous Switch Current		2.5	3	Α

Electrical Characteristics

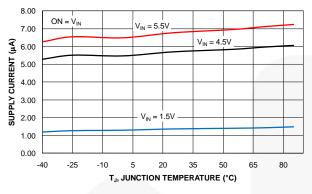
Unless otherwise noted, V_{IN} =1.5 to 5.5 V, T_A =-40 to +85°C; typical values are at V_{IN} =4.5 V and T_A =25°C.

Symbol	Parameters	Conditions	Min.	Тур.	Max.	Units			
Basic Op	eration			•		•			
V_{IN}	Input Voltage		1.5		5.5	V			
$I_{Q(OFF)}$	Off Supply Current	V _{ON} =GND, V _{OUT} =Open			1	μA			
I_{SD}	Shutdown Current	V_{ON} =GND, V_{OUT} =GND, T_A = -40 to +85°C		0.2	4.0	μΑ			
ΙQ	Quiescent Current	I _{OUT} =0 mA			11	μΑ			
		V_{IN} =5.5 V, I_{OUT} =3 $A^{(7)}$		22.0					
		$V_{IN}=5.5 \text{ V}, I_{OUT}=2 \text{ A}^{(7)}$		21.5					
		V _{IN} =5.5 V, I _{OUT} =1 A, T _A =25°C	28.0						
		V _{IN} =4.5 V, I _{OUT} =3 A ⁽⁷⁾		24.0					
Б	On Registeres	V _{IN} =4.5 V, I _{OUT} =2 A ⁽⁷⁾		23.5		m0			
R _{ON}	On Resistance	V _{IN} =4.5 V, I _{OUT} =1 A, T _A =25°C		23.0	30.0	mΩ			
		V _{IN} =3.3 V, I _{OUT} =500 mA, T _A =25°C		26.0					
- //	30	V _{IN} =2.5 V, I _{OUT} =500 mA, T _A =25°C		30.0					
		V _{IN} =1.8 V, I _{OUT} =250 mA, T _A =25°C		41.0					
		V _{IN} =1.5 V, I _{OUT} =250 mA, T _A =25°C		90.0	110.0				
V _{IH}	ON Input Logic High Voltage	V _{IN} =1.5 V to 5.5 V	1.15			V			
\ /	ONLIN THE STATE OF	V _{IN} =1.8 V to 5.5 V			0.65	V			
V_{IL}	ON Input Logic Low Voltage	V _{IN} =1.5 V to 1.8 V			0.60	V			
I _{ON}	ON Input Leakage	V _{ON} = V _{IN} or GND			1.0	μA			
R _{ON_PD}	Pull-Down Resistance at ON Pin	$V_{IN} = V_{ON} = 1.5 \text{ V to } 5.5 \text{ V}, T_A = -40 - +85^{\circ}\text{C}$	6.38	7.65	8.86	ΜΩ			
True Rev	erse Current Blocking								
V _{T_RCB}	RCB Protection Trip Point	V _{OUT} - V _{IN}		45		mV			
V _{R_RCB}	RCB Protection Release Trip Point	V _{IN} -V _{OUT}		25		mV			
	RCB Hysteresis			70		mV			
I _{SD_OUT}	V _{OUT} Shutdown Current	$V_{ON} = 0$, $V_{OUT} = 4.5$ V, $V_{IN} = $ Short to GND			2	μA			
t _{RCB_ON}	RCB Response Time, Device ON	V _{OUT} - V _{IN} =100 mV, V _{ON} = HIGH		4		μs			
t _{RCB_OFF}	RCB Response Time, Device OFF	V _{OUT} - V _{IN} =100 mV, V _{ON} = LOW		2.5		μs			
Dynamic	Characteristics								
t _{DON}	Turn-On Delay ^(8,9)			1.7		ms			
t _R	V _{OUT} Rise Time ^(8,9)	V_{IN} =4.5 V, R_L =5 Ω , C_L =100 μ F, T_A =25°C		2.7		ms			
ton	Turn-On Time ^(8,9)			4.4		ms			
t _{DON}	Turn-On Delay ^(8,9)			1.7		ms			
t _R	V _{OUT} Rise Time ^(8,9)	V_{IN} =4.5 V, R _L =150 Ω , C _L =100 μ F, T _A =25°C		1.5		ms			
ton	Turn-On Time ^(8,9)			3.2		ms			
t _{DOFF}	Turn-Off Delay ^(8,10)			1.8		ms			
t _F	V _{OUT} Fall Time ^(8,10)	V_{IN} =4.5 V, R _L =150 Ω , C _L =100 μ F, T _A =25°C		34		ms			
t _{OFF}	Turn-Off Time ^(8,10)	1A-23 G		35		ms			

Notes:

- 7. This parameter is guaranteed by design and characterization; not production tested.
- 8. $t_{DON}/t_{DOFF}/t_R/t_F$ are defined in Figure 22.
- 9. $t_{ON}=t_R + t_{DON}$.
- 10. $t_{OFF}=t_F+t_{DOFF}$.

Typical Characteristics



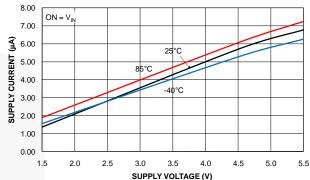
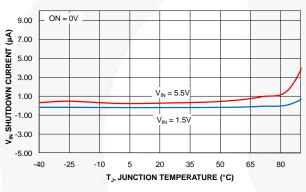


Figure 6. Supply Current vs. Temperature

Figure 7. Supply Current vs. Supply Voltage



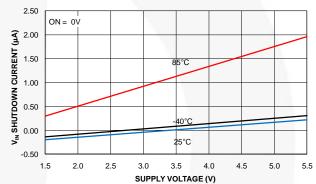
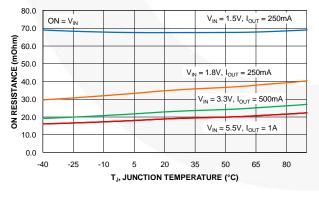


Figure 8. Shutdown Current vs. Temperature

Figure 9. Shutdown Current vs. Supply Voltage



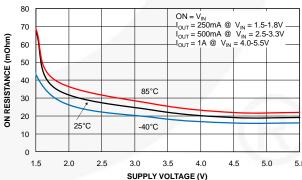
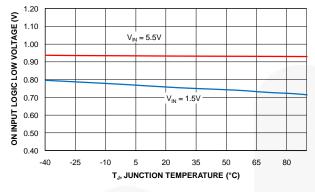


Figure 10. Ron vs. Temperature

Figure 11. Ron vs. Supply Voltage

Typical Characteristics



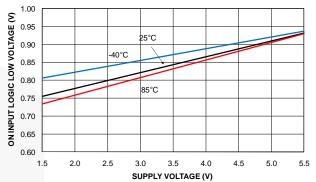
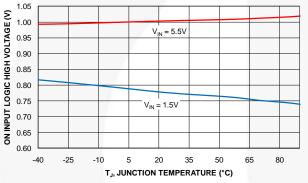


Figure 12. V_{IL} vs. Temperature

Figure 13. V_{IL} vs. Supply Voltage



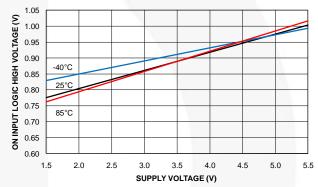


Figure 14. V_{IH} vs. Temperature

Figure 15. V_{IH} vs. Supply Voltage

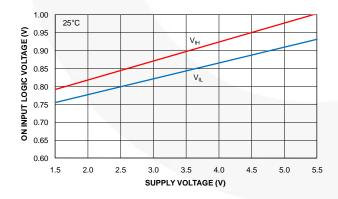
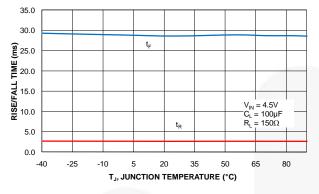


Figure 16. On Pin Threshold vs. Supply Voltage

Typical Characteristics



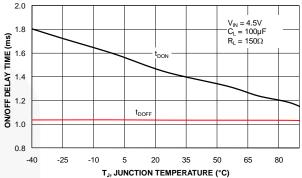
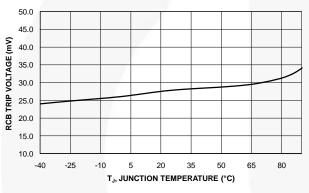


Figure 17. t_R / t_F vs. Temperature

Figure 18. t_{DON} vs. Temperature



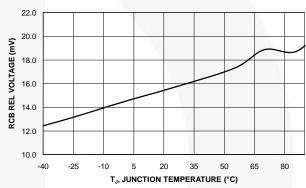
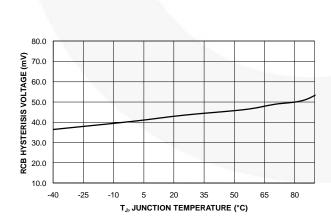


Figure 19. RCB Trip vs. Temperature

Figure 20. RCB Release vs. Temperature





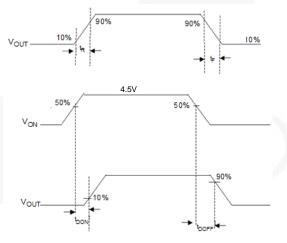
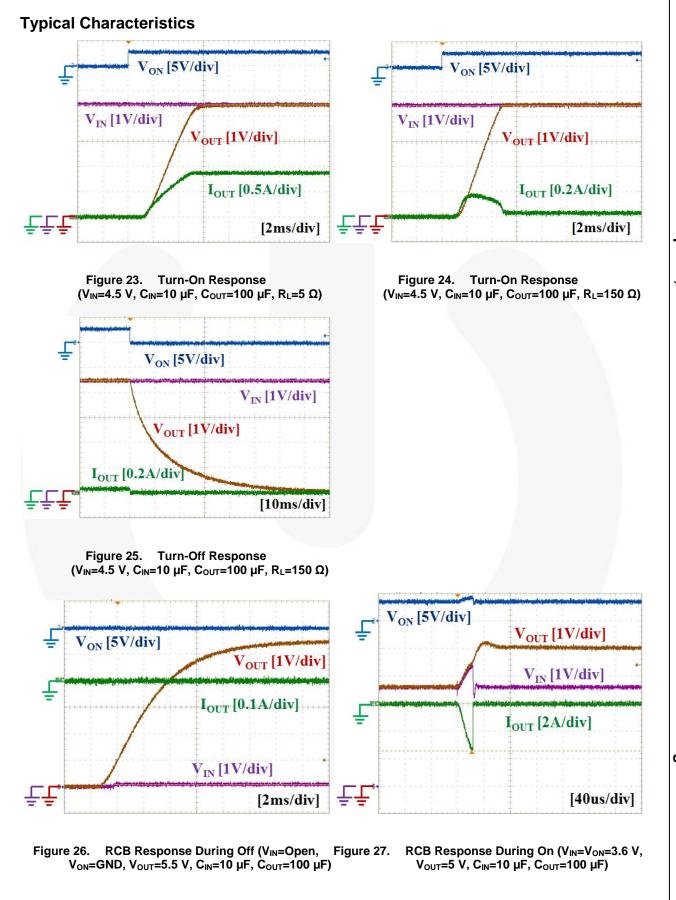


Figure 22. Timing Diagram



Operation and Application Description

The FPF1048 is a low-R_{ON} P-channel load switch with controlled turn-on and True Reverse Current Blocking (TRCB). The core is a 23 m Ω P-channel MOSFET and controller capable of functioning over a wide input operating range of 1.5 to 5.5 V. The ON pin, an active-HIGH, GPIO/CMOS-compatible input; controls the state of the switch. TRCB functionality blocks unwanted reverse current during both ON and OFF states when higher V_{OUT} than V_{IN} is applied.

Input Capacitor

To limit the voltage drop on the input supply caused by transient inrush current when the switch turns on into a discharged load capacitor; a capacitor must be placed between the V_{IN} and GND pins. At least 1 μ F ceramic capacitor, C_{IN} , placed close to the pins is usually sufficient. Higher-value C_{IN} can be used to reduce the voltage drop in higher-current applications.

Inrush Current

Inrush current occurs when the device is turned on. Inrush current is dependent on output capacitance and slew rate control capability, as expressed by:

$$I_{\mathit{INRUSH}} = C_{\mathit{OUT}} \times \frac{V_{\mathit{IN}} - V_{\mathit{INITIAL}}}{t_{\mathit{R}}} + I_{\mathit{LOAD}} \tag{1}$$

where:

C_{OUT}: Output capacitance;

 t_R : Slew rate or rise time at V_{OUT} ;

V_{IN}: Input voltage;

V_{INITIAL}: Initial voltage at C_{OUT}, usually GND; and

ILOAD: Load current.

Higher inrush current causes higher input voltage drop, depending on the distributed input resistance and input capacitance. High inrush current can cause problems.

FPF1048 has a 2.7 ms of slew rate capability under 4.5 V_{IN} at 1000 μ F of C_{OUT} and 5 Ω of R_L so inrush current can be minimized and no input voltage drop appears. Table 1 and Figure 28 show the values and actual waveforms with C_{IN} =10 μ F, C_{OUT} =100 μ F, and no load current.

Table 1. Inrush Current by Input Voltage

		Inrush Current [mA]			
V _{IN} [V]	t _R [ms]	Measured	Calculated with 2.7 ms t _R		
1.5	1.62	76	56		
3.3	2.03	140	122		
5.0	2.33	196	185		

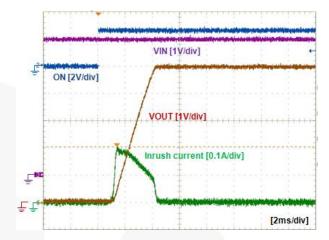


Figure 28. Inrush Current Waveform, Under 5 V_{IN}, C_{OUT}=100 μF, no Load

Output Capacitor

At least 0.1 μ F capacitor, C_{OUT} , should be placed between the V_{OUT} and GND pins. This capacitor prevents parasitic board inductance from forcing V_{OUT} below GND when the switch is on.

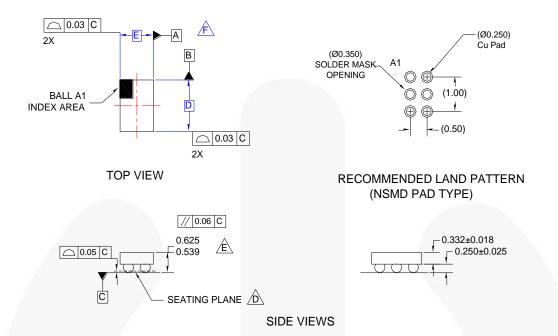
True Reverse Current Blocking

The true reverse current blocking feature protects the input source against current flow from output to input regardless of whether the load switch is on or off.

Board Layout

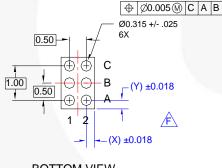
For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effect that parasitic trace inductance on normal and short-circuit operation. Using wide traces or large copper planes for all pins (V_{IN} , V_{OUT} , ON, and GND) minimizes the parasitic electrical effects and the case-to-ambient thermal impedance.

Physical Dimensions



NOTES:

- A. NO JEDEC REGISTRATION APPLIES.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCE PER ASMEY14.5M, 1994.
- D. DATUM C IS DEFINED BY THE SPHERICAL CROWNS OF THE BALLS.
- E. PACKAGE NOMINAL HEIGHT IS 582 MICRONS ±43 MICRONS (539-625 MICRONS).
- FOR DIMENSIONS D, E, X, AND Y SEE PRODUCT DATASHEET.
- G. DRAWING FILNAME: MKT-UC006AFrev2.



BOTTOM VIEW

6-Ball WLCSP, 2x3 Array, 0.5 mm Pitch, 300 µm Ball

Product-Specific Dimensions

Product	D	E	х	Υ	
FPF1048BUCX	1460 µm ±30 µm	960 μm ±30 μm	230 µm	230 µm	





The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

F-PFS™ AccuPower[™] AttitudeEngine™ FRFET[®] Global Power Resource SM Awinda[®] AX-CAP®* GreenBridge™ BitSiC™ Green FPS™ Build it Now™ Green FPS™ e-Series™

CorePLUS™ Gmax™ CorePOWER™ GTO™ CROSSVOLT™ IntelliMAX™ CTI™ ISOPLANAR™ Current Transfer Logic™ Making Small Speakers Sound Louder

DEUXPEED[®] Dual Cool™ EcoSPARK® EfficientMax™ ESBC™

Fairchild[®] Fairchild Semiconductor® FACT Quiet Series™ FACT FAST® Fast∨Core™

MicroPak2™ MillerDrive™ Motion Max™ Motion Grid® MTi® $MTx^{^{\circledR}}$ MVN® mWSaver® FETBench™ OptoHiT™ **OPTOLOGIC®** OPTOPLANAR®

Power Supply WebDesigner™

PowerTrench^o PowerXS™

Programmable Active Droop™ OFET

OSTM Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™

SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™

SPM® STEALTH™ SuperFET[®] SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM BY GENERAL BY TinyBoost[®] TinyBuck[®]

TinyCalc™ TinyLogic[®] TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®* uSerDes™

UHC® Ultra FRFET™ UniFET[™] VCX™ VisualMax™ VoltagePlus™ XS™ Xsens™ 仙竜™

* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

and Better[⊤]

MICROCOUPLER™

MegaBuck™

MicroFET™

MicroPak™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. TO OBTAIN THE LATEST, MOST UP-TO-DATE DATASHEET AND PRODUCT INFORMATION, VISIT OUR WEBSITE AT http://www.fairchildsemi.com. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification		Definition		
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary First Production		Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

Rev. 174

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: FPF1048BUCX



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.