

PMEG4010ER 1 A low VF MEGA Schottky barrier rectifier 13 October 2017

Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 1 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage •
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package
- Capable for reflow and wave soldering

### 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

### 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; T <sub>amb</sub> ≤ 115 °C; square wave	[1]	-	-	1
		$\delta$ = 0.5 ; f = 20 kHz; T <sub>sp</sub> ≤ 140 °C; square wave		-	-	1
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C		-	430	490
I <sub>R</sub>	reverse current	V <sub>R</sub> = 40 V; T <sub>i</sub> = 25 °C		-	10	50

[1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

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Unit А

А

V mV μA

### 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	К	cathode		1 🕂 2			
2	A	anode	CFP3 (SOD123W)	sym001			

## 6. Ordering information

#### Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG4010ER	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W		

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG4010ER	BD

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; T <sub>amb</sub> ≤ 115 °C; square wave	[1]	-	1	A
		$\delta$ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~$ 140 °C; square wave		-	1	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	50	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	0.57	W
			[3]	-	0.95	W
			[1]	-	1.8	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

Device mounted on a ceramic PCB,  $AI_2O_3$ , standard footprint. [1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. [2]

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1] [2]	-	-	220	K/W
			[3] [2]	-	-	130	K/W
			[4] [2]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	18	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

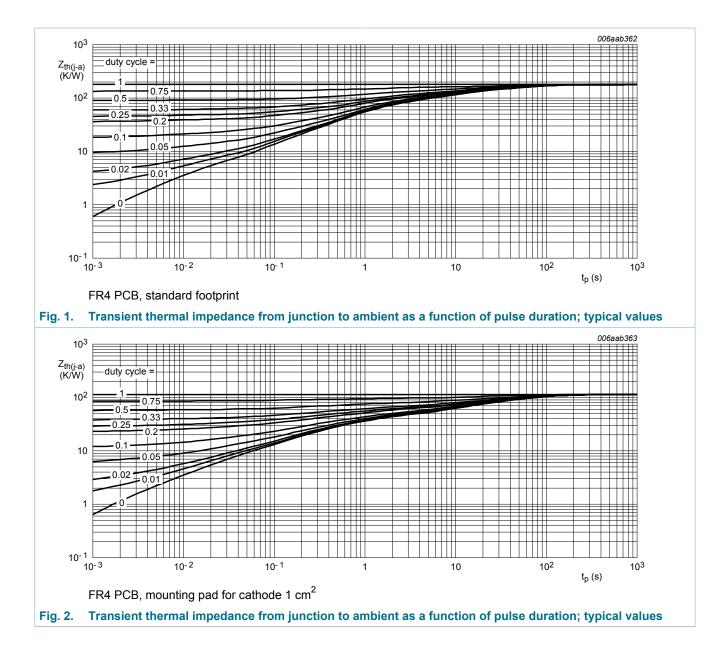
For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a [2] significant part of the total power losses.

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>. [3]

Device mounted on a ceramic PCB,  $AI_2O_3$ , standard footprint. [4]

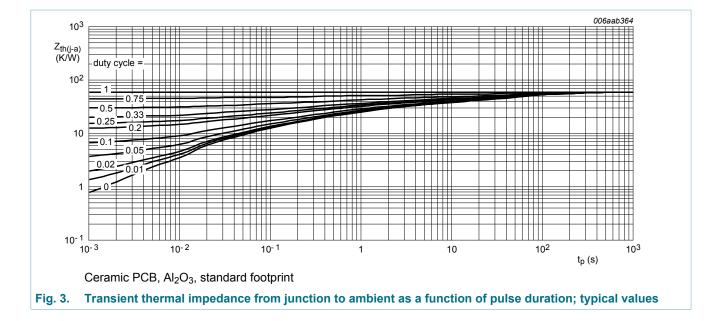
[5] Soldering point of cathode tab.

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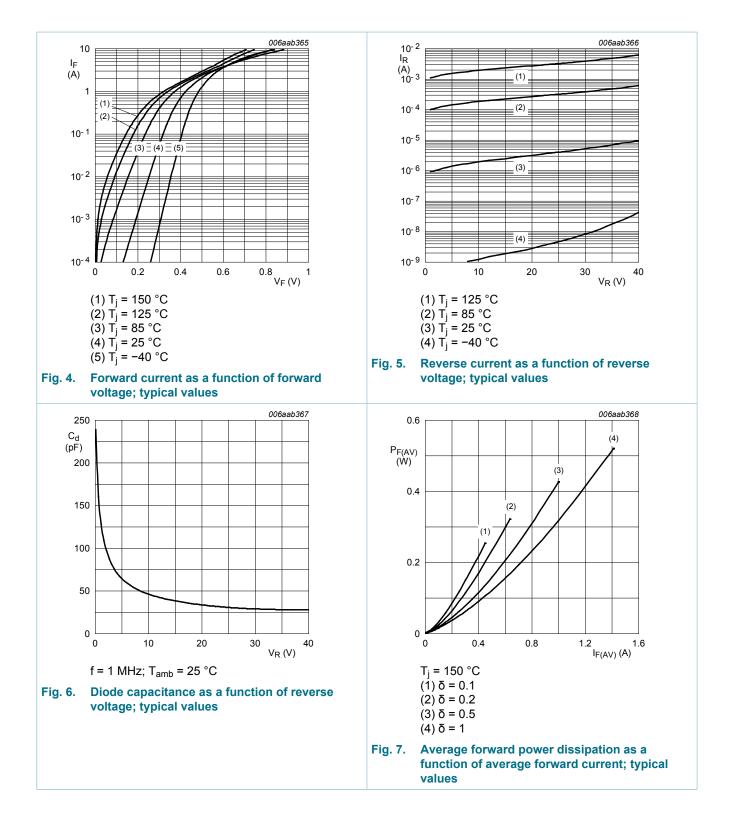
#### **1 A low VF MEGA Schottky barrier rectifier**



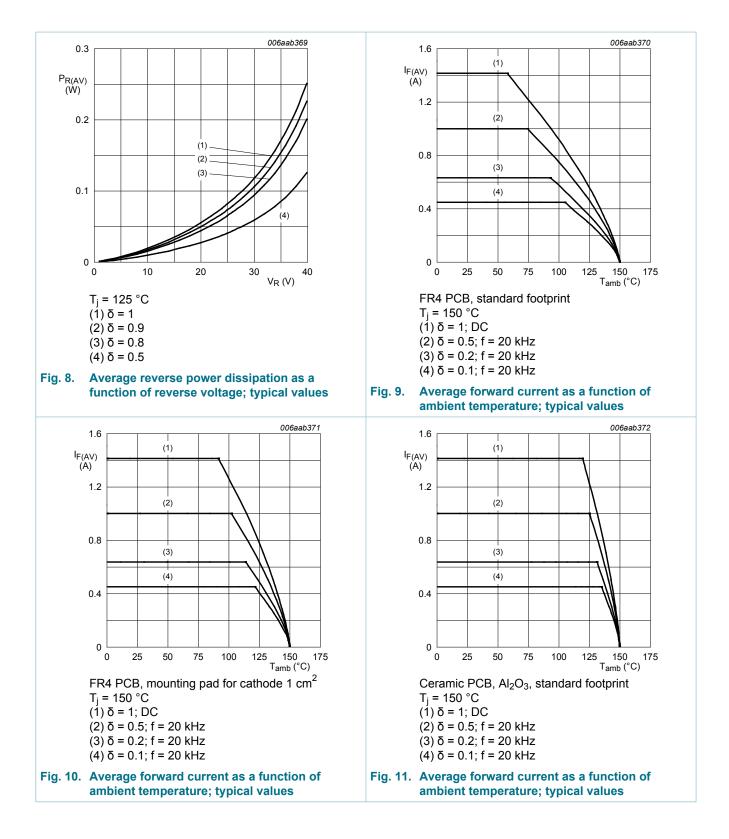
### **10. Characteristics**

Table 7. Cha	racteristics		 			
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	310	360	mV
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	430	490	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C	-	3	13	μA
		V <sub>R</sub> = 40 V; T <sub>j</sub> = 25 °C	-	10	50	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	130	-	pF
		$V_R$ = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	50	-	pF

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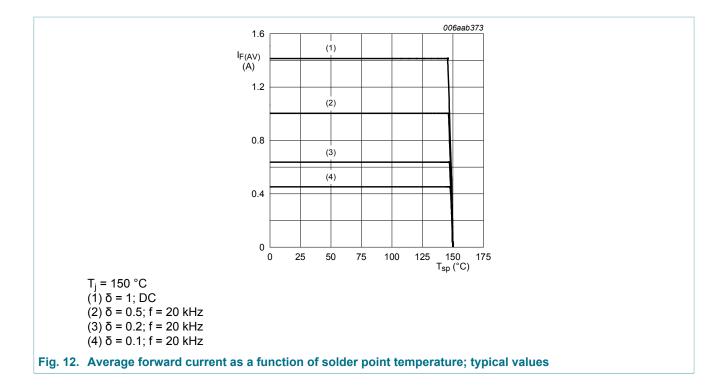


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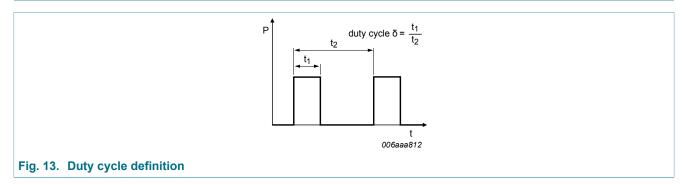
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### 11. Test information

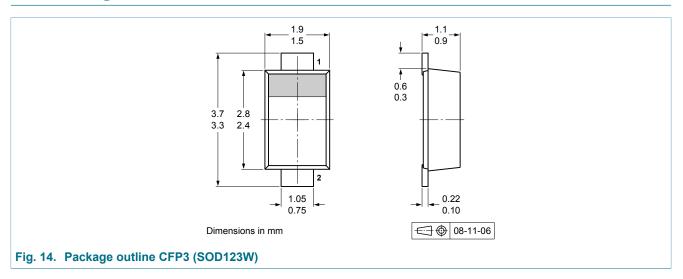


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

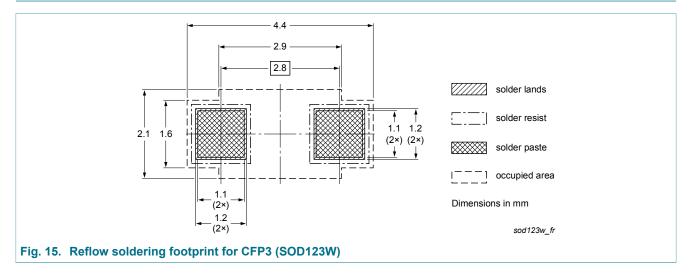
### 12. Package outline



### PMEG4010ER

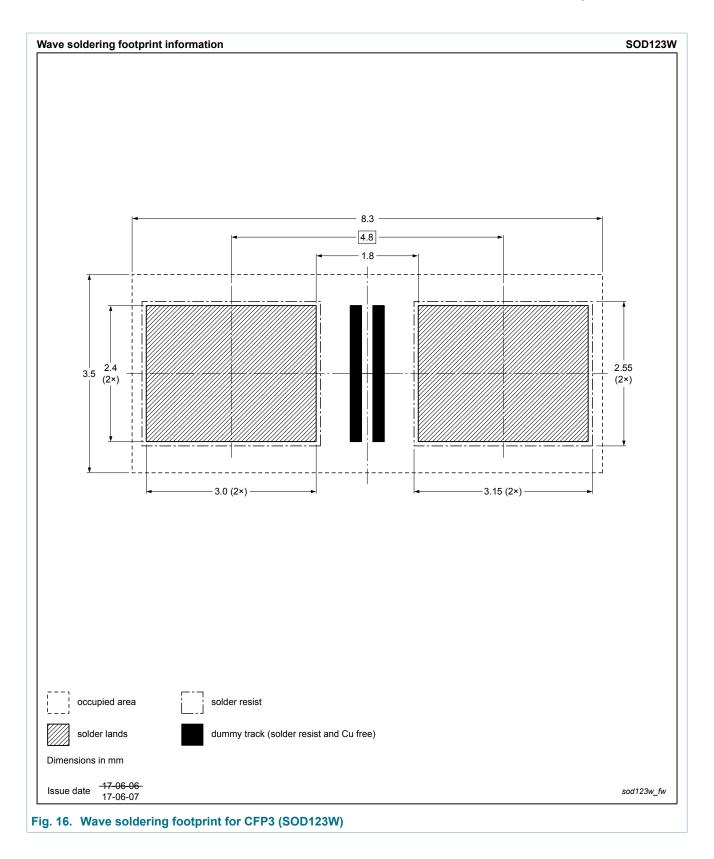
#### **1 A low VF MEGA Schottky barrier rectifier**

### 13. Soldering



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## 14. Revision history

Table 8. Revision his	tory						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG4010ER v.3	20171013	Product data sheet	-	PMEG4010ER_2			
Modifications:	<ul> <li>Features and benefits: Capable for reflow and wave soldering added</li> <li>Soldering: Wave soldering footprint added</li> </ul>						
PMEG4010ER_2	20100415	Product data sheet	-	PMEG4010ER_1			
PMEG4010ER_1	20081209	Product data sheet	-	-			

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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