

LM2673 SIMPLE SWITCHER® 3A Step-Down Voltage Regulator with Adjustable Current Limit

Check for Samples: [LM2673](http://www.ti.com/product/lm2673#samples)

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- **• 3.3V, 5V and 12V Fixed Output and Adjustable** adjustable output version. **(1.2V to 37V) Versions**
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-
-
-
- **• −40 to +125°C Operating Junction Temperature**

-
-
- **• Battery Chargers**

Typical Application

¹FEATURES DESCRIPTION

²³ The LM2673 series of regulators are monolithic **• Efficiency Up to 94%** integrated circuits which provide all of the active Simple and Easy to Design with (Using Off-

The-Shelf External Components)

Resistor Programmable Peak Current Limit and load regulation characteristics. High efficiency **• Resistor Programmable Peak Current Limit** and load regulation characteristics. High efficiency **Over a Range of 2A to 5A** (>90%) is obtained through the use of a low ON-
150 mO DMOS Qutput Switch example and resistance DMOS power switch. The series consists resistance DMOS power switch. The series consists **• ¹⁵⁰ ^m^Ω DMOS Output Switch** of fixed output voltages of 3.3V, 5V and 12V and an

The SIMPLE SWITCHER[®] concept provides for a • ±2% Maximum Output Tolerance Over Full

Line and Load Conditions

• Wide Input Voltage Range: 8V to 40V

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260KHz) allows the use of physically smaller sized **• Wide Input Voltage Range: 8V to 40V** (260KHz) allows the use of physically smaller sized **• 260 KHz Fixed Frequency Internal Oscillator** components. A family of standard inductors for use with the LM2673 are available from several **•• Softstart Capability ••• The Contemporal manufacturers** to greatly simplify the design process.

Other features include the ability to reduce the input **Range** surge current at power-ON by adding a softstart timing capacitor to gradually turn on the regulator. **APPLICATIONS** The LM2673 series also has built in thermal **• Simple to Design, High Efficiency (>90%) Step-** shutdown and resistor programmable current limit of **Down Switching Regulators** the power MOSFET switch to protect the device and
Efficient System Bre Bequisier for Linear load circuitry under fault conditions. The output • Efficient System Pre-Regulator for Linear
voltage is ensured to a $\pm 2\%$ tolerance. The clock
frequency is controlled to within a $\pm 11\%$ tolerance.

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Connection Diagrams

Figure 3. VSON-14 (Top View) See Package Number NHM0014A

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings(1)(2)

(1) Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Ratings indicate conditions under which of the device is ensured. Operating Ratings do not imply ensured performance limits. For ensured performance limits and associated test condition, see the Electrical [Characteristics](#page-2-0) tables.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(3) The absolute maximum specification of the 'Switch Voltage to Ground' applies to DC voltage. An extended negative voltage limit of -8V applies to a pulse of up to 20 ns, -6V of 60 ns and -3V of up to 100 ns.

(4) ESD was applied using the human-body model, a 100pF capacitor discharged through a 1.5 kΩ resistor into each pin.

RUMENTS

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Operating Ratings

Electrical Characteristics LM2673-3.3

Limits appearing in **bold type face** apply over the entire junction temperature range of operation, −40°C to 125°C. Specifications appearing in normal type apply for $T_A = T_J = 25^{\circ}C$. $R_{ADJ} = 8.2K\Omega$

(1) Typical values are determined with $T_A = T_J = 25^{\circ}C$ and represent the most likely norm.
(2) All limits are ensured at room temperature (standard type face) and at **temperature ex**

(2) All limits are ensured at room temperature (standard type face) and at **temperature extremes (bold type face)**. All room temperature limits are 100% tested during production with $T_A = T_J = 25^{\circ}\text{C}$. All limits at temperature extremes are ensured via correlation using standard Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).

LM2673-5.0

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LM2673-12

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LM2673-ADJ

(1) Typical values are determined with $T_A = T_J = 25^{\circ}C$ and represent the most likely norm.
(2) All limits are ensured at room temperature (standard type face) and at **temperature ex**

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All Output Voltage Versions Electrical Characteristics

Limits appearing in **bold type face** apply over the entire junction temperature range of operation, −40°C to 125°C. Specifications appearing in normal type apply for $T_A = T_J = 25^{\circ}$ C. Unless otherwise specified, $R_{ADJ} = 8.2K\Omega$, $V_{IN} = 12V$ for the 3.3V, 5V and Adjustable versions and V_{IN} =24V for the 12V version.

(1) The peak switch current limit is determined by the following relationship: $I_{CL}=37,125/R_{ADJ}$.

(2) Junction to ambient thermal resistance (no external heat sink) for the 7 lead TO-220 package mounted vertically, with ½ inch leads in a socket, or on a PC board with minimum copper area.

(3) Junction to ambient thermal resistance (no external heat sink) for the 7 lead TO-220 package mounted vertically, with ½ inch leads soldered to a PC board containing approximately 4 square inches of (1 oz.) copper area surrounding the leads.

(4) Junction to ambient thermal resistance for the 7 lead DDPAK mounted horizontally against a PC board area of 0.136 square inches (the same size as the DDPAK package) of 1 oz. (0.0014 in. thick) copper.

(5) Junction to ambient thermal resistance for the 7 lead DDPAK mounted horizontally against a PC board area of 0.4896 square inches (3.6 times the area of the DDPAK package) of 1 oz. (0.0014 in. thick) copper.

(6) Junction to ambient thermal resistance for the 7 lead DDPAK mounted horizontally against a PC board copper area of 1.0064 square inches (7.4 times the area of the DDPAK package) of 1 oz. (0.0014 in. thick) copper. Additional copper area will reduce thermal resistance further. See the thermal model in Switchers Made Simple® software.

(7) Junction to ambient thermal resistance for the 14-lead VSON mounted on a PC board copper area equal to the die attach paddle.

(8) Junction to ambient thermal resistance for the 14-lead VSON mounted on a PC board copper area using 12 vias to a second layer of copper equal to die attach paddle. Additional copper area will reduce thermal resistance further. For layout recommendations, refer to [Application](http://www.ti.com/lit/pdf/SNOA401) Note AN-1187 at [www.ti.com/lsds/ti/analog/powermanagement/power_portal.page.](http://www.ti.com/lsds/ti/analog/powermanagement/power_portal.page)

EXAS STRUMENTS

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A: V_{SW} Pin Voltage, 10 V/div.

B: Inductor Current, 1 A/div **A: V_{SW} Pin Voltage, 10 V/div.** A: V_{SW} Pin Voltage, 10 V/div.

C: Output Ripple Voltage, 20 mV/div AC-Coupled B: Inductor Current, 1 A/div

Figure 14. Horizontal Time Base: 1 μs/div Figure 15. Horizontal Time Base: 1 μs//iv

200 μ sec/Div

-
- B: Load Current: 200 mA to 3A Load Pulse **Figure 12. Horizontal Time Base: 100 μs/div Figure 13. Horizontal Time Base: 200 μs/div**

1 μ sec/Div

C: Output Ripple Voltage, 20 mV/div AC-Coupled
Figure 15. Horizontal Time Base: 1 us/liv

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^{*} Active Inductor Patent Number 5,514,947

† Active Capacitor Patent Number 5,382,918

[LM2673](http://www.ti.com/product/lm2673?qgpn=lm2673)

APPLICATION HINTS

The LM2673 provides all of the active functions required for a step-down (buck) switching regulator. The internal power switch is a DMOS power MOSFET to provide power supply designs with high current capability, up to 3A, and highly efficient operation.

The LM2673 is part of the SIMPLE SWITCHER family of power converters. A complete design uses a minimum number of external components, which have been pre-determined from a variety of manufacturers. Using either this data sheet or a design software program called **LM267X Made Simple** (version 2.0) a complete switching power supply can be designed quickly. The software is provided free of charge and can be downloaded from Texas Instrument's Internet site located at [www.ti.com.](http://www.ti.com)

SWITCH OUTPUT

This is the output of a power MOSFET switch connected directly to the input voltage. The switch provides energy to an inductor, an output capacitor and the load circuitry under control of an internal pulse-width-modulator (PWM). The PWM controller is internally clocked by a fixed 260KHz oscillator. In a standard step-down application the duty cycle (Time ON/Time OFF) of the power switch is proportional to the ratio of the power supply output voltage to the input voltage. The voltage on pin 1 switches between Vin (switch ON) and below ground by the voltage drop of the external Schottky diode (switch OFF).

INPUT

The input voltage for the power supply is connected to pin 2. In addition to providing energy to the load the input voltage also provides bias for the internal circuitry of the LM2673. For ensured performance the input voltage must be in the range of 8V to 40V. For best performance of the power supply the input pin should always be bypassed with an input capacitor located close to pin 2.

C BOOST

A capacitor must be connected from pin 3 to the switch output, pin 1. This capacitor boosts the gate drive to the internal MOSFET above Vin to fully turn it ON. This minimizes conduction losses in the power switch to maintain high efficiency. The recommended value for C Boost is 0.01μF.

GROUND

This is the ground reference connection for all components in the power supply. In fast-switching, high-current applications such as those implemented with the LM2673, it is recommended that a broad ground plane be used to minimize signal coupling throughout the circuit

CURRENT ADJUST

A key feature of the LM2673 is the ability to tailor the peak switch current limit to a level required by a particular application. This alleviates the need to use external components that must be physically sized to accommodate current levels (under shorted output conditions for example) that may be much higher than the normal circuit operating current requirements.

A resistor connected from pin 5 to ground establishes a current ($I_{(pin 5)} = 1.2V / R_{ADJ}$) that sets the peak current through the power switch. The maximum switch current is fixed at \ddot{a} level of 37,125 / R_{AD} .

FEEDBACK

This is the input to a two-stage high gain amplifier, which drives the PWM controller. It is necessary to connect pin 6 to the actual output of the power supply to set the dc output voltage. For the fixed output devices (3.3V, 5V and 12V outputs), a direct wire connection to the output is all that is required as internal gain setting resistors are provided inside the LM2673. For the adjustable output version two external resistors are required to set the dc output voltage. For stable operation of the power supply it is important to prevent coupling of any inductor flux to the feedback input.

SOFTSTART

A capacitor connected from pin 7 to ground allows for a slow turn-on of the switching regulator. The capacitor sets a time delay to gradually increase the duty cycle of the internal power switch. This can significantly reduce the amount of surge current required from the input supply during an abrupt application of the input voltage. If softstart is not required this pin should be left open circuited. Please see the C_{ss} [SOFTSTART](#page-11-0) CAPACITOR section for further information regarding softstart capacitor values.

DAP (VSON PACKAGE)

The Die Attach Pad (DAP) can and should be connected to PCB Ground plane/island. For CAD and assembly guidelines refer to [Application](http://www.ti.com/lit/pdf/SNOA401) Note AN-1187 at [www.ti.com/lsds/ti/analog/powermanagement/power_portal.page.](http://www.ti.com/lsds/ti/analog/powermanagement/power_portal.page)

DESIGN CONSIDERATIONS

Power supply design using the LM2673 is greatly simplified by using recommended external components. A wide range of inductors, capacitors and Schottky diodes from several manufacturers have been evaluated for use in designs that cover the full range of capabilities (input voltage, output voltage and load current) of the LM2673. A simple design procedure using nomographs and component tables provided in this data sheet leads to a working design with very little effort. Alternatively, the design software, **LM267X Made Simple** (version 6.0), can also be used to provide instant component selection, circuit performance calculations for evaluation, a bill of materials component list and a circuit schematic.

The individual components from the various manufacturers called out for use are still just a small sample of the vast array of components available in the industry. While these components are recommended, they are not exclusively the only components for use in a design. After a close comparison of component specifications, equivalent devices from other manufacturers could be substituted for use in an application.

Important considerations for each external component and an explanation of how the nomographs and selection tables were developed follows.

INDUCTOR

The inductor is the key component in a switching regulator. For efficiency the inductor stores energy during the switch ON time and then transfers energy to the load while the switch is OFF.

Nomographs are used to select the inductance value required for a given set of operating conditions. The nomographs assume that the circuit is operating in continuous mode (the current flowing through the inductor never falls to zero). The magnitude of inductance is selected to maintain a maximum ripple current of 30% of the maximum load current. If the ripple current exceeds this 30% limit the next larger value is selected.

The inductors offered have been specifically manufactured to provide proper operation under all operating conditions of input and output voltage and load current. Several part types are offered for a given amount of inductance. Both surface mount and through-hole devices are available. The inductors from each of the three manufacturers have unique characteristics.

Renco: ferrite stick core inductors; benefits are typically lowest cost and can withstand ripple and transient peak currents above the rated value. These inductors have an external magnetic field, which may generate EMI.

Pulse Engineering: powdered iron toroid core inductors; these also can withstand higher than rated currents and, being toroid inductors, will have low EMI.

Coilcraft: ferrite drum core inductors; these are the smallest physical size inductors and are available only as surface mount components. These inductors also generate EMI but less than stick inductors.

OUTPUT CAPACITOR

The output capacitor acts to smooth the dc output voltage and also provides energy storage. Selection of an output capacitor, with an associated equivalent series resistance (ESR), impacts both the amount of output ripple voltage and stability of the control loop.

The output ripple voltage of the power supply is the product of the capacitor ESR and the inductor ripple current. The capacitor types recommended in the tables were selected for having low ESR ratings.

In addition, both surface mount tantalum capacitors and through-hole aluminum electrolytic capacitors are offered as solutions.

Impacting frequency stability of the overall control loop, the output capacitance, in conjunction with the inductor, creates a double pole inside the feedback loop. In addition the capacitance and the ESR value create a zero. These frequency response effects together with the internal frequency compensation circuitry of the LM2673 modify the gain and phase shift of the closed loop system.

As a general rule for stable switching regulator circuits it is desired to have the unity gain bandwidth of the circuit to be limited to no more than one-sixth of the controller switching frequency. With the fixed 260KHz switching frequency of the LM2673, the output capacitor is selected to provide a unity gain bandwidth of 40KHz maximum. Each recommended capacitor value has been chosen to achieve this result.

In some cases multiple capacitors are required either to reduce the ESR of the output capacitor, to minimize output ripple (a ripple voltage of 1% of Vout or less is the assumed performance condition), or to increase the output capacitance to reduce the closed loop unity gain bandwidth (to less than 40KHz). When parallel combinations of capacitors are required it has been assumed that each capacitor is the exact same part type.

The RMS current and working voltage (WV) ratings of the output capacitor are also important considerations. In a typical step-down switching regulator, the inductor ripple current (set to be no more than 30% of the maximum load current by the inductor selection) is the current that flows through the output capacitor. The capacitor RMS current rating must be greater than this ripple current. The voltage rating of the output capacitor should be greater than 1.3 times the maximum output voltage of the power supply. If operation of the system at elevated temperatures is required, the capacitor voltage rating may be de-rated to less than the nominal room temperature rating. Careful inspection of the manufacturer's specification for de-rating of working voltage with temperature is important.

INPUT CAPACITOR

Fast changing currents in high current switching regulators place a significant dynamic load on the unregulated power source. An input capacitor helps to provide additional current to the power supply as well as smooth out input voltage variations.

Like the output capacitor, the key specifications for the input capacitor are RMS current rating and working voltage. The RMS current flowing through the input capacitor is equal to one-half of the maximum dc load current so the capacitor should be rated to handle this. Paralleling multiple capacitors proportionally increases the current rating of the total capacitance. The voltage rating should also be selected to be 1.3 times the maximum input voltage. Depending on the unregulated input power source, under light load conditions the maximum input voltage could be significantly higher than normal operation and should be considered when selecting an input capacitor.

The input capacitor should be placed very close to the input pin of the LM2673. Due to relative high current operation with fast transient changes, the series inductance of input connecting wires or PCB traces can create ringing signals at the input terminal which could possibly propagate to the output or other parts of the circuitry. It may be necessary in some designs to add a small valued (0.1μF to 0.47μF) ceramic type capacitor in parallel with the input capacitor to prevent or minimize any ringing.

CATCH DIODE

When the power switch in the LM2673 turns OFF, the current through the inductor continues to flow. The path for this current is through the diode connected between the switch output and ground. This forward biased diode clamps the switch output to a voltage less than ground. This negative voltage must be greater than −1V so a low voltage drop (particularly at high current levels) Schottky diode is recommended. Total efficiency of the entire power supply is significantly impacted by the power lost in the output catch diode. The average current through the catch diode is dependent on the switch duty cycle (D) and is equal to the load current times (1-D). Use of a diode rated for much higher current than is required by the actual application helps to minimize the voltage drop and power loss in the diode.

During the switch ON time the diode will be reversed biased by the input voltage. The reverse voltage rating of the diode should be at least 1.3 times greater than the maximum input voltage.

BOOST CAPACITOR

The boost capacitor creates a voltage used to overdrive the gate of the internal power MOSFET. This improves efficiency by minimizing the on resistance of the switch and associated power loss. For all applications it is recommended to use a 0.01μF/50V ceramic capacitor.

RADJ, ADJUSTABLE CURRENT LIMIT

A key feature of the LM2673 is the ability to control the peak switch current. Without this feature the peak switch current would be internally set to 5A or higher to accommodate 3A load current designs. This requires that both the inductor (which could saturate with excessively high currents) and the catch diode be able to safely handle up to 5A which would be conducted under load fault conditions.

If an application only requires a load current of 2A or so the peak switch current can be set to a limit just over the maximum load current with the addition of a single programming resistor. This allows the use of less powerful and more cost effective inductors and diodes.

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The peak switch current is equal to a factor of 37,125 divided by R_{ADJ} . A resistance of 8.2K Ω sets the current limit to typically 4.5A. For predictable control of the current limit it is recommended to keep the peak switch current greater than 1A. For lower current applications 500mA and 1A switching regulators, the LM2674 and LM2672, are available.

When the power switch reaches the current limit threshold it is immediately turned OFF and the internal switching frequency is reduced. This extends the OFF time of the switch to prevent a steady state high current condition. As the switch current falls below the current limit threshold, the switch will turn back ON. If a load fault continues, the switch will again exceed the threshold and switch back OFF. This will result in a low duty cycle pulsing of the power switch to minimize the overall fault condition power dissipation.

Css SOFTSTART CAPACITOR

This optional capacitor controls the rate at which the LM2673 starts up at power on. The capacitor is charged linearly by an internal current source. This voltage ramp gradually increases the duty cycle of the power switch until it reaches the normal operating duty cycle defined primarily by the ratio of the output voltage to the input voltage. The softstart turn-on time is programmable by the selection of Css.

The formula for selecting a softstart capacitor is:

$$
C_{SS} \cong (I_{SST} \cdot t_{SS}) / [V_{SST} + 2.6V \cdot (\frac{V_{OUT} + V_{SCHOTTKY}}{V_{IN}})]
$$

where

- I_{SST} = Softstart Current, 3.7µA typical
- \bullet t_{ss} = Softstart time, from design requirements
- V_{SST} = Softstart Threshold Voltage, 0.63V typical
- V_{OUT} = Output Voltage, from design requirements
- $V_{\text{SCHOTTKY}} =$ Schottky Diode Voltage Drop, typically 0.5V
- V_{IN} = Maximum Input Voltage, from design requirements (1)

If this feature is not desired, leave the Softstart pin (pin 7) open circuited

With certain softstart capacitor values and operating conditions, the LM2673 can exhibit an overshoot on the output voltage during turn on. Especially when starting up into no load or low load, the softstart function may not be effective in preventing a larger voltage overshoot on the output. With larger loads or lower input voltages during startup this effect is minimized. In particular, avoid using softstart capacitors between 0.033µF and 1µF.

ADDITIONAL APPLICATION INFORMATION

When the output voltage is greater than approximately 6V, and the duty cycle at minimum input voltage is greater than approximately 50%, the designer should exercise caution in selection of the output filter components. When an application designed to these specific operating conditions is subjected to a current limit fault condition, it may be possible to observe a large hysteresis in the current limit. This can affect the output voltage of the device until the load current is reduced sufficiently to allow the current limit protection circuit to reset itself.

Under current limiting conditions, the LM267x is designed to respond in the following manner:

- 1. At the moment when the inductor current reaches the current limit threshold, the ON-pulse is immediately terminated. This happens for any application condition.
- 2. However, the current limit block is also designed to momentarily reduce the duty cycle to below 50% to avoid subharmonic oscillations, which could cause the inductor to saturate.
- 3. Thereafter, once the inductor current falls below the current limit threshold, there is a small relaxation time during which the duty cycle progressively rises back above 50% to the value required to achieve regulation.

If the output capacitance is sufficiently 'large', it may be possible that as the output tries to recover, the output capacitor charging current is large enough to repeatedly re-trigger the current limit circuit before the output has fully settled. This condition is exacerbated with higher output voltage settings because the energy requirement of the output capacitor varies as the square of the output voltage (1⁄2CV²), thus requiring an increased charging current.

A simple test to determine if this condition might exist for a suspect application is to apply a short circuit across the output of the converter, and then remove the shorted output condition. In an application with properly selected external components, the output will recover smoothly.

Practical values of external components that have been experimentally found to work well under these specific operating conditions are C_{OUT} = 47µF, L = 22µH. It should be noted that even with these components, for a device's current limit of I_{CLIM}, the maximum load current under which the possibility of the large current limit hysteresis can be minimized is $I_{\text{CLIM}}/2$. For example, if the input is 24V and the set output voltage is 18V, then for a desired maximum current of 1.5A, the current limit of the chosen switcher must be confirmed to be at least 3A.

Under extreme over-current or short circuit conditions, the LM267X employs frequency foldback in addition to the current limit. If the cycle-by-cycle inductor current increases above the current limit threshold (due to short circuit or inductor saturation for example) the switching frequency will be automatically reduced to protect the IC. Frequency below 100 KHz is typical for an extreme short circuit condition.

SIMPLE DESIGN PROCEDURE

Using the nomographs and tables in this data sheet (or use the available design software at www.ti.com) a complete step-down regulator can be designed in a few simple steps.

Step 1: Define the power supply operating conditions:

Required output voltage

Maximum DC input voltage

Maximum output load current

Step 2: Set the output voltage by selecting a fixed output LM2673 (3.3V, 5V or 12V applications) or determine the required feedback resistors for use with the adjustable LM2673−ADJ

Step 3: Determine the inductor required by using one of the four nomographs, [Figure](#page-16-0) 18 through [Figure](#page-16-1) 21. [Table](#page-17-0) 1 provides a specific manufacturer and part number for the inductor.

Step 4: Using [Table](#page-20-0) 6 and [Table](#page-20-1) 7 (fixed output voltage) or [Table](#page-23-0) 12 and [Table](#page-24-0) 13 (adjustable output voltage), determine the output capacitance required for stable operation. [Table](#page-18-0) 3 and [Table](#page-24-0) 13 provide the specific capacitor type from the manufacturer of choice.

Step 5: Determine an input capacitor from [Table](#page-20-0) 6 and [Table](#page-21-0) 9 for fixed output voltage applications. Use [Table](#page-18-0) 3 or [Table](#page-19-0) 4 to find the specific capacitor type. For adjustable output circuits select a capacitor from [Table](#page-18-0) 3 or [Table](#page-19-0) 4 with a sufficient working voltage (WV) rating greater than Vin max, and an rms current rating greater than one-half the maximum load current (2 or more capacitors in parallel may be required).

Step 6: Select a diode from [Table](#page-22-0) 10. The current rating of the diode must be greater than I load max and the Reverse Voltage rating must be greater than Vin max.

Step 7: Include a 0.01μF/50V capacitor for Cboost in the design and then determine the value of a softstart capacitor if desired.

Step 8: Define a value for R_{ADJ} to set the peak switch current limit to be at least 20% greater than lout max to allow for at least 30% inductor ripple current (±15% of Iout). For designs that must operate over the full temperature range the switch current limit should be set to at least 50% greater than lout max $(1.5 \times I_{out}$ max).

FIXED OUTPUT VOLTAGE DESIGN EXAMPLE

A system logic power supply bus of 3.3V is to be generated from a wall adapter which provides an unregulated DC voltage of 13V to 16V. The maximum load current is 2.5A. A softstart delay time of 50mS is desired. Through-hole components are preferred.

Step 1: Operating conditions are:

Vout $= 3.3V$

Vin max $= 16V$

 $Iload$ max = 2.5A

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Step 2: Select an LM2673T-3.3. The output voltage will have a tolerance of

 $±2\%$ at room temperature and $±3\%$ over the full operating temperature range.

Step 3: Use the nomograph for the 3.3V device, [Figure](#page-16-0) 18. The intersection of the 16V horizontal line (V_{in} max) and the 2.5A vertical line (I_{load} max) indicates that L33, a 22 μ H inductor, is required.

From [Table](#page-17-0) 1, L33 in a through-hole component is available from Renco with part number RL-1283-22-43 or part number PE-53933 from Pulse Engineering.

Step 4: Use [Table](#page-20-0) 6 or [Table](#page-20-1) 7 to determine an output capacitor. With a 3.3V output and a 33μH inductor there are four through-hole output capacitor solutions with the number of same type capacitors to be paralleled and an identifying capacitor code given. [Table](#page-18-0) 3 or [Table](#page-19-0) 4 provide the actual capacitor characteristics. Any of the following choices will work in the circuit:

1 x 220μF/10V Sanyo OS-CON (code C5)

1 x 1000μF/35V Sanyo MV-GX (code C10)

1 x 2200μF/10V Nichicon PL (code C5)

1 x 1000μF/35V Panasonic HFQ (code C7)

Step 5: Use [Table](#page-20-0) 6 or [Table](#page-21-0) 9 to select an input capacitor. With 3.3V output and 22μH there are three throughhole solutions. These capacitors provide a sufficient voltage rating and an rms current rating greater than 1.25A (1/2 I_{load} max). Again using [Table](#page-19-0) 3 or Table 4 for specific component characteristics the following choices are suitable:

1 x 1000μF/63V Sanyo MV-GX (code C14)

1 x 820μF/63V Nichicon PL (code C24)

1 x 560μF/50V Panasonic HFQ (code C13)

Step 6: From [Table](#page-22-0) 10 a 3A or more Schottky diode must be selected. The 20V rated diodes are sufficient for the application and for through-hole components two part types are suitable:

1N5820

SR302

Step 7: A 0.01μF capacitor will be used for Cboost. For the 50mS softstart delay the following parameters are to be used:

 $I_{SST}: 3.7_μA$

 t_{SS} : 50mS

 $V_{SST}: 0.63V$

 V_{OUT} : 3.3V

V_{SCHOTTKY}: 0.5V

 $V_{IN}: 16V$

Using Vin max ensures that the softstart delay time will be at least the desired 50mS.

Using the formula for Css a value of 0.148μF is determined to be required. Use of a standard value 0.22μF capacitor will produce more than sufficient softstart delay.

Step 8: Determine a value for R_{ADJ} to provide a peak switch current limit of at least 2.5A plus 50% or 3.75A.

$$
R_{\text{ADJ}} = \frac{37,125}{3.75A} = 9.9 \text{ k}\Omega
$$

Use a value of 10KΩ.

(2)

ADJUSTABLE OUTPUT DESIGN EXAMPLE

In this example it is desired to convert the voltage from a two battery automotive power supply (voltage range of 20V to 28V, typical in large truck applications) to the 14.8VDC alternator supply typically used to power electronic equipment from single battery 12V vehicle systems. The load current required is 2A maximum. It is also desired to implement the power supply with all surface mount components. Softstart is not required.

Step 1: Operating conditions are:

Vout $= 14.8V$

Vin max $= 28V$

Iload max = 2A

Step 2: Select an LM2673S-ADJ. To set the output voltage to 14.9V two resistors need to be chosen (R1 and R2 in [Figure](#page-8-0) 17). For the adjustable device the output voltage is set by the following relationship:

$$
V_{\text{OUT}} = V_{\text{FB}} \left(1 + \frac{R_2}{R_1} \right)
$$

where

 V_{FR} is the feedback voltage of typically 1.21V (3)

A recommended value to use for R1 is 1K. In this example then R2 is determined to be:

$$
R_2 = R_1 \left(\frac{V_{\text{OUT}}}{V_{\text{FB}}} - 1 \right) = 1 k \Omega \left(\frac{14.8 V}{1.21 V} - 1 \right)
$$
(4)

 $R2 = 11.23KΩ$

The closest standard 1% tolerance value to use is 11.3KΩ

This will set the nominal output voltage to 14.88V which is within 0.5% of the target value.

Step 3: To use the nomograph for the adjustable device, [Figure](#page-16-1) 21, requires a calculation of the inductor Volt•microsecond constant (E•T expressed in V•μS) from the following formula:

$$
E \cdot T = (V_{IN(MAX)} - V_{OUT} - V_{SAT}) \cdot \frac{V_{OUT} + V_D}{V_{IN(MAX)} - V_{SAT} + V_D} \cdot \frac{1000}{260} (V \cdot \mu s)
$$

where

 V_{SAT} is the voltage drop across the internal power switch which is $R_{ds(ON)}$ times I_{load} (5)

In this example this would be typically 0.15 Ω x 2A or 0.3V and V_D is the voltage drop across the forward bisased Schottky diode, typically 0.5V. The switching frequency of 260KHz is the nominal value to use to estimate the ON time of the switch during which energy is stored in the inductor.

For this example E•T is found to be:

$$
E \cdot T = (28 - 14.8 - 0.3) \cdot \frac{14.8 + 0.5}{28 - 0.3 + 0.5} \cdot \frac{1000}{260} \text{ (V} \cdot \mu\text{s})
$$

\n
$$
E \cdot T = (12.9 \text{V}) \cdot \frac{15.3}{28.2} \cdot 3.85 \text{ (V} \cdot \mu\text{s}) = 26.9 \text{ (V} \cdot \mu\text{s})
$$
 (7)

Using [Figure](#page-16-1) 21, the intersection of 27V•µS horizontally and the 2A vertical line (I_{load} max) indicates that L38, a 68μH inductor, should be used.

From [Table](#page-17-0) 1, L38 in a surface mount component is available from Pulse Engineering with part number PE-54038S.

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Step 4: Use [Table](#page-23-0) 12 or [Table](#page-24-0) 13 to determine an output capacitor. With a 14.8V output the 12.5 to 15V row is used and with a 68µH inductor there are three surface mount output capacitor solutions. [Table](#page-19-0) 3 or Table 4 provide the actual capacitor characteristics based on the C Code number. Any of the following choices can be used:

1 x 33μF/20V AVX TPS (code C6)

1 x 47μF/20V Sprague 594 (code C8)

1 x 47μF/20V Kemet T495 (code C8)

NOTE

When using the adjustable device in low voltage applications (less than 3V output), if the nomograph, [Figure](#page-16-1) 21, selects an inductance of 22μH or less, [Table](#page-23-0) 12 and [Table](#page-24-0) 13 do not provide an output capacitor solution. With these conditions the number of output capacitors required for stable operation becomes impractical. It is recommended to use either a 33μH or 47μH inductor and the output capacitors from [Table](#page-23-0) 12 or [Table](#page-24-0) 13.

Step 5: An input capacitor for this example will require at least a 35V WV rating with an rms current rating of 1A (1/2 Iout max). From [Table](#page-18-0) 3 or [Table](#page-19-0) 4 it can be seen that C12, a 33μF/35V capacitor from Sprague, has the required voltage/current rating of the surface mount components.

Step 6: From [Table](#page-22-0) 10 a 3A Schottky diode must be selected. For surface mount diodes with a margin of safety on the voltage rating one of five diodes can be used:

SK34

30BQ040

30WQ04F

MBRS340

MBRD340

Step 7: A 0.01μF capacitor will be used for Cboost.

The softstart pin will be left open circuited.

Step 8: Determine a value for R_{ADJ} to provide a peak switch current limit of at least 2A plus 50% or 3A.

 $R_{ADJ} = \frac{37,125}{3\lambda} = 12.375 k\Omega$

(8)

Use a value of 12.4KΩ.

VSON PACKAGE DEVICES

The LM2673 is offered in the 14 lead VSON surface mount package to allow for a significantly decreased footprint with equivalent power dissipation compared to the DDPAK. For details on mounting and soldering specifications, \blacksquare refer to [Application](http://www.ti.com/lit/pdf/SNOA401) Note AN-1187 at www.ti.com/lsds/ti/analog/powermanagement/power_portal.page.

Inductor Selection Guides

For Continuous Mode Operation

Texas
Instruments

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Table 1. Inductor Manufacturer Part Numbers

Table 2. Inductor Manufacturer Contact Numbers

[LM2673](http://www.ti.com/product/lm2673?qgpn=lm2673)

Capacitor Selection Guides

Table 3. Input and Output Capacitor Codes—Surface Mount

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Table 4. Input and Output Capacitor Codes—Through Hole

Table 5. Capacitor Manufacturer Contact Numbers

RUMENTS

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Table 6. Output Capacitors for Fixed Output Voltage Application—Surface Mount(1)(2)

(1) No. represents the number of identical capacitor types to be connected in parallel

(2) C Code indicates the Capacitor Reference number in [Table](#page-18-0) 3 and [Table](#page-19-0) 4 for identifying the specific component from the manufacturer.

Table 7. Output Capacitors for Fixed Output Voltage Application—Through Hole(1)(2)

(1) No. represents the number of identical capacitor types to be connected in parallel

(2) C Code indicates the Capacitor Reference number in [Table](#page-18-0) 3 and [Table](#page-19-0) 4 for identifying the specific component from the manufacturer.

Table 8. Input Capacitors for Fixed Output Voltage Application—Surface Mount(1)(2)(3)

(1) Assumes worst case maximum input voltage and load current for a given inductance value

(2) No. represents the number of identical capacitor types to be connected in parallel

(3) C Code indicates the Capacitor Reference number in [Table](#page-18-0) 3 and [Table](#page-19-0) 4 for identifying the specific component from the manufacturer.

(4) Check voltage rating of capacitors to be greater than application input voltage.

Table 9. Input Capacitors for Fixed Output Voltage Application—Through Hole(1)(2)(3)

(1) Assumes worst case maximum input voltage and load current for a given inductance value

(2) No. represents the number of identical capacitor types to be connected in parallel

(3) C Code indicates the Capacitor Reference number in [Table](#page-19-0) 3 and Table 4 for identifying the specific component from the manufacturer.
(4) Check voltage rating of capacitors to be greater than application input voltage.

Check voltage rating of capacitors to be greater than application input voltage.

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Table 12. Output Capacitors for Adjustable Output Voltage Applications—Surface Mount(1)(2)

(1) No. represents the number of identical capacitor types to be connected in parallel

(2) C Code indicates the Capacitor Reference number in [Table](#page-18-0) 3 and [Table](#page-19-0) 4 for identifying the specific component from the manufacturer.

(3) Set to a higher value for a practical design solution. See [Application](#page-7-0) Hints section

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Table 13. Output Capacitors for Adjustable Output Voltage Applications—Through Hole(1)(2)

(1) No. represents the number of identical capacitor types to be connected in parallel (2) C Code indicates the Capacitor Reference number in Table 3 and Table 4 for ider

C Code indicates the Capacitor Reference number in [Table](#page-19-0) 3 and Table 4 for identifying the specific component from the manufacturer.

(3) Set to a higher value for a practical design solution. See [Application](#page-7-0) Hints section

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REVISION HISTORY

PACKAGING INFORMATION

PACKAGE OPTION ADDENDUM

(1) The marketing status values are defined as follows:

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LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check<http://www.ti.com/productcontent>for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

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the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

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