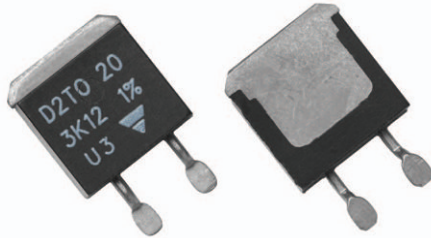


## Surface Mount Power Resistor Thick Film Technology



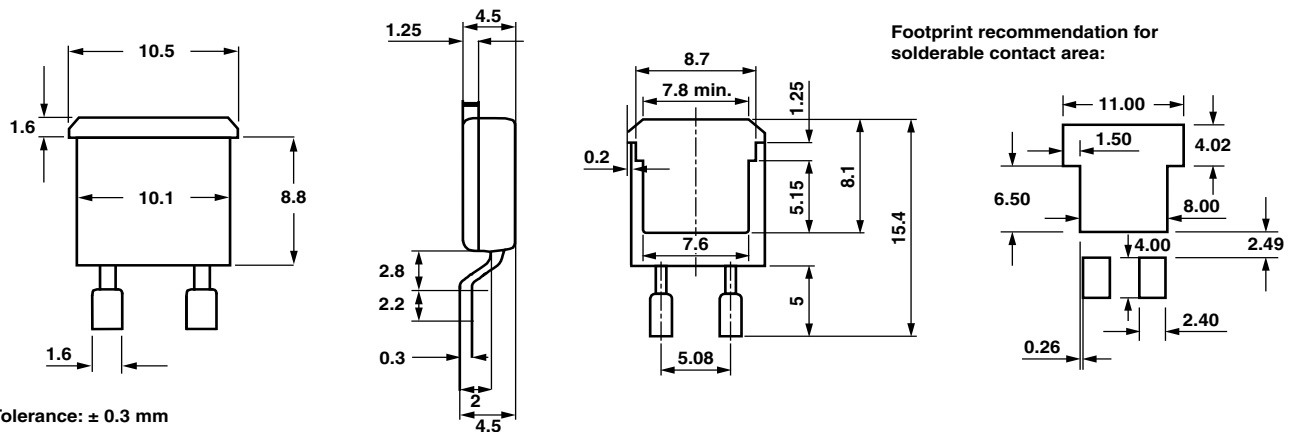
### FEATURES

- AEC-Q200 qualified
- 20 W at 25 °C case temperature
- Surface mounted resistor - TO-263 (D<sup>2</sup>PAK) style package
- Wide resistance range from 0.01 Ω to 550 kΩ
- Non inductive
- Resistor isolated from metal tab
- Solder reflow secure at 270 °C/10 s
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

 AUTOMOTIVE  
GRADE

**RoHS**  
COMPLIANT

### DIMENSIONS in millimeters



### Notes

- For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C
- Power dissipation is 3.1 W at an ambient temperature of 25 °C when mounted on a double sided copper board using FR4 HTG, 70 μm of copper, 39 mm x 30 mm x 1.6 mm, with thermal vias

### STANDARD ELECTRICAL SPECIFICATIONS

| MODEL  | SIZE   | RESISTANCE RANGE<br>Ω | RATED POWER<br>$P_{25\text{ °C}}$<br>W | LIMITING ELEMENT VOLTAGE $U_L$<br>V | TOLERANCE<br>± % | TEMPERATURE COEFFICIENT<br>± ppm/°C | CRITICAL RESISTANCE<br>Ω |
|--------|--------|-----------------------|--|-------------------------------------|------------------|-------------------------------------|--------------------------|
| D2TO20 | TO-263 | 0.01 to 550K          | 20                                     | 500                                 | 1, 2, 5, 10      | 150, 250, 700, 1100                 | 12.5K                    |

### MECHANICAL SPECIFICATIONS

|                       |               |
|-----------------------|---------------|
| Mechanical Protection | Molded        |
| Resistive Element     | Thick film    |
| Substrate             | Alumina       |
| Connections           | Tinned copper |
| Weight                | 2.2 g max.    |

### ENVIRONMENTAL SPECIFICATIONS

|                   |   |
|-------------------|---|
| Temperature Range | -55 °C to 155 °C  |
| Flammability      | IEC 60695-11-5<br>2 applications 30 s separated by 60 s |

### TECHNICAL SPECIFICATIONS

|  |   |
|--|---|
| Power Rating and Thermal Resistance of the Component | 20 W at 25 °C (case temperature)<br>$R_{TH(j-c)}$ : 6.5 °C/W                |
| Temperature Coefficient<br>Standard                  | See Special Feature table<br>± 150 ppm/°C                                   |
| Dielectric Strength IEC 60115-1                      | 2000 V <sub>RMS</sub> - 1 min - 10 mA max.<br>(between terminals and board) |
| Insulation Resistance                                | ≥ 10 <sup>6</sup> MΩ  |
| Inductance   | ≤ 0.1 μH  |

### DIMENSIONS

|                  |                                      |
|------------------|--------------------------------------|
| Standard Package | TO-263 style<br>(D <sup>2</sup> PAK) |
|------------------|--------------------------------------|



| SPECIAL FEATURES   |                 |              |              |              |
|--|-----------------|--------------|--------------|--------------|
| Resistance Values  | ≥ 0.010         | ≥ 0.045      | ≥ 0.1        | ≥ 0.5        |
| Tolerances   | ± 1 % at ± 10 % |              |              |              |
| Requirement Temperature Coefficient (TCR)<br>(-55 °C +150 °C)<br>IEC 60115-1 | ± 1100 ppm/°C   | ± 700 ppm/°C | ± 250 ppm/°C | ± 150 ppm/°C |

| PERFORMANCE               |  |                              |
|---------------------------|--|------------------------------|
| TESTS                     | CONDITIONS   | REQUIREMENTS                 |
| Momentary Overload        | IEC 60115-1 §4.13<br>2 Pr 5 s for $R < 2 \Omega$<br>1.5 Pr 5 s for $R \geq 2 \Omega$<br>US < 1.5 UL  | ± (0.25 % + 0.005 $\Omega$ ) |
| Load Life                 | IEC 60115-1<br>1000 h, 90/30 Pr at +25 °C  | ± (1 % + 0.005 $\Omega$ )    |
| High Temperature Exposure | AEC-Q200 REV C conditions:<br>MIL-STD-202 method 108<br>1000 h, +175 °C, unpowered   | ± (0.25 % + 0.005 $\Omega$ ) |
| Temperature Cycling       | Pre-conditioning 3 reflows according<br>JESTD020D<br>IEC 60068-2-14 test Na<br>1000 cycles, -55 °C/+175 °C<br>Dwell time - 15 min            | ± (0.5 % + 0.005 $\Omega$ )  |
| Moisture Resistance       | AEC-Q200 REV C conditions:<br>MIL-STD-202 method 106<br>10 cycles, 24 h, unpowered   | ± (0.5 % + 0.005 $\Omega$ )  |
| Biased Humidity           | AEC-Q200 REV C conditions:<br>MIL-STD-202 method 103<br>1000 h, 85°C, 85 % RH  | ± (1 % + 0.005 $\Omega$ )    |
| Operational Life          | AEC-Q200 REV C conditions:<br>Pre-conditioning 3 reflows according<br>JESTD020D<br>MIL-STD-202 method 108<br>1000 h, 90/30, powered, +125 °C | ± (1 % + 0.005 $\Omega$ )    |
| ESD Human Body Model      | AEC-Q200 REV C conditions:<br>AEC-Q200-002<br>25 kV <sub>AD</sub>  | ± (0.5 % + 0.005 $\Omega$ )  |
| Vibration                 | AEC-Q200 REV C conditions:<br>MIL-STD-202 method 204<br>5 g's for 20 min, 12 cycles<br>test from 10 Hz to 2000 Hz                            | ± (0.5 % + 0.005 $\Omega$ )  |
| Mechanical Shock          | AEC-Q200 REV C conditions:<br>MIL-STD-202 method 213<br>100 g's, 6 ms, 3.75 m/s<br>3 shocks/direction  | ± (0.5 % + 0.005 $\Omega$ )  |
| Board Flex                | AEC-Q200 REV C conditions:<br>AEC-Q200-005<br>Bending 2 mm/60 s  | ± (0.25 % + 0.01 $\Omega$ )  |
| Terminal Strength         | AEC-Q200 REV C conditions:<br>AEC-Q200-006<br>1.8 kgf/60 s   | ± (0.25 % + 0.01 $\Omega$ )  |

| ASSEMBLY SPECIFICATIONS  |  |  |
|--|--|--|
| For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C |  |  |
| TESTS  | CONDITIONS   | REQUIREMENTS   |
| Resistance to Soldering Heat   | IEC 60115-1<br>IEC 60068-2-58<br>Solder bath method: 270 °C/10 s | ± (0.5 % + 0.005 $\Omega$ )  |
| Moisture Sensitivity Level (MSL)   | IPC/JEDEC J-STD-020C<br>85 °C/85 % RH/168 h                      | Level: 1<br>+ Pass requirements of TCR<br>Overload and Dielectric Strength after MSL |



**CHOICE OF THE BOARD**

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH(j-c)} + R_{TH(c-h)} + R_{TH(h-a)}} \quad (1)$$

P: Expressed in W

ΔT: Difference between maximum working temperature and room temperature or fluid cooling temperature

R<sub>TH(j-c)</sub>: Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component: 6.5 °C/W.

R<sub>TH(c-h)</sub>: Thermal resistance value measured between outer side of the resistor and upper side of the board. This is the thermal resistance of the solder layer.

R<sub>TH(h-a)</sub>: Thermal resistance of the board.

**Example:**

R<sub>TH(c-h)</sub> + R<sub>TH(h-a)</sub> for D2TO20 power rating 2.5 W at ambient temperature +25 °C.

Thermal resistance R<sub>TH(j-c)</sub>: 6.5 °C/W

Considering equation (1) we have:

$$\Delta T = 155 \text{ °C} - 25 \text{ °C} = 130 \text{ °C}$$

$$R_{TH(j-c)} + R_{TH(c-h)} + R_{TH(h-a)} = \Delta T/P = 130/2.5 = 52 \text{ °C/W}$$

$$R_{TH(c-h)} + R_{TH(h-a)} = 52 \text{ °C/W} - 6.5 \text{ °C/W} = 45.5 \text{ °C/W}$$

Single Pulse:

These informations are for a single pulse on a cold resistor at 25 °C (not already used for a dissipation) and for pulses of 100 ms maximum duration.

The formula used to calculate E is:

$$E = P \times t = \frac{U^2}{R} \times t$$

with:

E (J): Pulse energy

P (W): Pulse power

t (s): Pulse duration

U (V): Pulse voltage

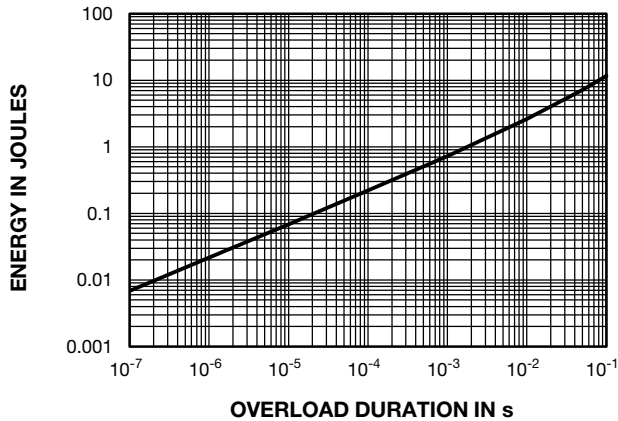
R (W): Resistor

The energy calculated must be less than that allowed by the graph.

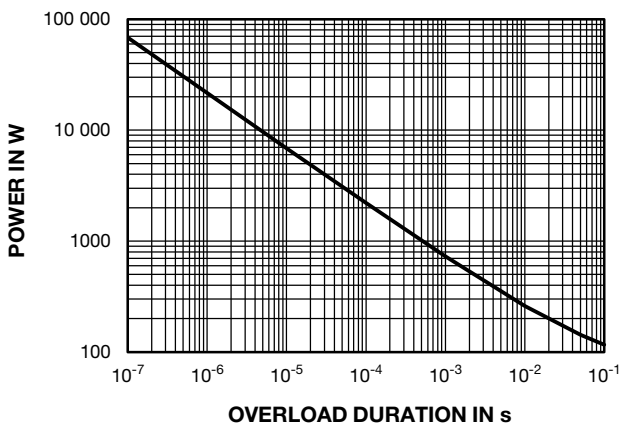
**OVERLOADS**

In any case the applied voltage must be lower than the maximum overload voltage of 750 V. The values indicated on the graph below are applicable to resistors in air or mounted onto a board.

**ENERGY CURVE**

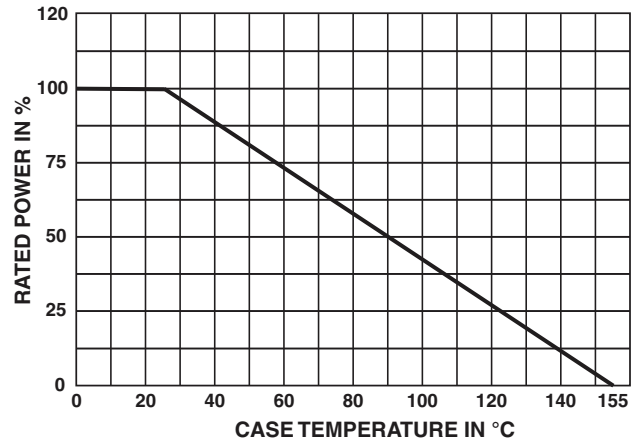


**POWER CURVE**



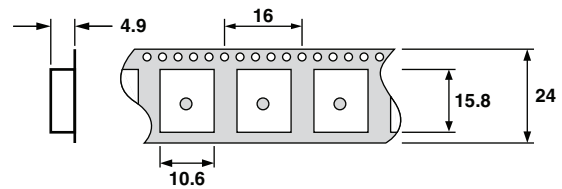
**POWER RATING**

The temperature of the case should be maintained within the limits specified.



**PACKAGING**

- Reel
- Tube
- Tape dimensions (mm) for reel:

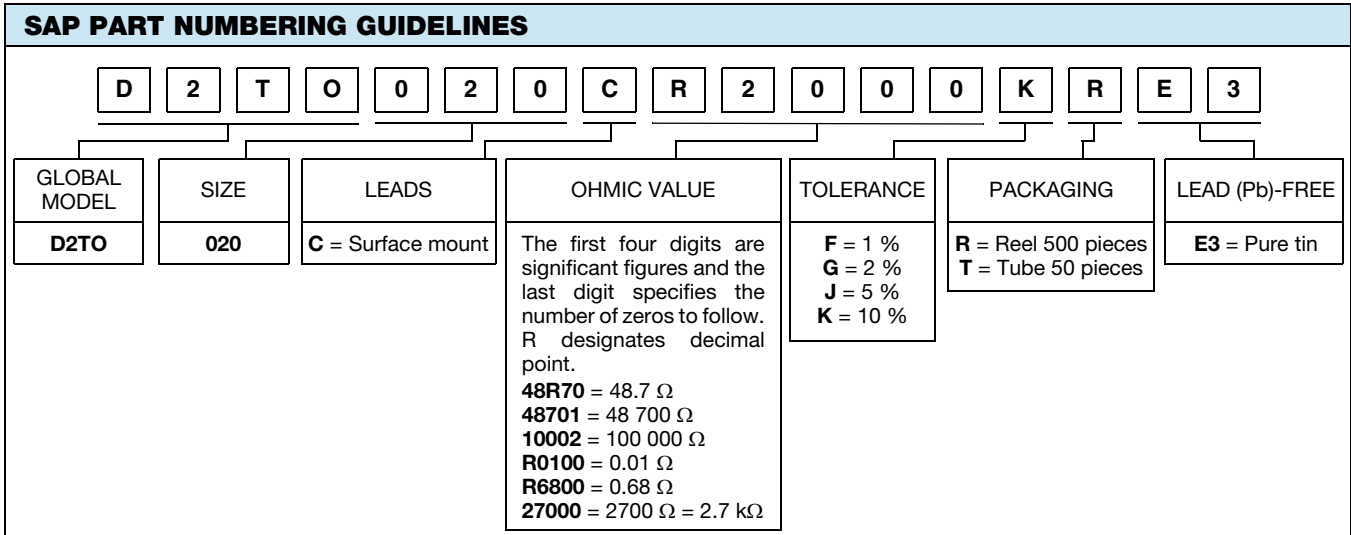


**MARKING**

Model, style, resistance value (in  $\Omega$ ), tolerance (in %), manufacturing date, Vishay Sfernice trademark



| ORDERING INFORMATION |            |             |                                 |   |                                     |                |
|----------------------|------------|-------------|---------------------------------|---|-------------------------------------|----------------|
| <b>D2TO</b>          | <b>020</b> | <b>C</b>    | <b>100 K<math>\Omega</math></b> | <b><math>\pm 1\%</math></b>                                       | <b>XXX</b>                          | <b>e3</b>      |
| MODEL                | STYLE      | CONNECTIONS | RESISTANCE VALUE                | TOLERANCE   | CUSTOM DESIGN                       | LEAD (Pb)-FREE |
|                      |            |             |                                 | F = $\pm 1\%$<br>G = $\pm 2\%$<br>J = $\pm 5\%$<br>K = $\pm 10\%$ | Optional on request:<br>Shape, etc. |                |





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