Unit: mm

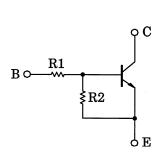
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

# RN1601, RN1602, RN1603 RN1604, RN1605, RN1606

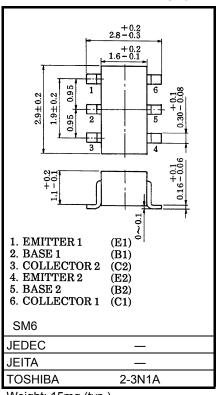
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Including two devices in SM6 (super-mini-type with six (6) leads)
- With built-in bias resistors
- Simplified circuit design
- Reduced number of parts and manufacturing process
- Complementary to RN2601 to RN2606

#### **Equivalent Circuit and Bias Resistor Values**



Type No.	R1 (kΩ)	R2 (kΩ)		
RN1601	4.7	4.7		
RN1602	10	10		
RN1603	22	22		
RN1604	47	47		
RN1605	2.2	47		
RN1606	4.7	47		

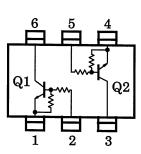


Weight: 15mg (typ.)

## Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristi	Symbol	Rating	Unit		
Collector-base voltage	RN1601 to 1606	$V_{CBO}$	50	V	
Collector-emitter voltage	10001101000	V <sub>CEO</sub>	50	٧	
Emitter-base voltage	RN1601 to 1604	V <sub>EBO</sub>	10	V	
	RN1605, 1606	vEBO	5		
Collector current		IC	100	mA	
Collector power dissipation	RN1601 to 1606	P <sub>C</sub> *	300	mW	
Junction temperature	T <sub>j</sub>		150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to150	°C	

# Equivalent Circuit (Top View)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 1988-11

<sup>\*</sup>Total rating

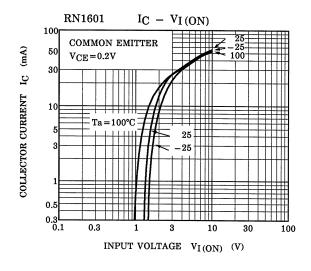


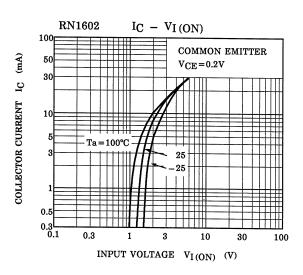
# Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

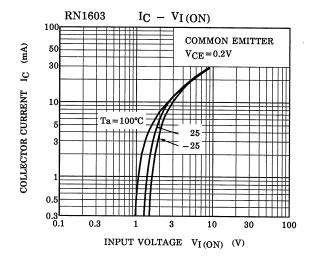
Character	istic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1601 to 1606	I <sub>CBO</sub>	_	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0	_	_	100	nA
	RIV 1601 10 1606		_	V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0	_	_	500	
Emitter cut-off current	RN1601	l <sub>EBO</sub>	_	V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	0.82	_	1.52	mA
	RN1602		_		0.38	_	0.71	
	RN1603		_		0.17	_	0.33	
	RN1604		_		0.082	_	0.15	
	RN1605		_	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	0.078	_	0.145	
	RN1606		_		0.074	_	0.138	
	RN1601		_		30	_	_	_
	RN1602		_		50	_	_	
DC surrent rein	RN1603	<b>L</b>	_	\\\ -5\\\\ -40 m^\	70	_	_	
DC current gain	RN1604	h <sub>FE</sub>	_	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	80	_	_	
	RN1605		_		80	_	_	
	RN1606		_	-	80	_	_	
Collector-emitter saturation voltage	RN1601 to 1606	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	V
	RN1601	V <sub>I (ON)</sub>	_	-V <sub>CE</sub> = 0.2 V, I <sub>C</sub> = 5 mA	1.1	_	2.0	V
Input voltage (ON)	RN1602		_		1.2	_	2.4	
	RN1603		_		1.3	_	3.0	
	RN1604		_		1.5	_	5.0	
	RN1605		_		0.6	_	1.1	
	RN1606		_		0.7	_	1.3	
Land with a control	RN1601 to 1604	V <sub>I</sub> (OFF)	_	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.1 mA	1.0	_	1.5	V
Input voltage (OFF)	RN1605 to 1606		_		0.5	_	0.8	
Transition frequency	RN1601 to 1606	f <sub>T</sub>	_	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	_	250	_	MHz
Collector output capacitance	RN1601 to 1606	C <sub>ob</sub>	_	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 f = 1 MHz	_	3	6	pF
Input resistor	RN1601	R1	_	7 15.4 32.9 1.54	3.29	4.7	6.11	
	RN1602		_		7	10	13	
	RN1603		_		15.4	22	28.6	
	RN1604		_		47	61.1	kΩ	
	RN1605		_		2.2	2.86		
	RN1606		_		3.29	4.7	6.11	
Resistor ratio	RN1601 to 1604	R1/R2	_		0.9	1.0	1.1	
	RN1605		_	_	0.0421	0.0468	0.0515	_
	RN1606		_		0.09	0.1	0.11	

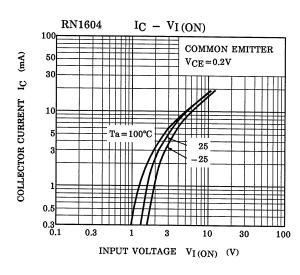
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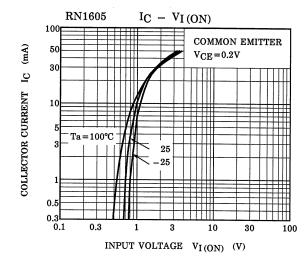
### (Q1, Q2 Common)

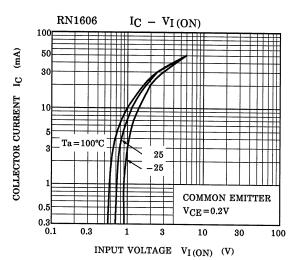






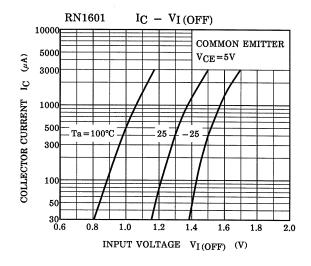


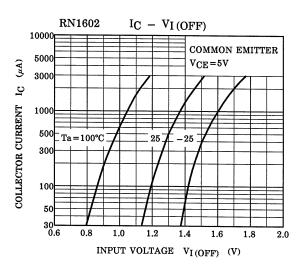


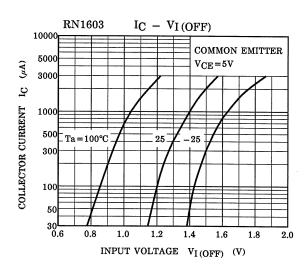


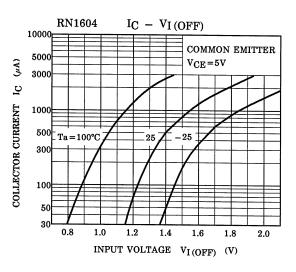
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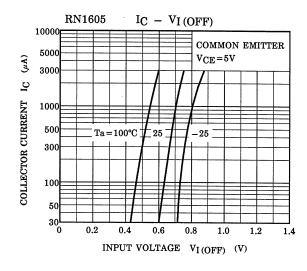
### (Q1, Q2 Common)

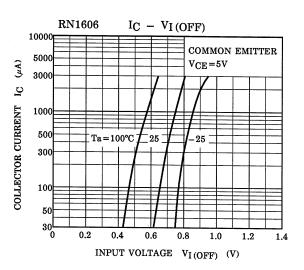




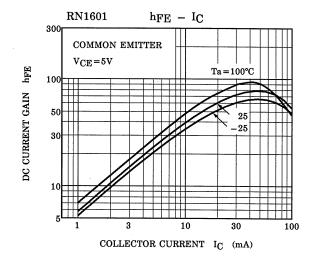


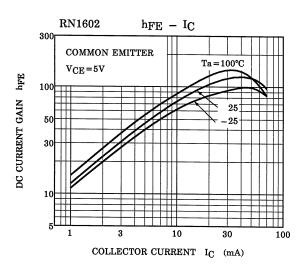


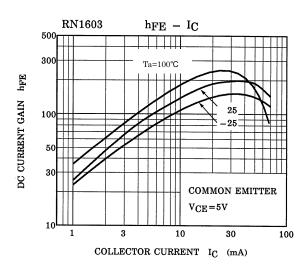


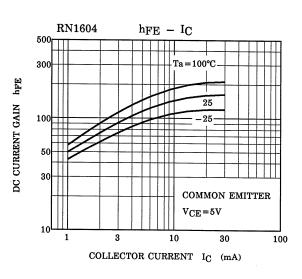


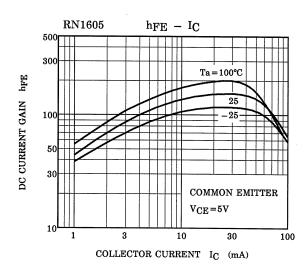
### (Q1, Q2 Common)

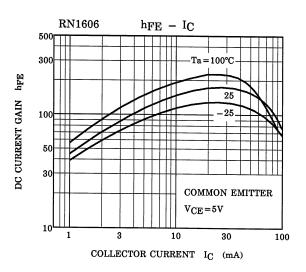












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# Marking

Type Name	Marking
RN1601	Type Name  XA
RN1602	Type Name  X B
RN1603	Type Name  X C
RN1604	Type Name  XD
RN1605	Type Name  X E
RN1606	Type Name  X F

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