Low-power 2-input NOR gate Rev. 7 — 21 January 2015

Product data sheet

General description 1.

The 74AUP1G02 provides the single 2-input NOR function.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. **Features and benefits**

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from –40 °C to +85 °C and –40 °C to +125 °C

nexperia

3. Ordering information

Table 1.	Ordering	information
----------	----------	-------------

Type number	Package						
	Temperature range Name		Description	Version			
74AUP1G02GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
74AUP1G02GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886			
74AUP1G02GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1 \times 0.5 mm	SOT891			
74AUP1G02GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115			
74AUP1G02GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202			
74AUP1G02GX	–40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226			

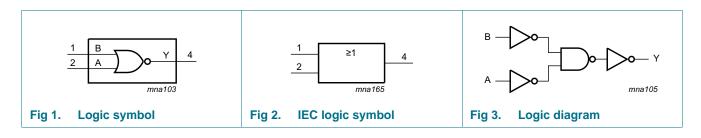
4. Marking

Table 2. Marking

Type number	Marking code ^[1]
74AUP1G02GW	рВ
74AUP1G02GM	рВ
74AUP1G02GF	рВ
74AUP1G02GN	рВ
74AUP1G02GS	рВ
74AUP1G02GX	рВ

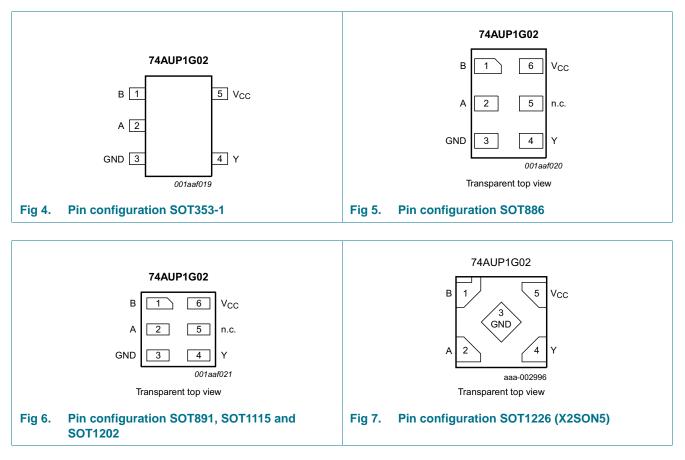
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1 Pinning



6.2 Pin description

Symbol	Pin		Description	
	TSSOP5 and X2SON5	XSON6		
В	1	1	data input	
A	2	2	data input	
GND	3	3	ground (0 V)	
Y	4	4	data output	
n.c.	-	5	not connected	
V _{CC}	5	6	supply voltage	

74AUP1G02 Product data sheet

7. Functional description

Table 4.Function table

Input		Output
A	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

[1] H = HIGH voltage level;

L = LOW voltage level.

8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V ₁ < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{ОК}	output clamping current	$V_{\rm O} > V_{\rm CC}$ or $V_{\rm O} < 0$ V	-	±50	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
I _O	output current	$V_{O} = 0 V \text{ to } V_{CC}$	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to +125 \ ^{\circ}C$ [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For TSSOP5 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.
 For XSON6 and X2SON5 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; $V_{CC} = 0 V$	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	-	-	V
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	$0.30\times V_{CC}$	V
		$V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$	-	-	$0.35 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_O = –20 $\mu\text{A};V_{CC}$ = 0.8 V to 3.6 V	$V_{CC}-0.1$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	2.05	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3\times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.31	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		I_{O} = 2.7 mA; V_{CC} = 3.0 V	-	-	0.31	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.44	V
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μA
ΔI_{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current		-	-	0.5	μA
Δl _{CC}	additional supply current		l -	-	40	μA
CI	input capacitance	$V_{CC} = 0 V$ to 3.6 V; $V_I = GND$ or V_{CC}	-	0.8	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = –	40 °C to +85 °C					
VIH	HIGH-level input voltage	V _{CC} = 0.8 V	$0.70\times V_{CC}$	-	-	V
		V _{CC} = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
VIL	LOW-level input voltage	$V_{CC} = 0.8 V$	-	-	$0.30 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{он}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.7 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O} = 20 \ \mu\text{A}; V_{CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I.	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA
OFF	power-off leakage current	$V_{1} \text{ or } V_{0} = 0 \text{ V to } 3.6 \text{ V; } V_{CC} = 0 \text{ V}$	-	-	±0.5	μA
∆I _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
СС	supply current	$V_1 = GND \text{ or } V_{CC}; I_0 = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
Δl _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ [1] $V_{CC} = 3.3 \text{ V}$	l -	-	50	μΑ

Table 7. Static characteristics ... continued

At recommended operating conditions: voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = –	40 °C to +125 °C					1
VIH	HIGH-level input voltage	V _{CC} = 0.8 V	$0.75 \times V_{CC}$	-	-	V
		V _{CC} = 0.9 V to 1.95 V	$0.70 \times V_{CC}$	-	-	V
		V_{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
VIL	LOW-level input voltage	$V_{CC} = 0.8 V$	-	-	$0.25 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.30 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{он}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O} = -20 \ \mu\text{A}; \ V_{CC} = 0.8 \ V \ \text{to} \ 3.6 \ V$	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	$0.6 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O} = 20 \ \mu\text{A}; V_{CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I	input leakage current	$V_1 = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.75	μA
OFF	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μA
∆I _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
сс	supply current	$\label{eq:VI} \begin{array}{l} V_I = GND \text{ or } V_{CC}; \ I_O = 0 \ A; \\ V_{CC} = 0.8 \ V \ \text{to} \ 3.6 \ V \end{array}$	-	-	1.4	μΑ
∆I _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$		-	75	μΑ

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

[1] One input at V_{CC} – 0.6 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	Min	Typ 🚹	Max	Unit
T _{amb} = 25	°C; C _L = 5 pF					
t _{pd}	propagation delay	A, B to Y; see Figure 8				
		V _{CC} = 0.8 V	-	17.0	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.5	5.1	10.8	ns
		V_{CC} = 1.4 V to 1.6 V	1.6	3.7	6.7	ns
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	1.3	3.0	5.3	ns
		V_{CC} = 2.3 V to 2.7 V	1.0	2.4	3.9	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	1.0	2.2	3.4	ns
T _{amb} = 25	°C; C _L = 10 pF					
t _{pd}	propagation delay	A, B to Y; see Figure 8				
		V _{CC} = 0.8 V	-	20.4	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.4	6.0	12.8	ns
		V _{CC} = 1.4 V to 1.6 V	1.9	4.3	7.9	ns
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	1.6	3.6	6.2	ns
		V_{CC} = 2.3 V to 2.7 V	1.4	3.0	4.7	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	1.3	2.7	4.2	ns
T _{amb} = 25	°C; C _L = 15 pF	I				
t _{pd}	propagation delay	A, B to Y; see Figure 8	l			
		V _{CC} = 0.8 V	-	23.9	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.4	6.8	14.6	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.8	8.9	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	4.0	7.0	ns
		V_{CC} = 2.3 V to 2.7 V	1.7	3.4	5.4	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	1.6	3.2	4.8	ns
T _{amb} = 25	°C; C _L = 30 pF		- 1			
t _{pd}	propagation delay	A, B to Y; see Figure 8	l			
		V _{CC} = 0.8 V	-	34.2	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.6	9.0	19.9	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	3.4	6.4	11.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	5.3	9.3	ns
		V_{CC} = 2.3 V to 2.7 V	2.4	4.5	7.1	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	2.3	4.2	6.4	ns

74AUP1G02

Low-power 2-input NOR gate

Symbol	Parameter	Conditions	Min	Typ 🛄	Max	Unit
T _{amb} = 25	°C	II			I	
C _{PD} power dissipation capacitance	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]				
	V _{CC} = 0.8 V	-	2.6	-	pF	
		V _{CC} = 1.1 V to 1.3 V	-	2.7	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.1	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.5	-	pF
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	4.1	-	pF

Table 8. Dynamic characteristics ... continued

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $\label{eq:PD} \mathsf{P}_\mathsf{D} = \mathsf{C}_\mathsf{PD} \times \mathsf{V}_\mathsf{CC}^2 \times \mathsf{f}_i \times \mathsf{N} + \Sigma(\mathsf{C}_\mathsf{L} \times \mathsf{V}_\mathsf{CC}^2 \times \mathsf{f}_o) \text{ where:}$

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

Table 9. **Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	–40 °C to +85 °C		–40 °C to +125 °C		Unit
		-	Min	Max	Min	Max	
C _L = 5 pF	1						
t _{pd}	propagation delay	A, B to Y; see Figure 8 [1]					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	2.1	12.1	2.1	13.4	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	1.4	7.8	1.4	8.6	ns
		V _{CC} = 1.65 V to 1.95 V	1.1	6.2	1.1	6.9	ns
		V_{CC} = 2.3 V to 2.7 V	0.9	4.6	0.9	5.1	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	0.8	4.0	0.8	4.4	ns
C _L = 10 pl	=						
t _{pd}	propagation delay	A, B to Y; see Figure 8 [1]					
		V _{CC} = 1.1 V to 1.3 V	2.2	14.3	2.2	15.8	ns
		V _{CC} = 1.4 V to 1.6 V	1.7	9.2	1.7	10.2	ns
		V _{CC} = 1.65 V to 1.95 V	1.5	7.3	1.5	8.1	ns
		V_{CC} = 2.3 V to 2.7 V	1.2	5.6	1.2	6.2	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	5.0	1.2	5.5	ns

Symbol	Parameter	Conditions	–40 °C to +85 °C		–40 °C to +125 °C		Unit
		-	Min	Max	Min	Max	
C _L = 15 pl	F					1	
t _{pd}	propagation delay	A, B to Y; see <u>Figure 8</u> [1]					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	3.1	16.4	3.1	18.1	ns
		$V_{CC} = 1.4 \text{ V} \text{ to } 1.6 \text{ V}$	2.0	10.4	2.0	11.5	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	8.3	1.7	9.2	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	6.3	1.5	7.0	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	1.4	5.7	1.4	6.3	ns
C _L = 30 pl	F						
t _{pd}	propagation delay	A, B to Y; see Figure 8 [1]					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	4.1	22.4	4.1	24.7	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	2.9	13.9	2.9	15.3	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	11.1	2.3	12.3	ns
		V_{CC} = 2.3 V to 2.7 V	2.1	8.5	2.1	9.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	7.7	2.1	8.5	ns

Table 9. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

12. Waveforms

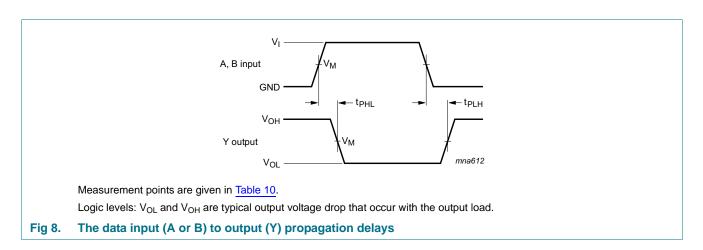


Table 10. Measurement points

Supply voltage	Output	Input		
V _{cc}	V _M	V _M	VI	$t_r = t_f$
0.8 V to 3.6 V	$0.5 imes V_{CC}$	$0.5 imes V_{CC}$	V _{CC}	≤ 3.0 ns

74AUP1G02

Low-power 2-input NOR gate

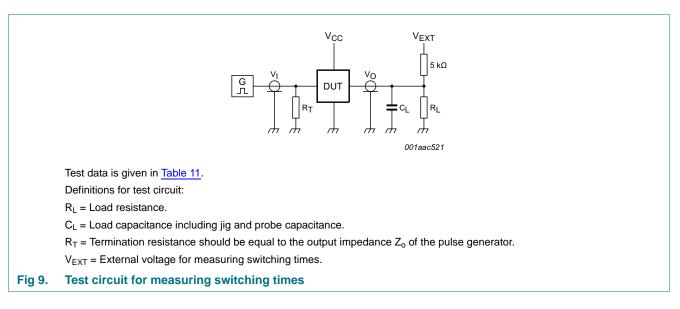


Table 11. Test data

Supply voltage Load		V _{EXT}			
V _{cc}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

13. Package outline

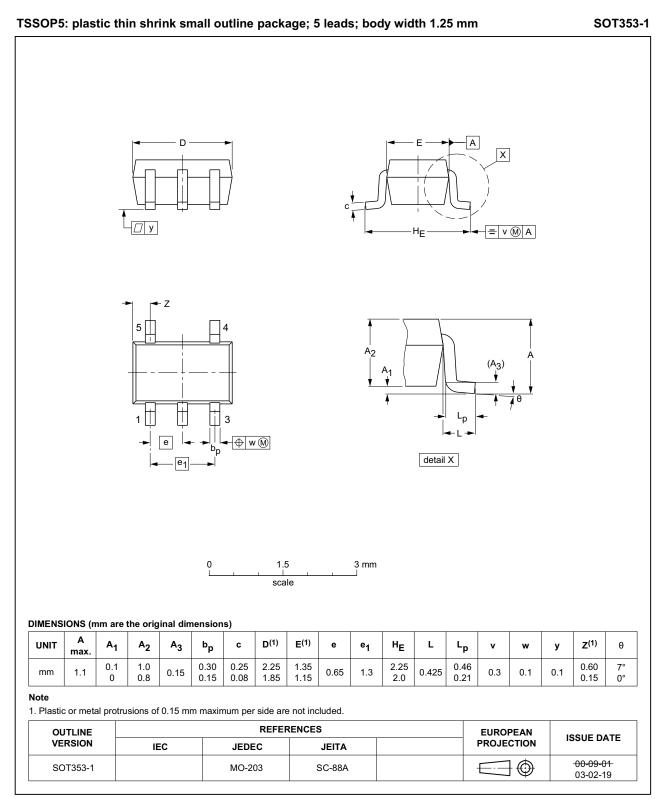
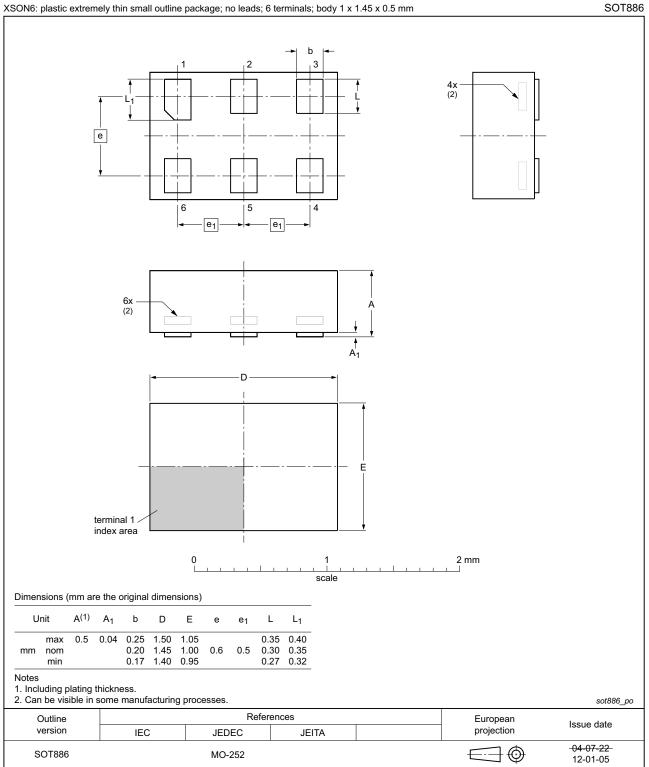


Fig 10. Package outline SOT353-1 (TSSOP5)

All information provided in this document is subject to legal disclaimers.



XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

Fig 11. Package outline SOT886 (XSON6)

All information provided in this document is subject to legal disclaimers.

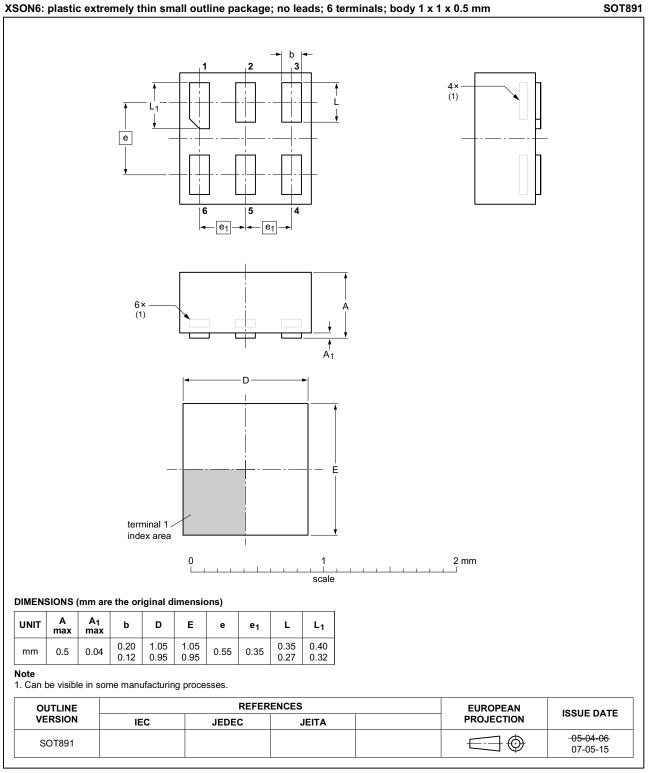
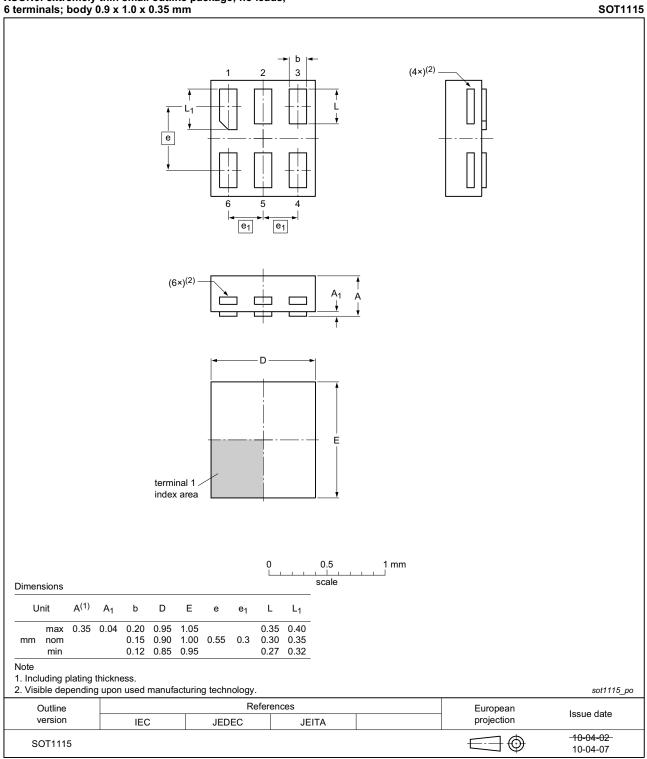


Fig 12. Package outline SOT891 (XSON6)

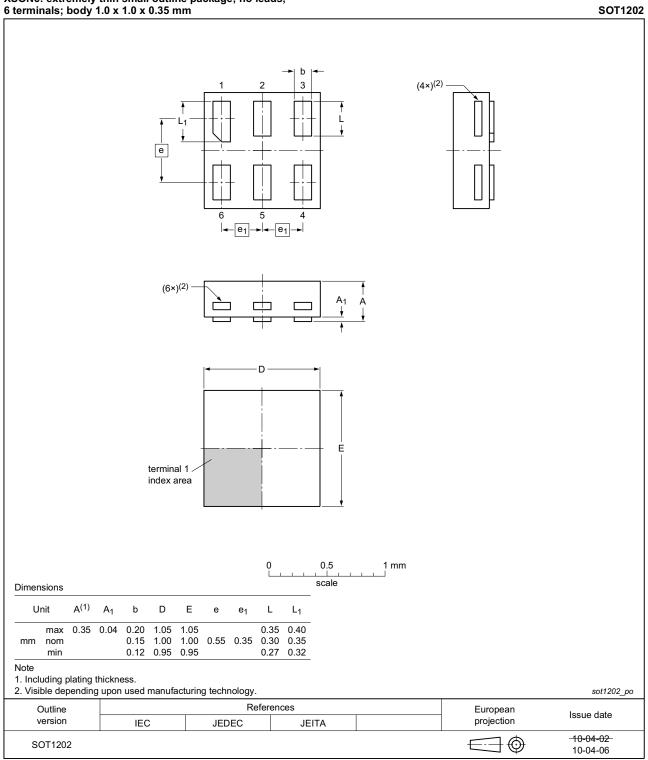
All information provided in this document is subject to legal disclaimers.



XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 13. Package outline SOT1115 (XSON6)

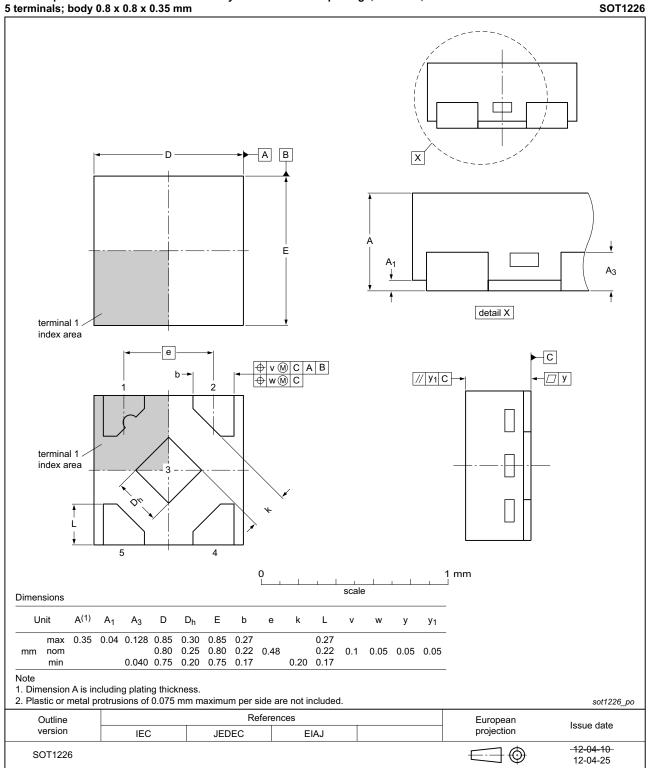
All information provided in this document is subject to legal disclaimers.



XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 14. Package outline SOT1202 (XSON6)

All information provided in this document is subject to legal disclaimers.



X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm

Fig 15. Package outline SOT1226 (X2SON5)

All information provided in this document is subject to legal disclaimers.

14. Abbreviations

Table 12. Abbreviations				
Acronym	Description			
CDM	Charged Device Model			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
НВМ	Human Body Model			
MM	Machine Model			

15. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74AUP1G02 v.7	20150121	Product data sheet	-	74AUP1G02 v.6		
Modifications:	 X2SON5 add 	led to pin description table (able 3).			
74AUP1G02 v.6	20120627	Product data sheet	-	74AUP1G02 v.5		
Modifications:	 Added type n 	umber 74AUP1G02GX (SO	Г1226).			
74AUP1G02 v.5	20120216	Product data sheet	-	74AUP1G02 v.4		
Modifications:	Logic diagram (Figure 3) modified.					
	Package outline drawing of SOT886 (Figure 11) modified.					
74AUP1G02 v.4	20111115	Product data sheet	-	74AUP1G02 v.3		
Modifications:	 Legal pages 	updated.		I		
74AUP1G02 v.3	20101012	Product data sheet	-	74AUP1G02 v.2		
74AUP1G02 v.2	20060628	Product data sheet	-	74AUP1G02 v.1		
74AUP1G02 v.1	20050718	Product data sheet	-	-		

16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

16.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any

representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and

customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

16.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Product data sheet

Nexperia

74AUP1G02

Low-power 2-input NOR gate

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of

non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

16.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

17. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

74AUP1G02

18. Contents

1	General description 1
2	Features and benefits 1
3	Ordering information 2
4	Marking 2
5	Functional diagram 2
6	Pinning information 3
6.1	Pinning 3
6.2	Pin description 3
7	Functional description 4
8	Limiting values 4
9	Recommended operating conditions 4
10	Static characteristics 5
11	Dynamic characteristics 8
12	Waveforms 10
13	Package outline 12
14	Abbreviations
15	Revision history 18
16	Legal information
16.1	Data sheet status 19
16.2	Definitions 19
16.3	Disclaimers
16.4	Trademarks 20
17	Contact information 20
18	Contents 21



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.