

NDTL03N150C

Power MOSFET 1500V, 10.5Ω, 2.5A, N-Channel



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Features

- Low On-Resistance
- Ultra High Voltage
- High Speed Switching
- 100% Avalanche Tested
- Pb-Free and RoHS compliance

Typical Applications

- Switch mode power supply
- AC Drive

SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS at Ta = 25°C (Note 1, 2, 3, 4)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	1500	V
Gate to Source Voltage	V _{GSS}	±30	V
Drain Current (DC)	I _D	2.5	A
Drain Current (Pulse) PW≤10μs, duty cycle≤1%	I _{DP}	5	A
Power Dissipation	P _D	2.5	W
		Tc=25°C 140	
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Avalanche Energy (Single Pulse) (Note 2)	E _{AS}	34	mJ
Avalanche Current (Note 3)	I _{AV}	2.5	A

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note 2 : V_{DD}=50V, L=10mH, I_{AV}=2.5A (Fig.1)

Note 3 : L≤10mH, Single Pulse

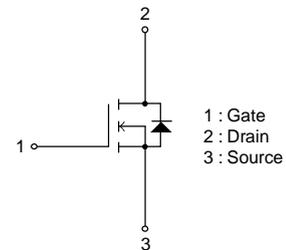
THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State	R _{θJC}	0.89	°C/W
Junction to Ambient (Note 4)	R _{θJA}	50.0	

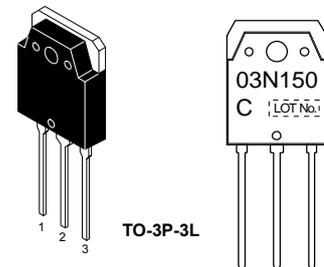
Note 4 : Insertion mounted

V _{DSS}	R _{DS(on)} Max	I _D Max
1500V	10.5Ω@10V	2.5A

ELECTRICAL CONNECTION N-Channel



MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

NDTL03N150C

ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 5, 6, 7)

Parameter	Symbol	Conditions	Value			Unit	
			min	typ	max		
Drain to Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	1500			V	
Zero-Gate Voltage Drain Current	IDSS	VDS=1200V, VGS=0V			1	mA	
Gate to Source Leakage Current	IGSS	VGS=30V, VDS=0V			±100	nA	
Gate Threshold Voltage	VGS(th)	VDS=10V, ID=1mA	2		4	V	
Forward Transconductance	gFS	VDS=20V, ID=1.25A		1.9		S	
Static Drain to Source On-State Resistance	RDS(on)	ID=1.25A, VGS=10V		8	10.5	Ω	
Input Capacitance	Ciss	VDS=30V, f=1MHz		650		pF	
Output Capacitance	Coss				70		pF
Reverse Transfer Capacitance	Crss				20		pF
Turn-ON Delay Time	t _{d(on)}	See Fig.2		15		ns	
Rise Time	t _r			24		ns	
Turn-OFF Delay Time	t _{d(off)}			140		ns	
Fall Time	t _f			47		ns	
Total Gate Charge	Qg	VDS=200V, VGS=10V, ID=2.5A		34		nC	
Gate to Source Charge	Qgs			4.7		nC	
Gate to Drain "Miller" Charge	Qgd			15		nC	
Forward Diode Voltage	VSD	IS=2.5A, VGS=0V		0.8	1.5	V	
Reverse Recovery Time	trr	See Fig.3		350		ns	
Reverse Recovery Charge	Qrr	IS=2.5A, VGS=0V, di/dt=100A/μs		2220		nC	
Effective Output Capacitance, Energy Related (Note 6)	Co(er)	VGS=0V, VDS=0 to 1200V		18.3		pF	
Effective Output Capacitance, Time Related (Note 7)	Co(tr)				29.6		pF

Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted.

Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Note 6 : Co(er) is a fixed capacitance that gives the same stored energy as Coss while VDS is rising from 0 to 80% V(BR)DSS.

Note 7 : Co(tr) is a fixed capacitance that gives the same charging time as Coss while VDS is rising from 0 to 80% V(BR)DSS.

Fig. 1 Unclamped Inductive Switching Test Circuit

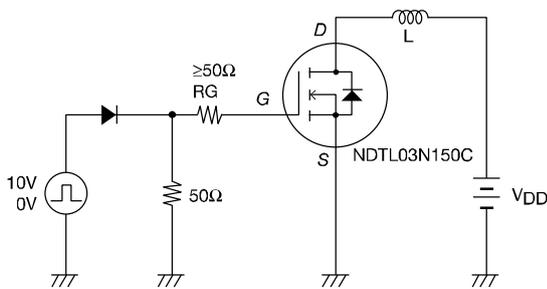


Fig. 2 Switching Time Test Circuit

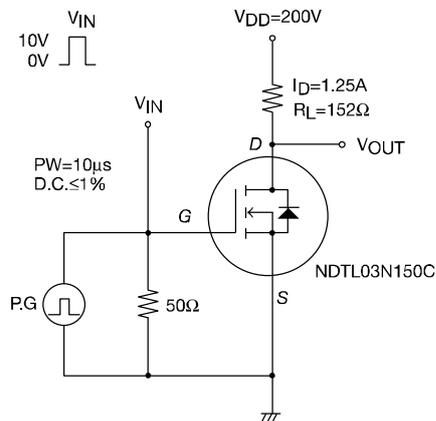
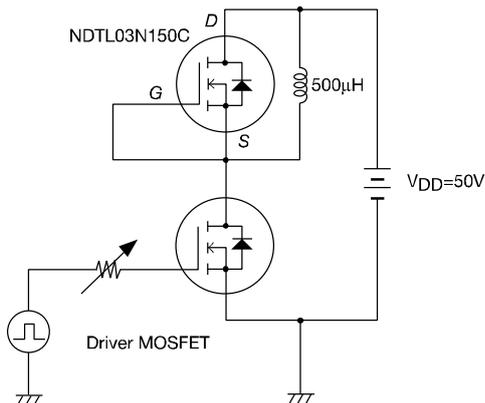
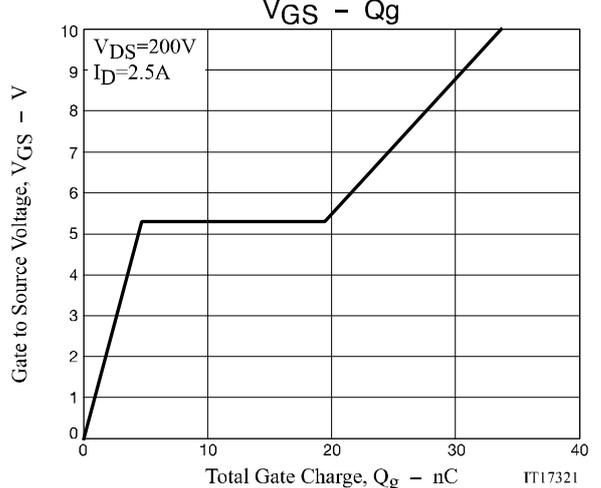
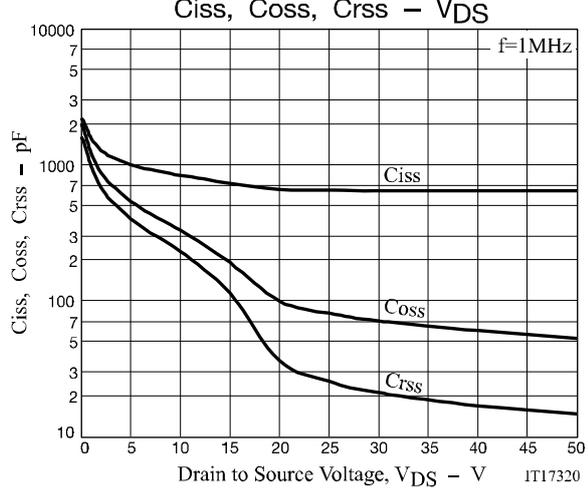
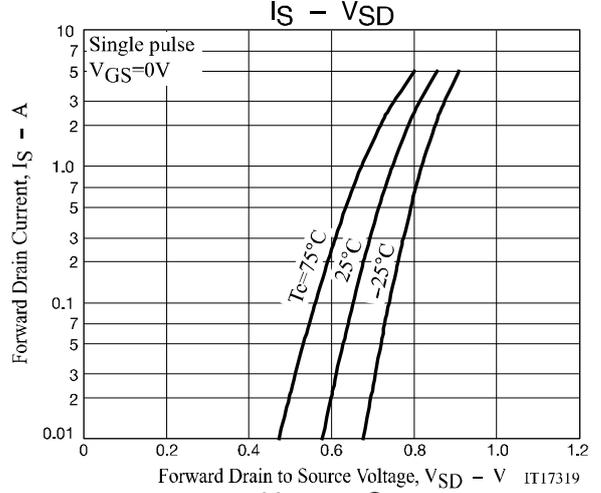
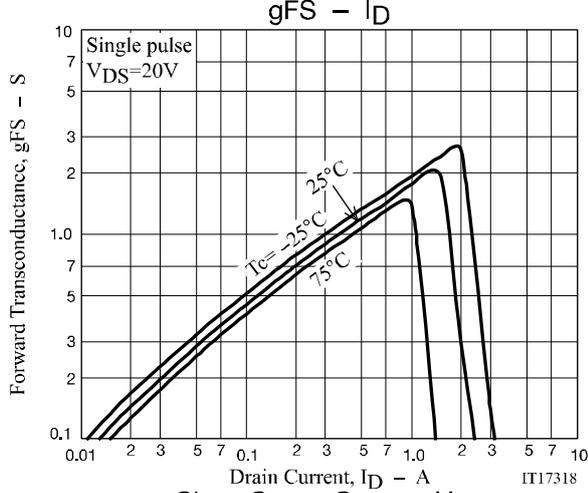
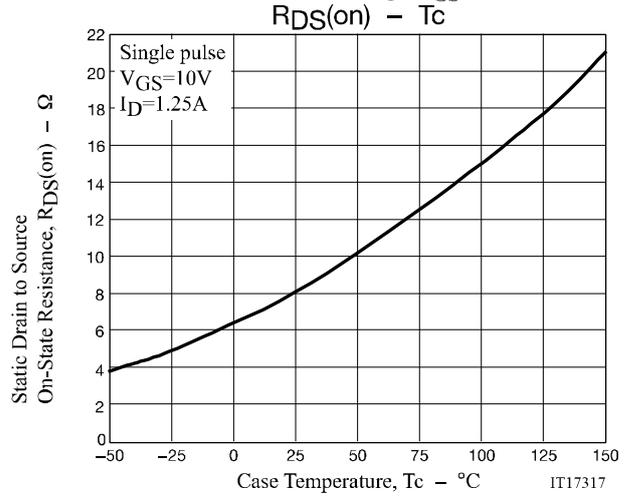
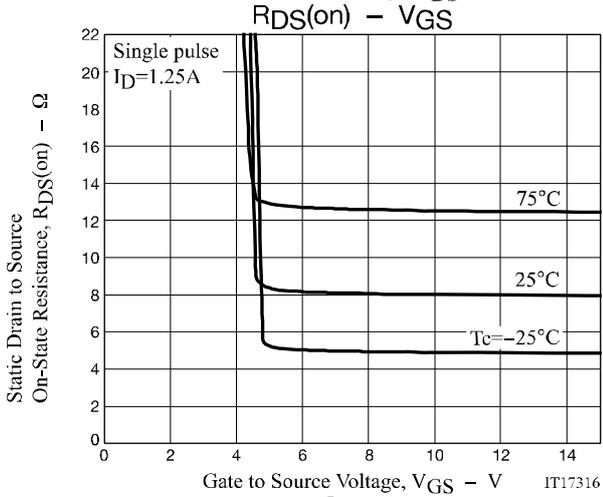
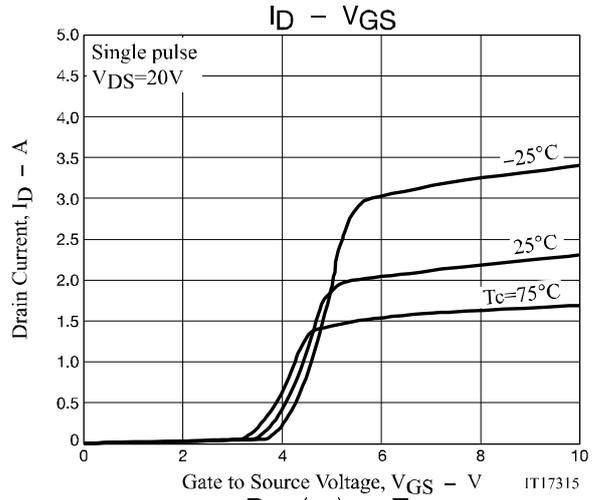
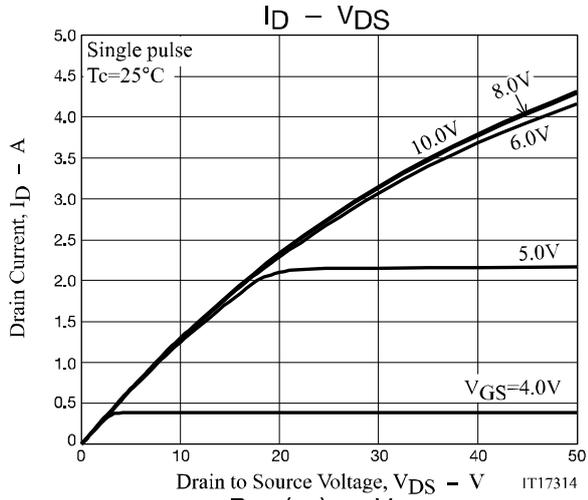


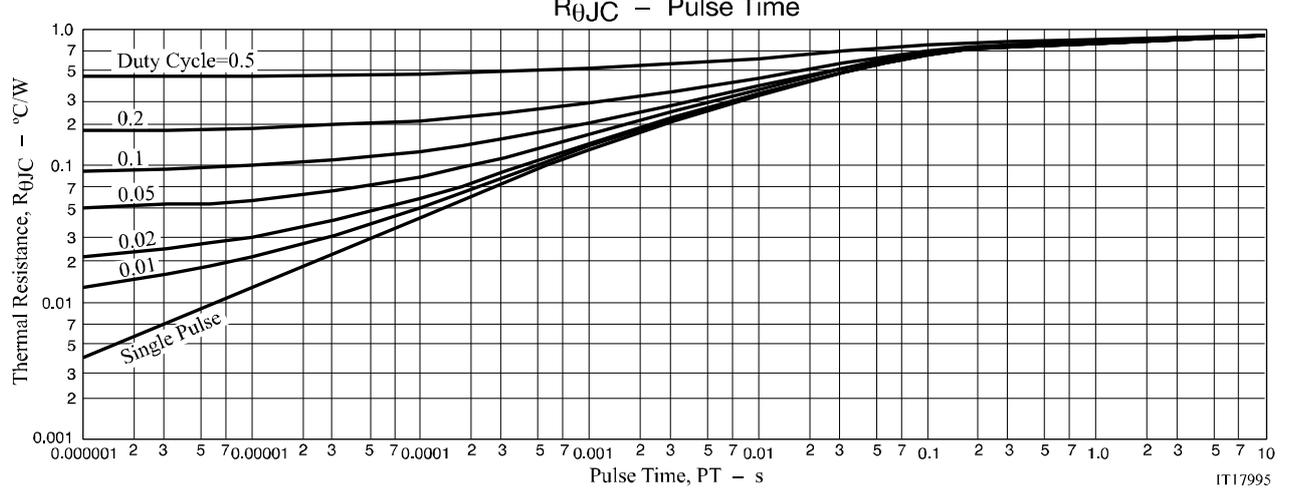
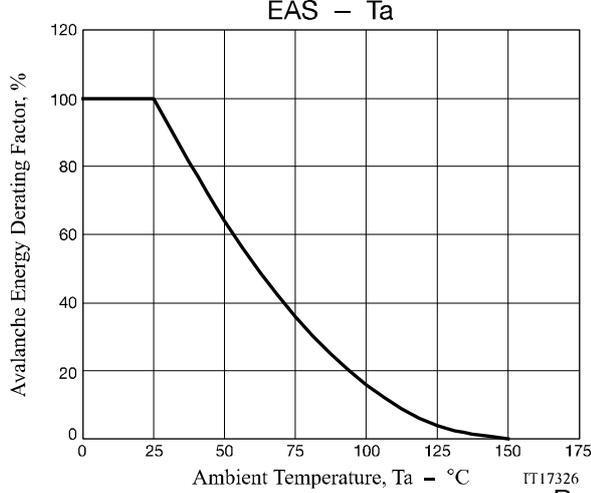
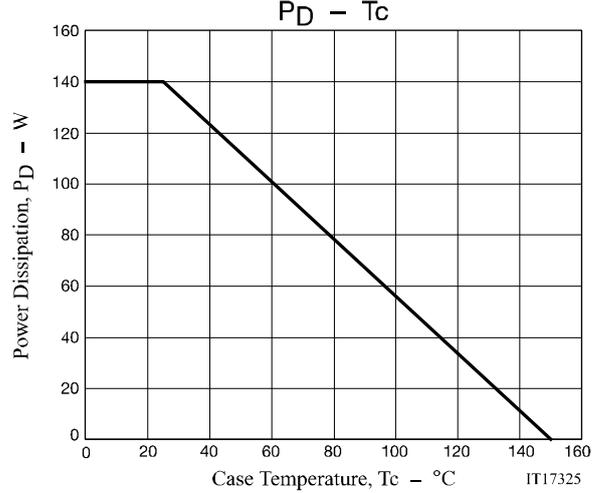
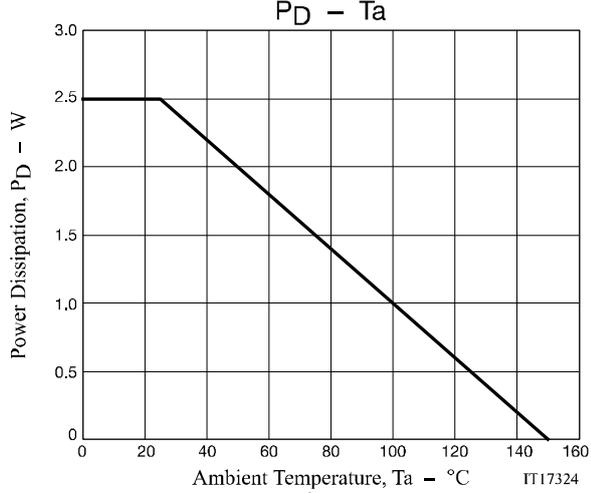
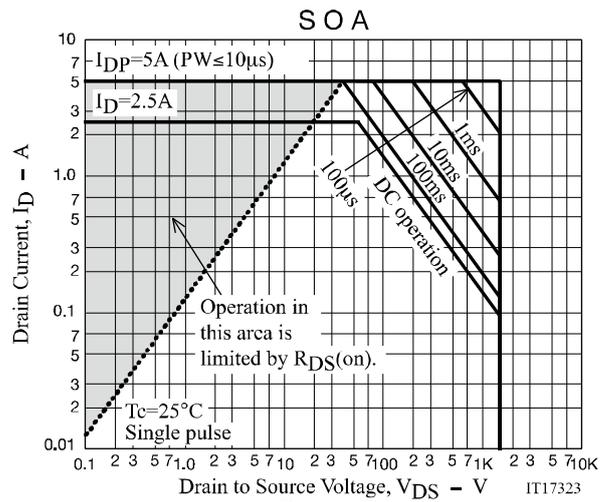
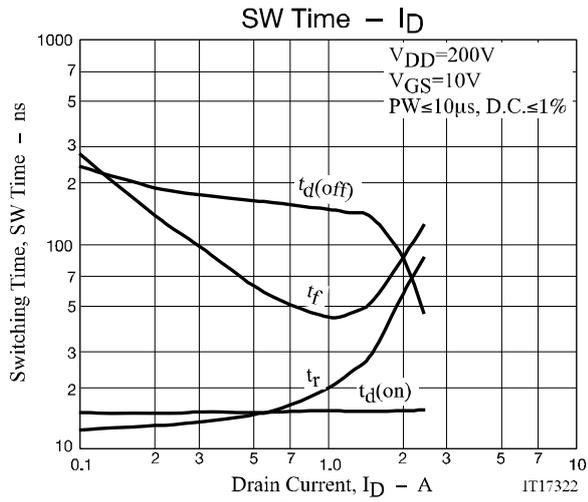
Fig. 3 Reverse Recovery Time Test Circuit



NDTL03N150C



NDTL03N150C

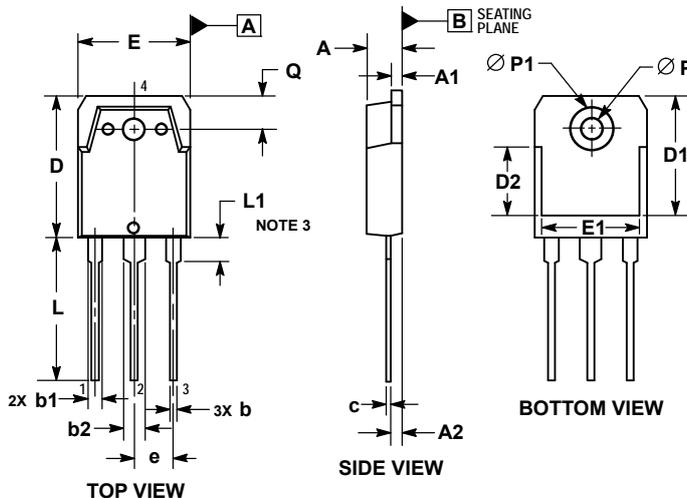


NDTL03N150C

PACKAGE DIMENSIONS

unit : mm

TO-3P-3L
CASE 340AF
ISSUE A

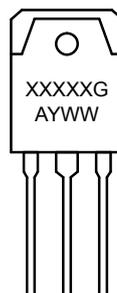


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS
 3. CONTOUR UNCONTROLLED IN THIS AREA.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS, MOLD FLASH OR GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
 5. DIMENSIONS b1 AND b2 DO NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL NOT EXCEED 0.10.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.60	4.80	5.00
A1	1.45	1.50	1.65
A2	1.20	1.40	1.60
b	0.80	1.00	1.20
b1	1.80	2.00	2.20
b2	2.80	3.00	3.20
c	0.55	0.60	0.75
D	19.70	19.90	20.10
D1	16.56	16.76	16.96
D2	9.80	10.00	10.20
E	15.40	15.60	15.80
E1	13.40	13.60	13.80
e	5.15	5.45	5.75
L	19.80	20.00	20.20
L1	3.30	3.50	3.70
P	3.00	3.20	3.40
P1	6.80	7.00	7.20
Q	4.80	5.00	5.20

- 1 : Gate
- 2 : Drain
- 3 : Source

GENERIC MARKING DIAGRAM*



- XXXXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
NDTL03N150CG	03N150C	TO-3P-3L (Pb-Free)	30 / Tube

Note on usage : Since the NDTL03N150C is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



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Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.