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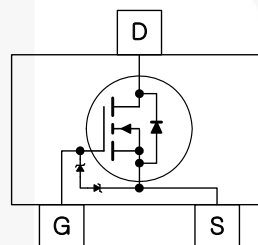
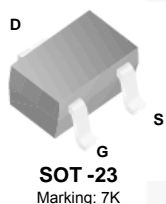
September 2014

## 2N7002K

### N-Channel Enhancement Mode Field Effect Transistor

#### Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage
- Ultra-Small Surface Mount Package
- Pb Free / RoHS Compliant
- ESD HBM = 2000 V (Typical: 3000 V) as per JESD22 A114 and ESD CDM = 2000 V as per JESD22 C101



#### Ordering Information

Part Number	Top Mark	Package	Packing Method
2N7002K	7K	SOT-23 3L	Tape and Reel

#### Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter		Value	Unit
$V_{DS}$	Drain-Source Voltage		60	V
$V_{DGR}$	Drain-Gate Voltage ( $R_{GS} \leq 1.0 \text{ M}\Omega$ )		60	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Drain Current	Continuous	300	mA
		Pulsed	800	
$T_J$	Operating Junction Temperature Range		-55 to +150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range		-55 to +150	$^\circ\text{C}$

## Thermal Characteristics

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
$P_D$	Total Power Dissipation	350	mW
	Derate Above $T_A = 25^\circ\text{C}$	2.8	mW/ $^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	350	$^\circ\text{C/W}$

### Note:

1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. Minimum land pad size.

## Electrical Characteristics

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Characteristics <sup>(2)</sup>					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 μA	60		V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V		1.0	μA
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C		500	
I <sub>GSS</sub>	Gate-Body Leakage	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V		±10	μA
On Characteristics <sup>(2)</sup>					
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.0	2.5	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A		2	Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 200 mA		4	
I <sub>D(ON)</sub>	On-State Drain Current	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 7.5 V	1.5		A
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.2 A	200		mS
Dynamic Characteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		50	pF
C <sub>oss</sub>	Output Capacitance			15	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			6	pF
Switching Characteristics					
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 30 V, I <sub>DSS</sub> = 200 mA, R <sub>G</sub> = 10 Ω, V <sub>GS</sub> = 10 V		5	ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			30	ns

### Note:

2. Short duration test pulse used to minimize self-heating effect.

## Typical Performance Characteristics

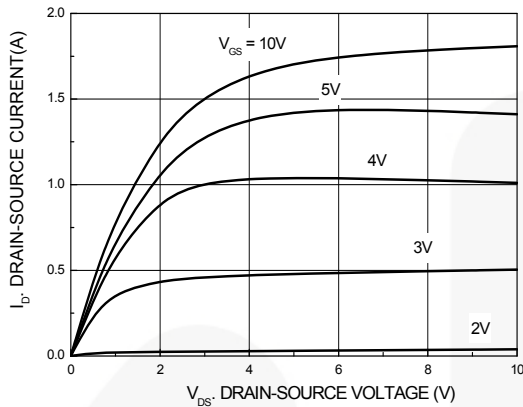


Figure 1. On-Region Characteristics

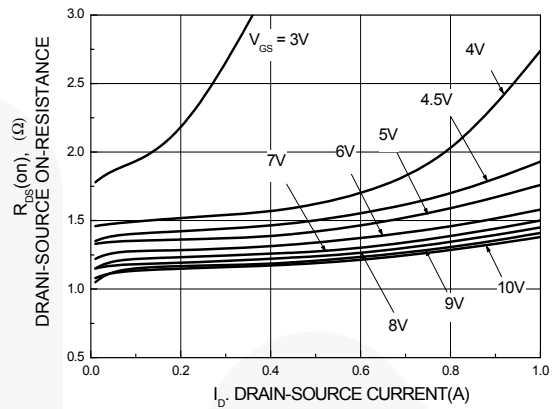


Figure 2. On-Resistance Variation with Gate Voltage and Drain Current

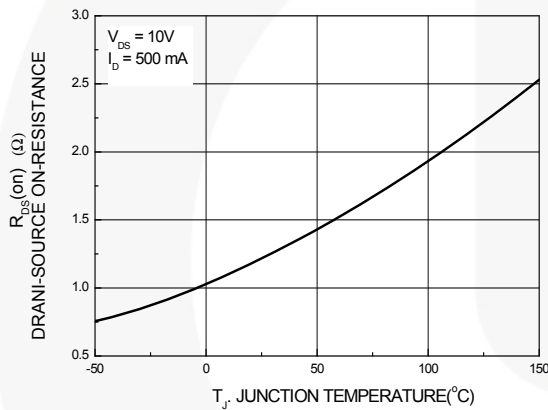


Figure 3. On-Resistance Variation with Temperature

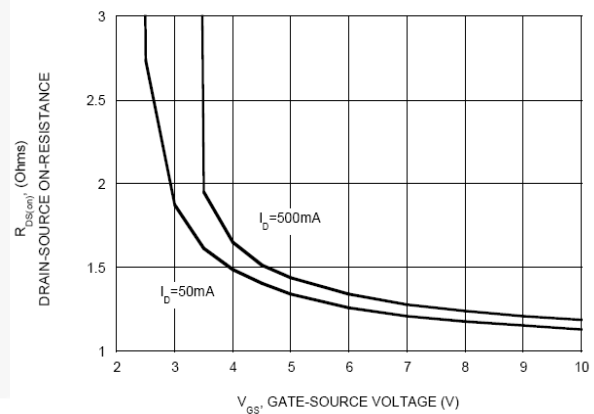


Figure 4. On-Resistance Variation with Gate-Source Voltage

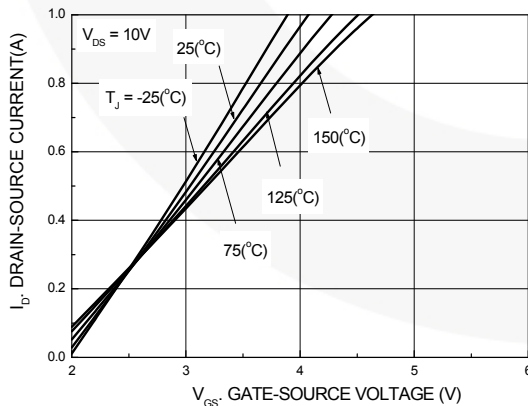


Figure 5. Transfer Characteristics

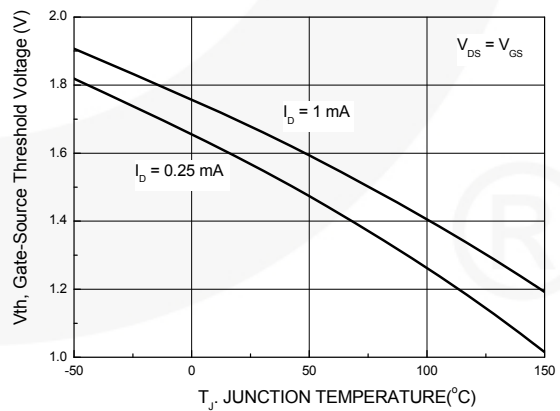


Figure 6. Gate Threshold Variation with Temperature

## Typical Performance Characteristics (Continued)

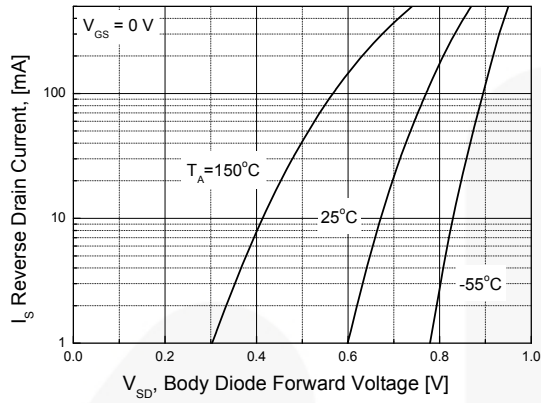


Figure 7. Reverse Drain Current Variation with Diode Forward Voltage and Temperature

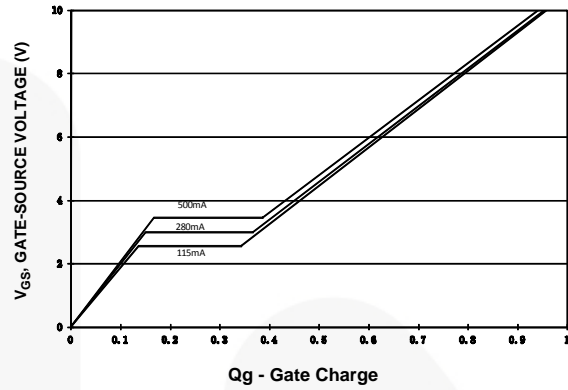


Figure 8. Gate Charge Characteristics

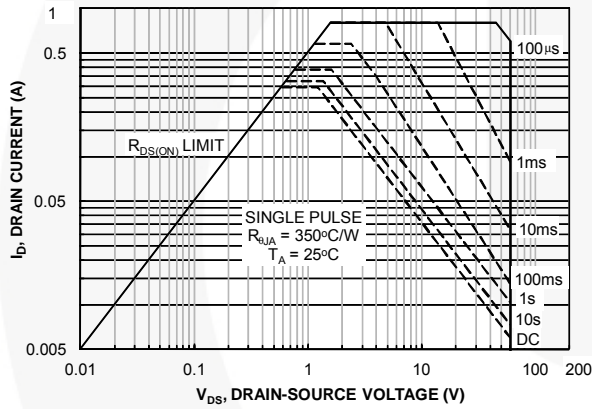


Figure 9. Maximum Safe Operating Area

# Physical Dimensions

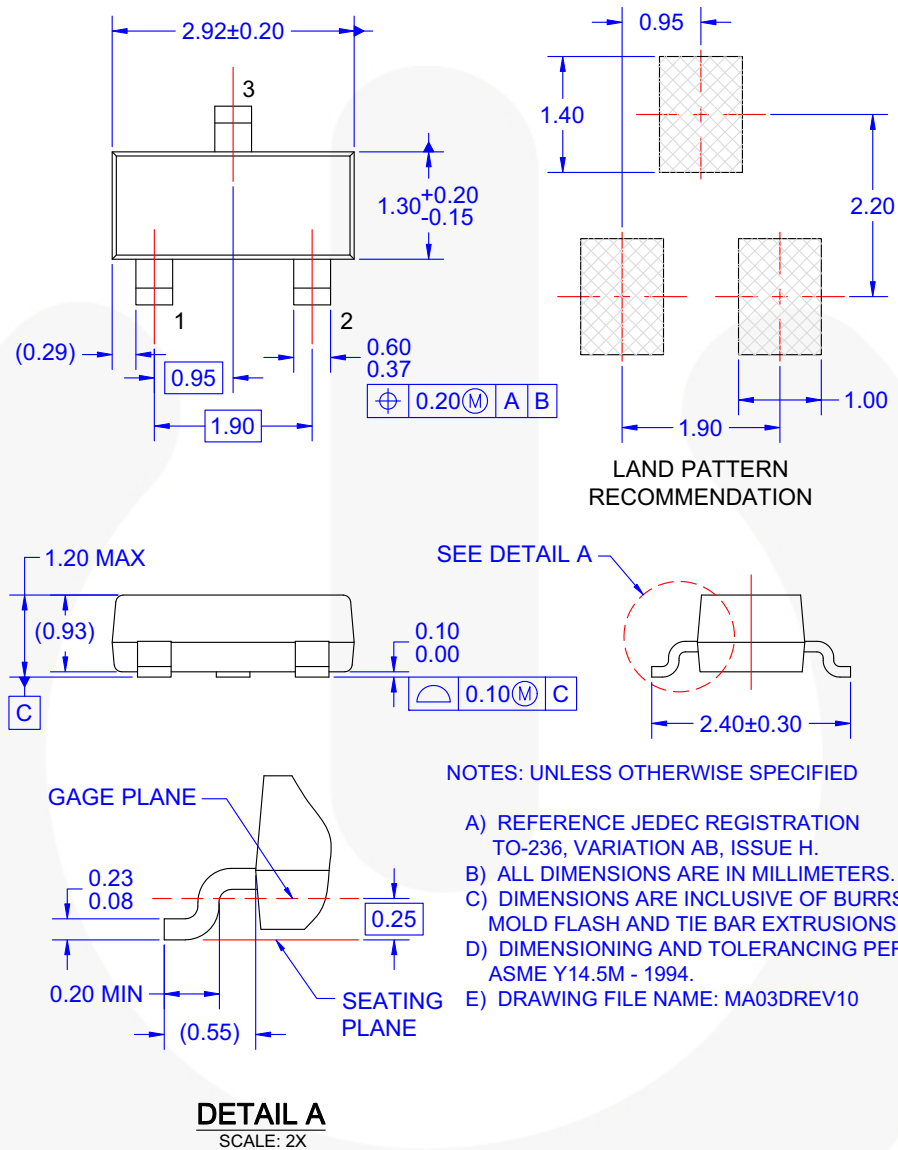


Figure 10. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE



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